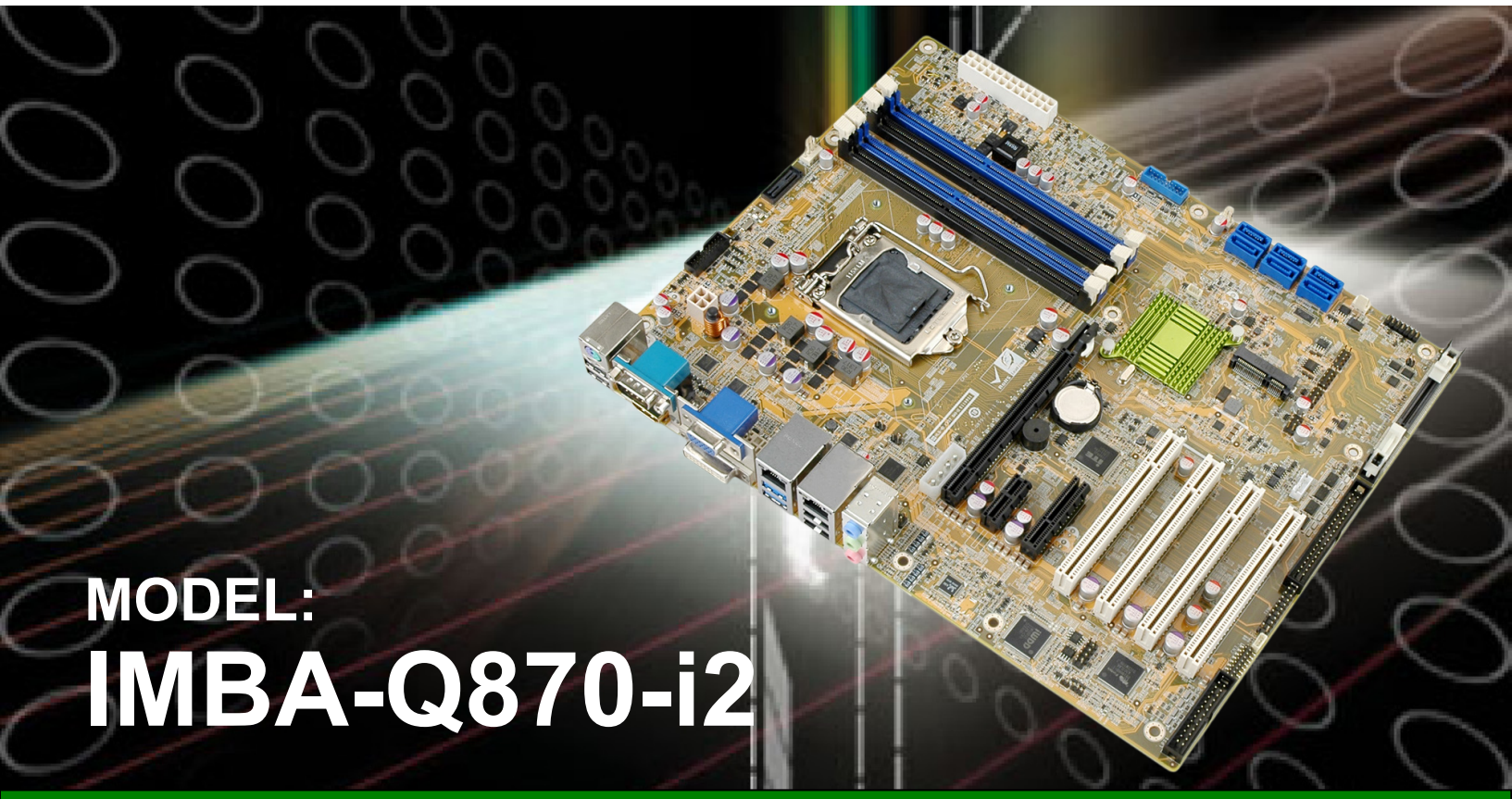
IEI Integration Corp.



MODEL:

IMBA-Q870-i2

ATX Motherboard with LGA1150 Intel® Core™ i7/i5/i3, Pentium® or Celeron® CPU, Intel® Q87 Chipset, Dual GbE, DDR3, DVI, HDMI, DisplayPort, VGA, USB 3.1 Gen 1, COM Ports, Six SATA 6Gb/s Ports, IPMI 2.0 and RoHS

User Manual

Rev. 1.04 – March 25, 2019

RoHS Lead Free

Revision

Date	Version	Changes
March 25, 2019	1.04	Enhanced image quality of dimension drawing (Figure 1-3) Updated Chapter 2: Packing List Updated Chapter 6: Software Drivers
December 13, 2016	1.03	Modified the max. supported resolution of the DVI-D connector (Table 1-1)
September 6, 2016	1.02	Modified the system fan connector 2 (SYS_FAN2) pinouts (Table 3-13)
March 13, 2014	1.01	Deleted I^2C information
January 8, 2014	1.00	Initial release

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Manual Conventions

WARNING

Warnings appear where overlooked details may cause damage to the equipment or result in personal injury. Warnings should be taken seriously.

CAUTION

Cautionary messages should be heeded to help reduce the chance of losing data or damaging the product.

NOTE

These messages inform the reader of essential but non-critical information. These messages should be read carefully as any directions or instructions contained therein can help avoid making mistakes.

Table of Contents

1 INTRODUCTION 1

1.1 INTRODUCTION 2
1.2 BENEFITS 2
1.3 FEATURES 3
1.4 CONNECTORS 4
1.5 DIMENSIONS 5
1.6 DATA FLOW 6
1.7 TECHNICAL SPECIFICATIONS 7

2 PACKING LIST 10

2.1 ANTI-STATIC PRECAUTIONS 11
2.2 UNPACKING PRECAUTIONS 11
2.3 PACKING LIST 12
2.4 OPTIONAL ITEMS 13

3 CONNECTORS 15

3.1 PERIPHERAL INTERFACE CONNECTORS 16
3.1.1 IMBA-Q870-i2 Layout 16
3.1.2 Peripheral Interface Connectors 17
3.1.3 External Interface Panel Connectors 18
3.2 INTERNAL PERIPHERAL CONNECTORS 19
3.2.1 Additional Power Connector 19
3.2.2 ATX Power Connector 20
3.2.3 Battery Connector 21
3.2.4 Chassis Intrusion Connector 22
3.2.5 CPU Power Connector 23
3.2.6 DisplayPort Connector 24
3.2.7 Digital I/O Connector 25
3.2.8 EC Debug Connector 26
3.2.9 Fan Connector (CPU) 27
3.2.10 Fan Connectors (System) 28

3.2.11 Front Panel Audio Connector 30
3.2.12 Front Panel Connector 31
3.2.13 iRIS Module Slot 32
3.2.14 Keyboard and Mouse Connector 33
3.2.15 LAN LED Connectors 34
3.2.16 Memory Card Slots 35
3.2.17 mSATA Card Slot 35
3.2.18 Parallel Port Connector 37
3.2.19 Power Button 38
3.2.20 SATA 6Gb/s Drive Connectors 39
3.2.21 Serial Port Connectors, RS-232 40
3.2.22 Serial Port Connector, RS-422/485 42
3.2.23 SMBus Connector 43
3.2.24 SPI Flash Connector 44
3.2.25 SPI Flash Connector, EC 44
3.2.26 TPM Connector 45
3.2.27 USB 2.0 Connectors 46
3.2.28 USB 3.1 Gen 1 Connector 47
3.3 External Peripheral Interface Connector Panel 48
3.3.1 Audio Connector 49
3.3.2 Keyboard/Mouse and USB 2.0 Connectors 50
3.3.3 Ethernet and USB 2.0 Connectors 50
3.3.4 Ethernet and USB 3.1 Gen 1 Connectors 51
3.3.5 HDMI Port Connector 52
3.3.6 Serial Port Connector (COM1) 53
3.3.7 VGA and DVI Connectors 54

4 INSTALLATION 56

4.1 Anti-static Precautions 57
4.2 Installation Considerations 57
4.2.1 Socket LGA1150 CPU Installation 59
4.2.2 Socket LGA1150 Cooling Kit Installation 62
4.2.3 DIMM Installation 64
4.2.4 iRIS-2400 Module Installation 65
4.2.5 mSATA Card Installation 66

4.3 SYSTEM CONFIGURATION 67
4.3.1 AT/ATX Power Mode Setting *68*
4.3.2 Clear CMOS Button *68*
4.3.3 Flash Descriptor Security Override *69*
4.3.4 mSATA Slot Setup *69*
4.3.5 PCIe x16 Interface Setup *70*
4.3.6 USB Power Select *70*
4.4 INTERNAL PERIPHERAL DEVICE CONNECTIONS 71
4.4.1 SATA Drive Connection *71*
4.5 INTEL® AMT SETUP PROCEDURE 73
4.6 IPMI SETUP PROCEDURE 74
4.6.1 Managed System Hardware Setup *74*
4.6.2 Using the IEI iMAN Web GUI *75*

5 BIOS 77

5.1 INTRODUCTION 78
5.1.1 Starting Setup *78*
5.1.2 Using Setup *78*
5.1.3 Getting Help *79*
5.1.4 Unable to Reboot after Configuration Changes *79*
5.1.5 BIOS Menu Bar *79*
5.2 MAIN 80
5.3 ADVANCED 82
5.3.1 ACPI Settings *83*
5.3.2 RTC Wake Settings *84*
5.3.3 Trusted Computing *85*
5.3.4 CPU Information *86*
5.3.5 SATA Configuration *89*
5.3.6 Intel(R) Rapid Start Technology *90*
5.3.7 AMT Configuration *91*
5.3.8 USB Configuration *92*
5.3.9 F81866 Super IO Configuration *93*
5.3.9.1 Serial Port n Configuration 94
5.3.9.2 Parallel Port Configuration 100
5.3.10 iWDD H/W Monitor *101*

5.3.10.1 Smart Fan Mode Configuration 103
5.3.11 Serial Port Console Redirection *104*
5.3.12 iEi Feature *107*
5.4 CHIPSET 108
5.4.1 PCH-IO Configuration *109*
5.4.1.1 PCI Express Configuration 111
5.4.1.2 PCH Azalia Configuration 112
5.4.2 System Agent (SA) Configuration *112*
5.4.2.1 Graphics Configuration 113
5.4.2.2 NB PCIe Configuration 116
5.4.2.3 Memory Configuration 117
5.5 BOOT 118
5.6 SECURITY 120
5.7 EXIT 120

6 SOFTWARE DRIVERS 122

6.1 AVAILABLE DRIVERS 123
6.2 DRIVER DOWNLOAD 123

A REGULATORY COMPLIANCE 125

B PRODUCT DISPOSAL 127

C BIOS OPTIONS 129

D DIGITAL I/O INTERFACE 133

E WATCHDOG TIMER 136

F HAZARDOUS MATERIALS DISCLOSURE 139

List of Figures

Figure 1-1: IMBA-Q870-i2 2
Figure 1-2: IMBA-Q870-i2 Connectors 4
Figure 1-3: IMBA-Q870-i2 Dimensions (mm) 5
Figure 1-4: Data Flow Diagram 6
Figure 3-1: Connectors and Jumpers 16
Figure 3-2: Additional Power Connector Location 19
Figure 3-3: ATX Power Connector Location 20
Figure 3-4: Battery Connector Location 22
Figure 3-5: Chassis Intrusion Connector Location 23
Figure 3-6: CPU Power Connector Location 24
Figure 3-7: DisplayPort Connector Location 25
Figure 3-8: Digital I/O Connector Location 26
Figure 3-9: EC Debug Connector Location 27
Figure 3-10: CPU Fan Connector Location 28
Figure 3-11: System Fan Connector Locations 29
Figure 3-12: Front Panel Audio Connector Location 30
Figure 3-13: Front Panel Connector Location 31
Figure 3-14: iRIS Module Slot Location 32
Figure 3-15: Keyboard and Mouse Location 33
Figure 3-16: LAN LED Connector Locations 34
Figure 3-17: Memory Card Slot Locations 35
Figure 3-18: mSATA Card Slot Location 36
Figure 3-19: Parallel Port Connector Location 38
Figure 3-20: Power Button Location 39
Figure 3-21: SATA 6Gb/s Drive Connector Locations 39
Figure 3-22: Serial Port Connector Location 41
Figure 3-23: RS-422/485 Connector Location 42
Figure 3-24: SMBus Connector Location 43
Figure 3-25: SPI Flash Connector Location 44
Figure 3-26: SPI EC Flash Connector Location 45

Figure 3-27: TPM Connector Location 46
Figure 3-28: USB 2.0 Connector Locations 47
Figure 3-29: USB 3.1 Gen 1 Connector Location 48
Figure 3-30: External Peripheral Interface Connector 49
Figure 3-31: Audio Connector 49
Figure 3-32: Serial Port Connector Pinouts 53
Figure 3-33: VGA Connector 54
Figure 3-34: DVI-I Connector 55
Figure 4-1: Disengage the CPU Socket Load Lever 59
Figure 4-2: Remove Protective Cover 60
Figure 4-3: Insert the Socket LGA1150 CPU 61
Figure 4-4: Close the Socket LGA1150 61
Figure 4-5: Cooling Kits (CF-115XA-R10 and CF-115XE-R10) 62
Figure 4-6: Cooling Kit Support Bracket 63
Figure 4-7: DIMM Installation 64
Figure 4-8: iRIS-2400 Module Installation 65
Figure 4-9: Remove the Retention Screws for the mSATA Card 66
Figure 4-10: Insert the mSATA Card into the Socket at an Angle 67
Figure 4-11: Secure the mSATA Card 67
Figure 4-12: AT/ATX Power Mode Switch Location 68
Figure 4-13: Clear CMOS Button Location 68
Figure 4-14: Flash Descriptor Security Override Jumper Location 69
Figure 4-15: mSATA Slot Setup Jumper Location 70
Figure 4-16: SATA Drive Cable Connection 72
Figure 4-17: SATA Power Drive Connection 73
Figure 4-18: IEI iMAN Web Address 75
Figure 4-19: IEI iMAN Web GUI 76
Figure 6-1: IEI Resource Download Center 123

List of Tables

Table 1-1: IMBA-Q870-i2 Specifications 9
Table 2-1: Packing List 12
Table 2-2: Optional Items 14
Table 3-1: Peripheral Interface Connectors 18
Table 3-2: Rear Panel Connectors 19
Table 3-3: Additional Power Connector Pinouts 20
Table 3-4: ATX Power Connector Pinouts 21
Table 3-5: Battery Connector Pinouts 22
Table 3-6: Chassis Intrusion Connector Pinouts 23
Table 3-7: CPU Power Connector Pinouts 24
Table 3-8: DisplayPort Connector Pinouts 25
Table 3-9: Digital I/O Connector Pinouts 26
Table 3-10: EC Debug Connector Pinouts 27
Table 3-11: CPU Fan Connector Pinouts 28
Table 3-12: System Fan Connector 1 Pinouts (SYS_FAN1) 29
Table 3-13: System Fan Connector 2 Pinouts (SYS_FAN2) 29
Table 3-14: Front Panel Audio Connector Pinouts 30
Table 3-15: Front Panel Connector Pinouts 31
Table 3-16: Keyboard and Mouse Connector Pinouts 33
Table 3-17: LAN1 LED Connector (LED_LAN1) Pinouts 34
Table 3-18: LAN2 LED Connector (LED_LAN2) Pinouts 34
Table 3-19: mSATA Card Slot Pinouts 37
Table 3-20: Parallel Port Connector Pinouts 38
Table 3-21: SATA 6Gb/s Drive Connector Pinouts 40
Table 3-22: COM3~6 Serial Port Connector Pinouts 41
Table 3-23: RS-422/485 Connector Pinouts 42
Table 3-24: DB-9 RS-422/485 Pinouts 43
Table 3-25: SMBus Connector Pinouts 43
Table 3-26: SPI Flash Connector Pinouts 44
Table 3-27: SPI EC Flash Connector Pinouts 45

Table 3-28: TPM Connector Pinouts 46
Table 3-29: USB 2.0 Connector Pinouts 47
Table 3-30: USB 3.1 Gen 1 Connector Pinouts 48
Table 3-31: USB 2.0 Port Pinouts 50
Table 3-32: PS/2 Connector Pinouts 50
Table 3-33: USB 2.0 Port Pinouts 51
Table 3-34: LAN2 Pinouts 51
Table 3-35: USB 3.1 Gen 1 Port Pinouts 52
Table 3-36: LAN1 Pinouts 52
Table 3-37: HDMI Connector Pinouts 53
Table 3-38: Serial Port Connector Pinouts 53
Table 3-39: VGA Connector Pinouts 54
Table 3-40: DVI Connector Pinouts 55
Table 4-1: Flash Descriptor Security Override Jumper Settings 69
Table 4-2: mSATA Slot Setup Jumper Settings 69
Table 4-3: PCIe x16 Interface Setup 70
Table 4-4: BIOS Options and Configured USB Ports 71
Table 5-1: BIOS Navigation Keys 79
Table 5-2: BIOS Options and Configured USB Ports 110

BIOS Menus

BIOS Menu 1: Main 81
BIOS Menu 2: Advanced 82
BIOS Menu 3: ACPI Configuration 83
BIOS Menu 4: RTC Wake Settings 84
BIOS Menu 5: Trusted Computing 85
BIOS Menu 6: CPU Information 86
BIOS Menu 7: SATA Configuration 89
BIOS Menu 8: Intel(R) Rapid Start Technology 90
BIOS Menu 9: AMT Configuration 91
BIOS Menu 10: USB Configuration 92
BIOS Menu 11: F81866 Super IO Configuration 93
BIOS Menu 12: Serial Port n Configuration Menu 94
BIOS Menu 13: Parallel Port Configuration Menu 100
BIOS Menu 14: iWDD H/W Monitor 102
BIOS Menu 15: Smart Fan Mode Configuration 103
BIOS Menu 16: Serial Port Console Redirection 105
BIOS Menu 17: iEi Feature 107
BIOS Menu 18: Chipset 108
BIOS Menu 19: PCH-IO Configuration 109
BIOS Menu 20: PCI Express Configuration 111
BIOS Menu 21: PCH Azalia Configuration 112
BIOS Menu 22: System Agent (SA) Configuration 113
BIOS Menu 23: Graphics Configuration 113
BIOS Menu 24: LCD Control 115
BIOS Menu 25: NB PCIe Configuration 116
BIOS Menu 26: Memory Configuration 117
BIOS Menu 27: Boot 118
BIOS Menu 28: Security 120
BIOS Menu 29:Exit 121

Chapter

1

Introduction

1.1 Introduction

Figure 1-1: IMBA-Q870-i2

The IMBA-Q870-i2 is an ATX motherboard. It accepts a Socket LGA1150 Intel® Core™ i7, Core™ i5, Core™ i3, Pentium® or Celeron® processor and supports four 240-pin 1333/1066 MHz dual-channel DDR3 DIMM modules up to 32.0 GB maximum. The IMBA-Q870-i2 includes VGA, HDMI, and DVI-D display ports for triple independent display. Expansion and I/O include four PCI slots, one PCIe x16 slot, one PCIe x4 slot with x1 signal, one PCIe x1 slot, two USB 3.1 Gen 1 ports on the rear panel, two USB 3.1 Gen 1 ports by pin header, four USB 2.0 on the rear panel, four USB 2.0 by pin header, six SATA 6Gb/s connectors, six COM ports, and two keyboard/mouse connectors.

1.2 Benefits

Some of the IMBA-Q870-i2 motherboard benefits include:

- Powerful graphics with multiple monitors
- Staying connected with both wired LAN connections
- Speedy running of multiple programs and applications

1.3 Features

Some of the IMBA-Q870-i2 motherboard features are listed below:

- ATX form factor
- RoHS compliant
- LGA1150 Intel® Core™ i7, Core™ i5, Core™ i3, Pentium® or Celeron® processor supported
- Intel® Q87 Chipset
- Four 240-pin 1333/1066 MHz dual-channel DDR3 DIMMs with up to 32.0 GB memory
- HDMI, DisplayPort, DVI-D and VGA interfaces support triple independent display
- Supports IPMI 2.0 via IEI iRIS-2400 module
- Two Intel® PCIe GbE connectors, LAN1 with Intel® AMT 9.0 support
- Six SATA 6Gb/s connectors support RAID 0, 1, 5, 10
- One PCIe Mini slot for mSATA modules or USB devices
- Four PCI card expansion slots
- One PCIe x16 card expansion slot
- One PCIe x4 card expansion slot with x1 signal
- One PCIe x1 card expansion slot
- Multiple USB 3.1 Gen 1 (5 Gb/s) and USB 2.0 ports
- High Definition Audio

1.4 Connectors

The connectors on the IMBA-Q870-i2 are shown in the figure below.

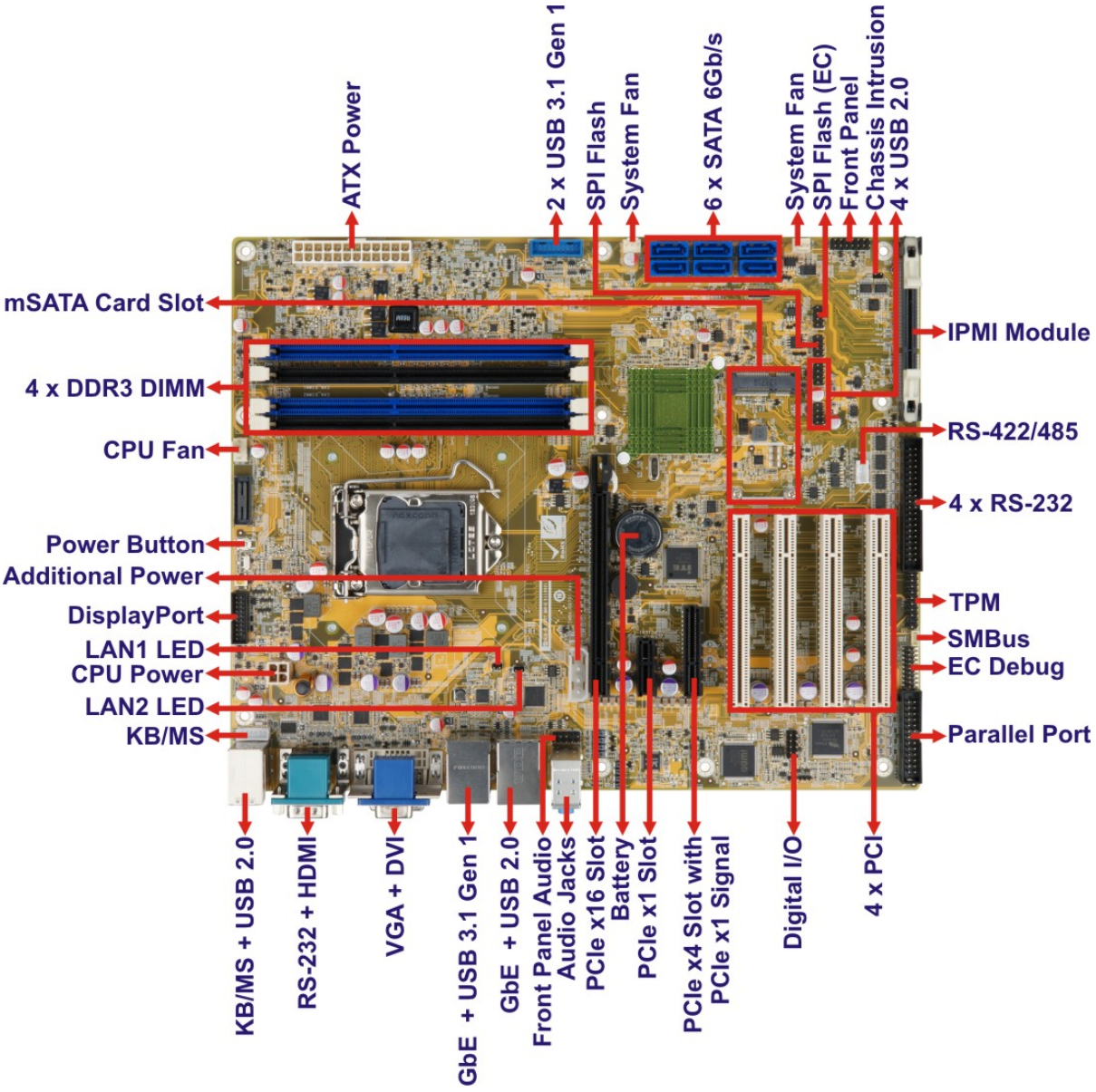


Figure 1-2: IMBA-Q870-i2 Connectors

1.5 Dimensions

The main dimensions of the IMBA-Q870-i2 are shown in the diagram below.

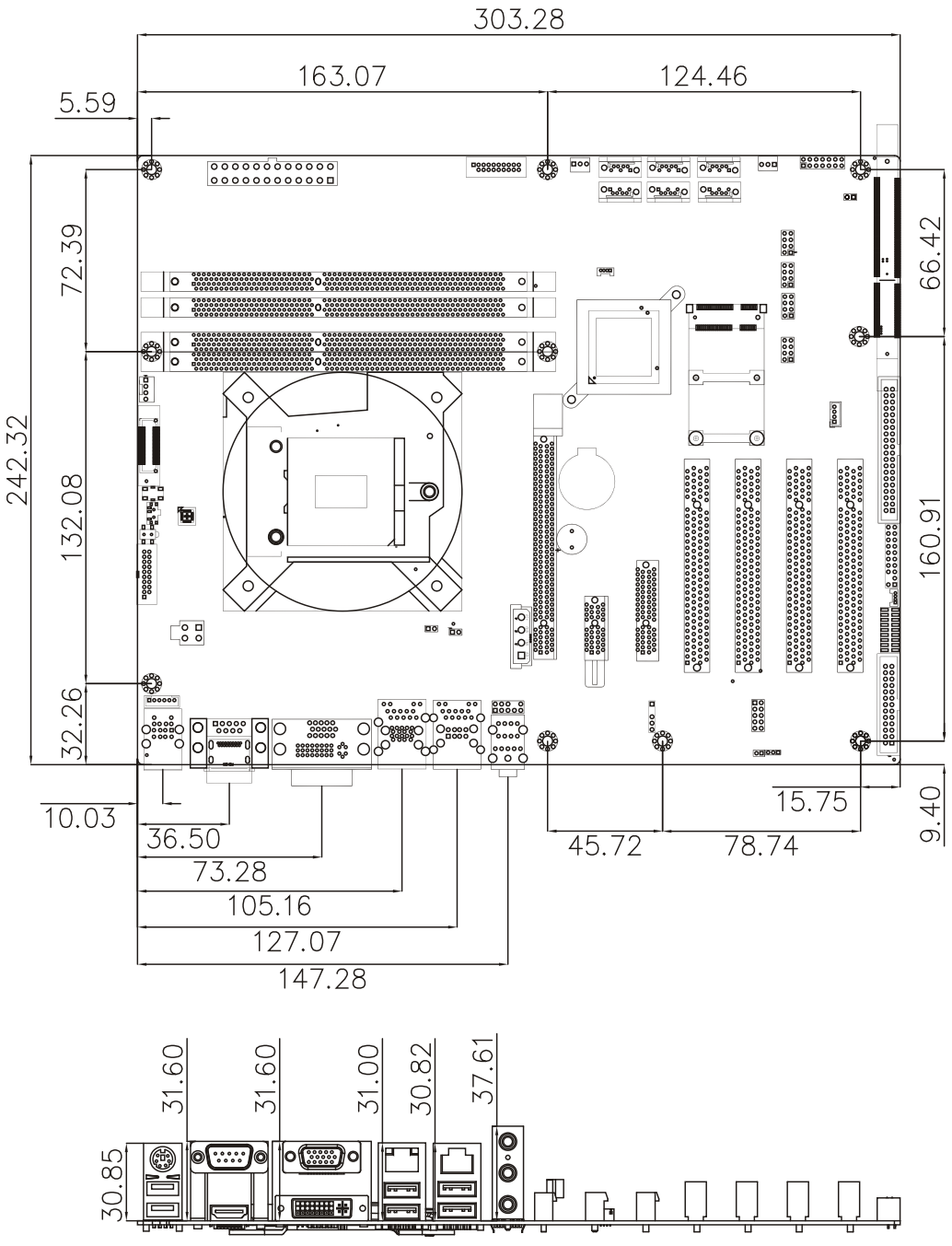


Figure 1-3: IMBA-Q870-i2 Dimensions (mm)

1.6 Data Flow

Figure 1-4 shows the data flow between the system chipset, the CPU and other components installed on the motherboard.

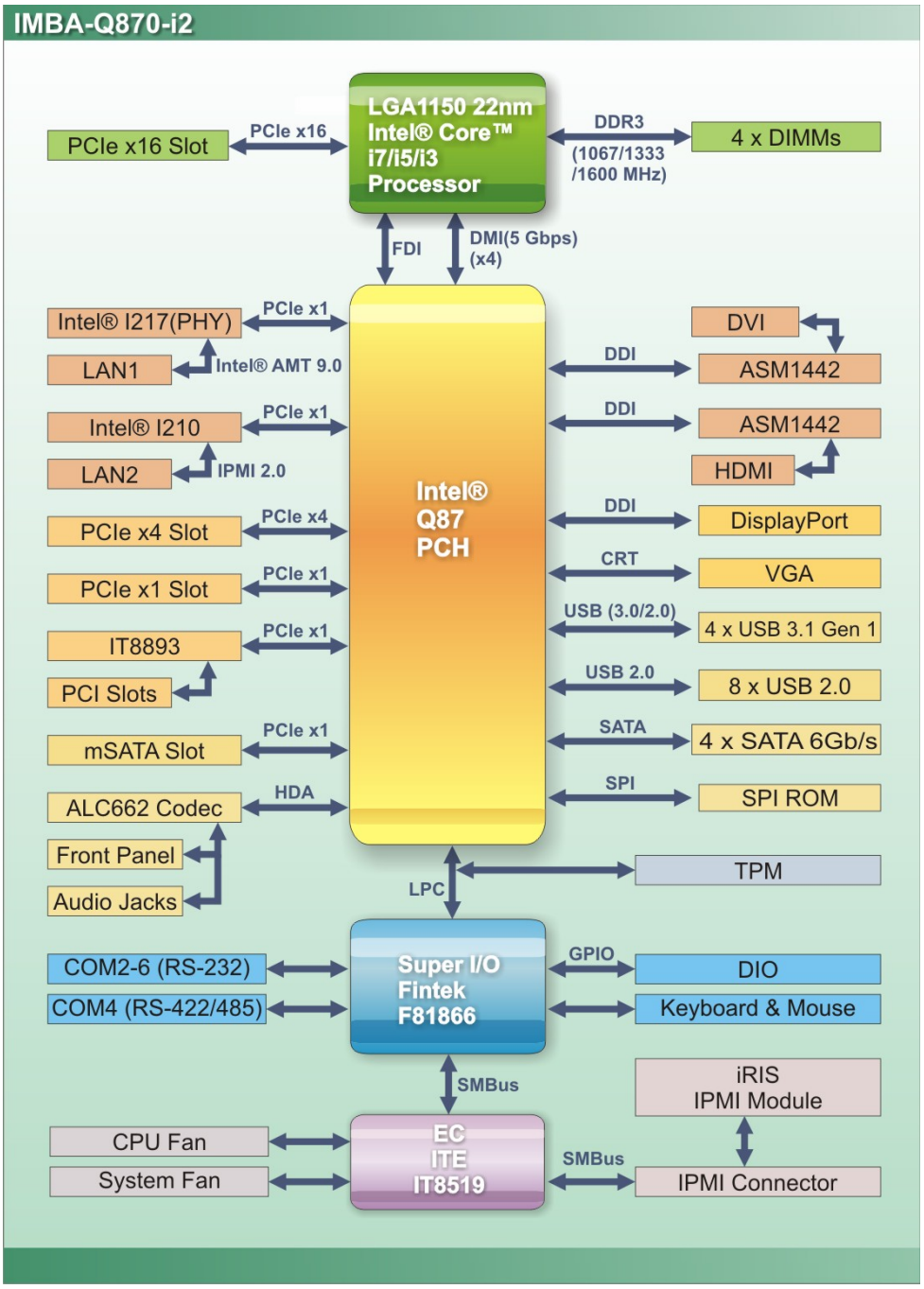


Figure 1-4: Data Flow Diagram

1.7 Technical Specifications

IMBA-Q870-i2 technical specifications are listed below.

Specification/Model	IMBA-Q870-i2
Form Factor	ATX
CPU Supported	LGA1150 Intel® Core™ i7, Core™ i5, Core™ i3, Pentium® or Celeron® processor supported
Chipset	Intel® Q87
Integrated Graphics	Intel® HD Graphics Gen 7.5 supports DirectX 11.1, OpenCL 1.2, OpenGL 3.2, Full MPEG2, VC1, AVC Decode
Memory	Four 240-pin 1333/1066 MHz dual-channel DDR3 SDRAM DIMMs support up to 32.0 GB maximum
Audio	Realtek ALC662 HD Audio codec (line-in, line-out, mic-in)
BIOS	UEFI BIOS
Digital I/O	8-bit, 4-bit input/4-bit output
Ethernet Controllers	**LAN1:** Intel® I217LM PHY with Intel® AMT 9.0 support **LAN2**: Intel® I210-AT PCIe Ethernet controller with NCSI & IPMI 2.0 support
Super I/O Controller	Fintek F81866
Watchdog Timer	Software programmable supports 1~255 sec. system reset
Expansion	
PCI	Four PCI slots
PCIe	One PCIe x1 slot One PCIe x16 slot One PCIe x4 slot (with x1 signal) One PCIe Mini slot for mSATA card or USB devices only
I/O Interface Connectors	

Specification/Model	IMBA-Q870-i2
Audio Connectors	One external audio jack (line-in, line-out, mic-in) One internal front panel audio connector (2x5 pin header)
Display Ports	One HDMI integrated in the Intel® Q87 (up to 2560x1600 @ 60Hz) One DVI-D integrated in the Intel® Q87 (up to 1920x1200 @ 60Hz) One VGA integrated in the Intel® Q87 (up to 1920x1200 @ 60Hz) One internal DisplayPort integrated in the Intel® Q87 supports HDMI, LVDS, VGA, DVI, DisplayPort (up to 3840x2160, 60Hz)
Ethernet	Two RJ-45 GbE ports
Keyboard/Mouse	One internal keyboard and mouse connector One PS/2 keyboard and mouse connector
TPM	One TPM connector via 20-pin header
Serial Ports	One external RS-232 serial port One RS-422/485 via internal wafer connector Four RS-232 via internal box headers
USB ports	Two external USB 3.1 Gen 1 (5 Gb/s) ports on rear IO Two internal USB 3.1 Gen 1 (5 Gb/s) ports by pin headers Four external USB 2.0 ports on rear IO Four internal USB 2.0 ports by pin headers
Serial ATA	Six SATA 6Gb/s connectors support RAID 0, 1, 5, 10
LAN LED	Two 2-pin LAN active LED connectors
Environmental and Power Specifications	
Power Supply	ATX power supported
Power Consumption	3.3V@0.66A, 5V@4.34A , 12V@0.16A, Vcore_12V@4.01A, 5VSB@0.21A (3.90GHz Intel® i7-4770K CPU with four 4GB 1333MHz DDR3 DIMMs)

Specification/Model	IMBA-Q870-i2
Operating Temperature	-20ºC ~ 60ºC/-4°F ~ 140°F
Humidity	5% ~ 95% (non-condensing)
Physical Specifications	
Dimensions	244 mm x 305 mm
Weight GW/NW	1200 g / 700 g

Table 1-1: IMBA-Q870-i2 Specifications

Chapter

2

Packing List

2.1 Anti-static Precautions

WARNING!

Static electricity can destroy certain electronics. Make sure to follow the ESD precautions to prevent damage to the product, and injury to the user.

Make sure to adhere to the following guidelines:

- ***Wear an anti-static wristband***: Wearing an anti-static wristband can prevent electrostatic discharge.
- ***Self-grounding***: Touch a grounded conductor every few minutes to discharge any excess static buildup.
- ***Use an anti-static pad***: When configuring any circuit board, place it on an anti-static mat.
- ***Only handle the edges of the PCB***: Don't touch the surface of the motherboard. Hold the motherboard by the edges when handling.

2.2 Unpacking Precautions

When the IMBA-Q870-i2 is unpacked, please do the following:

- Follow the antistatic guidelines above.
- Make sure the packing box is facing upwards when opening.
- Make sure all the packing list items are present.

2.3 Packing List

NOTE:

If any of the components listed in the checklist below are missing, do not proceed with the installation. Contact the IEI reseller or vendor the IMBA-Q870-i2 was purchased from or contact an IEI sales representative directly by sending an email to sales@ieiworld.com.

The IMBA-Q870-i2 is shipped with the following components:

Quantity	Item and Part Number	Image
1	IMBA-Q870-i2 single board computer	
2	SATA cable	
1	I/O shielding	
1	Quick Installation Guide	

Table 2-1: Packing List

2.4 Optional Items

The following are optional components which may be separately purchased:

Item and Part Number	Image
IPMI 2.0 adapter card with AST2400 BMC chip (**P/N**: iRIS-2400-R10)	
Dual-port USB cable with bracket (**P/N**: 19800-003100-300-RS)	
Dual-port USB 3.1 Gen 1 cable with bracket (**P/N**: 19800-010500-200-RS)	
SATA power cable (**P/N**: 32102-000100-200-RS)	
RS-422/485 cable, 200mm (**P/N**: 32205-003800-300-RS)	
Quad port RS-232 cable with bracket (400/400/400/400MM) (**P/N**: 32205-001203-200-RS)	
KB/MS cable (**P/N**: 19800-000075-RS)	
Parallel port cable (**P/N**:19800-000049-RS)	

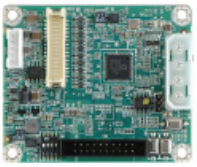
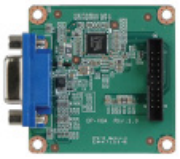
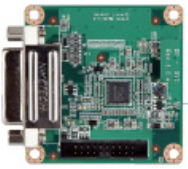
Item and Part Number	Image
High-performance LGA1155/1156 cooler kit (1U chassis compatible, 73W) (**P/N**: CF-115XA-R10)	
High-performance LGA1155/LGA1156 cooler kit (95W) (**P/N**: CF-115XE-R10)	
DisplayPort to HDMI converter board for IEI IDP connector (**P/N**: DP-HDMI-R10)	
DisplayPort to 24-bit dual-channel LVDS converter board for iEi IDP connector (**P/N**: DP-LVDS-R10)	
DisplayPort to VGA converter board for iEi IDP connector (**P/N**: DP-VGA-R10)	
DisplayPort to DVI-D converter board for iEi IDP connector (**P/N**: DP-DVI-R10)	
DisplayPort to DisplayPort converter board for iEi IDP connector (**P/N**: DP-DP-R10)	
20-pin Infineon TPM 1.2 module, software management tool, firmware v4.4 (**P/N**: TPM-IN01-R20)	

Table 2-2: Optional Items

Chapter

3

Connectors

3.1 Peripheral Interface Connectors

This chapter details all the jumpers and connectors.

3.1.1 IMBA-Q870-i2 Layout

The figures below show all the connectors and jumpers.

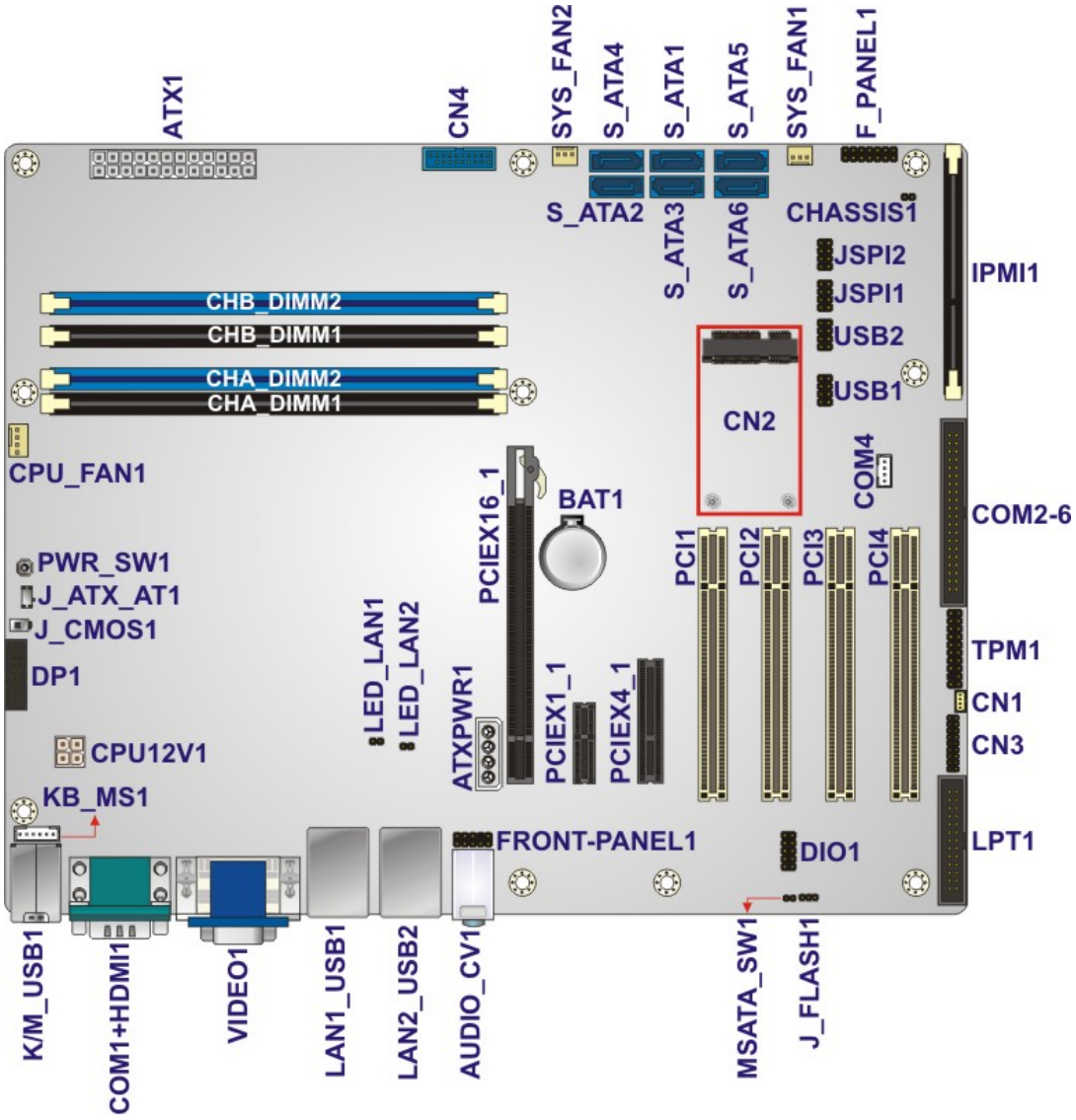


Figure 3-1: Connectors and Jumpers

3.1.2 Peripheral Interface Connectors

The table below lists all the connectors on the board.

Connector	Type	Label
Additional power connector	4-pin connector	ATXPWR1
ATX power connector	24-pin connector	ATX1
Battery connector	Battery holder	BAT1
Chassis intrusion connector	2-pin header	CHASSIS1
CPU power connector	4-pin connector	CPU12V1
Digital I/O connector	10-pin header	DIO1
DisplayPort connector	19-pin box header	DP1
EC debug connector	18-pin header	CN3
Fan connector (CPU)	4-pin wafer	CPU_FAN1
Fan connectors (system)	3-pin wafer	SYS_FAN1, SYS_FAN2
Front panel audio connector	10-pin header	FRONT-PANEL1
Front panel connector	14-pin header	F_PANEL1
iRIS module connector	204-pin SO-DIMM slot	IPMI1
Keyboard and mouse connector	6-pin wafer	KB_MS1
LAN1 LED connector	2-pin header	LED_LAN1
LAN2 LED connector	2-pin header	LED_LAN2
Memory card slot	DIMM slot	CHA_DIMM1, CHA_DIMM2, CHB_DIMM1, CHB_DIMM2
mSATA card slot	PCIe Mini socket	CN2
Parallel port connector	26-pin box header	LPT1
PCI slot	PCI slot	PCI1, PCI2, PCI3, PCI4

Connector	Type	Label
PCIe x1 slot	PCIe x1 slot	PCIEX1_1
PCIe x16 slot	PCIe x16 slot	PCIEX16_1
PCIe x4 slot with PCIe x1 signal	PCIe x4 slot	PCIEX4_1
Power button	Push button	PWR_SW1
SATA 6Gb/s drive connectors	7-pin SATA connector	S_ATA1, S_ATA2, S_ATA3, S_ATA 4, S_ATA5, S_ATA 6
Serial port, RS-232	40-pin box header	COM2-6
Serial port, RS-422/485	4-pin wafer	COM4
SMBus connector	4-pin wafer	CN1
SPI flash connector	8-pin header	JSPI1
SPI flash connector, EC	8-pin header	JSPI2
TPM connector	20-pin header	TPM1
USB 2.0 connectors	8-pin headers	USB1, USB2
USB 3.1 Gen 1 connector	19-pin box header	CN4

Table 3-1: Peripheral Interface Connectors

3.1.3 External Interface Panel Connectors

The table below lists the connectors on the external I/O panel.

Connector	Type	Label
Audio connector	Audio jacks	AUDIO_CV1
Keyboard/Mouse and USB 2.0 ports	PS/2, USB 2.0	K/M_USB1
Ethernet and USB 2.0 ports	RJ-45, USB 2.0	LAN2_USB2
Ethernet and USB 3.1 Gen 1 ports	RJ-45, USB 3.1 Gen 1	LAN1_USB1

Connector	Type	Label
HDMI connector	HDMI port	HDMI1
Serial Port connector (COM1)	9-pin male DB-9	COM1
VGA and DVI connector	15-pin female, 24-pin female	VIDEO1

Table 3-2: Rear Panel Connectors

3.2 Internal Peripheral Connectors

The section describes all of the connectors on the IMBA-Q870-i2.

3.2.1 Additional Power Connector

CN Label: **ATXPWR1**

CN Type: 4-pin connector, p=5.08 mm

CN Location: See **Figure 3-2**

CN Pinouts: See **Table 3-3**

The additional power connector provides extra +12V and +5V power to the system.

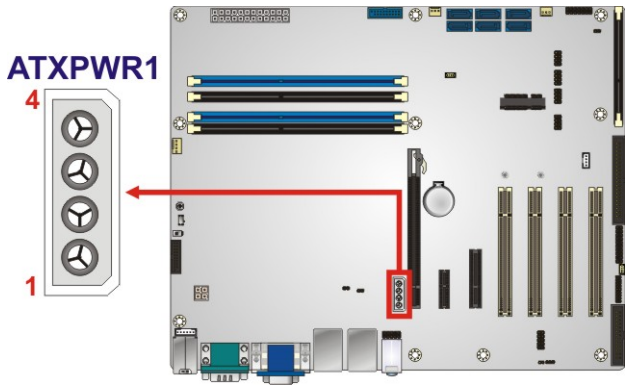


Figure 3-2: Additional Power Connector Location

Pin	Description
1	+12V
2	GND
3	GND
4	+5V

Table 3-3: Additional Power Connector Pinouts

3.2.2 ATX Power Connector

CN Label: **ATX1**

CN Type: 24-pin ATX, p=4.2 mm

CN Location: See **Figure 3-3**

CN Pinouts: See **Table 3-4**

The ATX power connector connects to an ATX power supply.

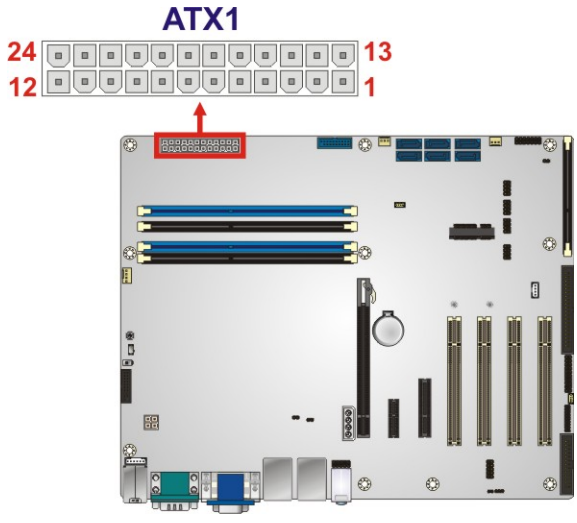


Figure 3-3: ATX Power Connector Location

Pin	Description	Pin	Description
1	+3.3V	13	+3.3V
2	+3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON

Pin	Description	Pin	Description
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	Power good	20	-5V
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND

Table 3-4: ATX Power Connector Pinouts

3.2.3 Battery Connector

CAUTION:

Risk of explosion if battery is replaced by an incorrect type. Only certified engineers should replace the on-board battery.

Dispose of used batteries according to instructions and local regulations.

CN Label: **BAT1**

CN Type: Battery holder

CN Location: See **Figure 3-4**

CN Pinouts: See **Table 3-5**

A system battery is placed in the battery holder. The battery provides power to the system clock to retain the time when power is turned off.

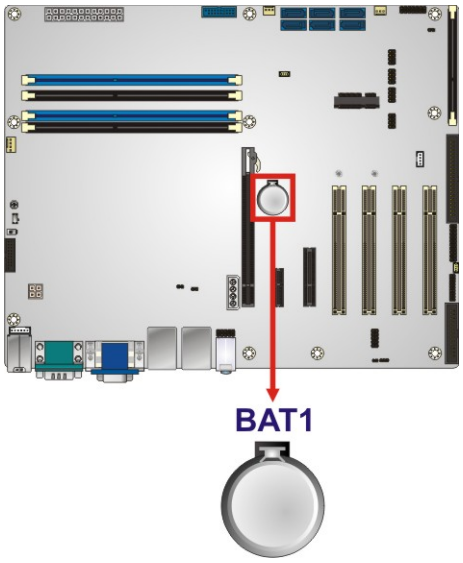


Figure 3-4: Battery Connector Location

Pin	Description
1	NC
2	BAT+
3	BAT-(GND)

Table 3-5: Battery Connector Pinouts

3.2.4 Chassis Intrusion Connector

CN Label: **CHASSIS1**

CN Type: 2-pin header, p=2.54 mm

CN Location: See **Figure 3-5**

CN Pinouts: See **Table 3-6**

The chassis intrusion connector is for a chassis intrusion detection sensor or switch that detects if a chassis component is removed or replaced.

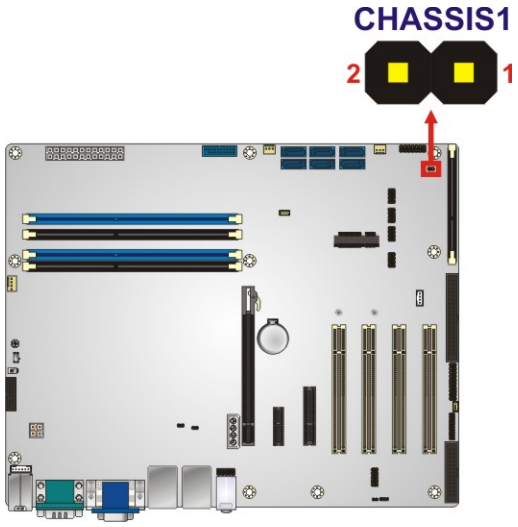


Figure 3-5: Chassis Intrusion Connector Location

Pin	Description
1	+3.3VSB
2	CHASSIS OPEN

Table 3-6: Chassis Intrusion Connector Pinouts

3.2.5 CPU Power Connector

CN Label: **CPU12V1**

CN Type: 4-pin connector, p=4.2 mm

CN Location: See **Figure 3-6**

CN Pinouts: See **Table 3-7**

The CPU power input connector provides power to the CPU.

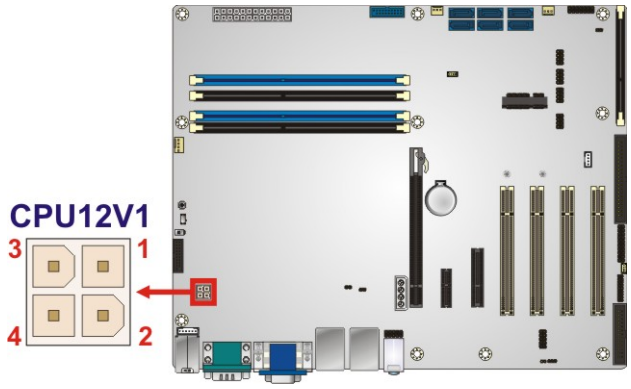


Figure 3-6: CPU Power Connector Location

Pin	Description
1	GND
2	GND
3	+12V
4	+12V

Table 3-7: CPU Power Connector Pinouts

3.2.6 DisplayPort Connector

CN Label: **DP1**

CN Type: 19-pin box header, p=2.0 mm

CN Location: See **Figure 3-7**

CN Pinouts: See **Table 3-8**

The DisplayPort connector supports HDMI, LVDS, VGA, DVI and DisplayPort graphics interfaces with up to 3840x2160 resolutions.

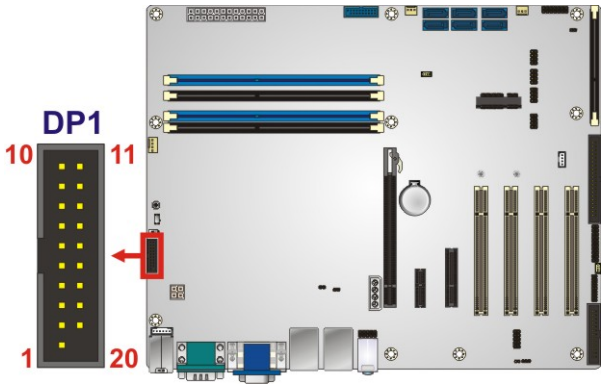


Figure 3-7: DisplayPort Connector Location

Pin	Description	Pin	Description
1	+5V	11	AUXP
2	LANE1N	12	AUXN
3	LANE1P	13	GND
4	GND	14	LANE2P
5	LANE3N	15	LANE2N
6	LANE3P	16	GND
7	GND	17	LANE0P
8	AUX_CTRL_DET_D	18	LANE0N
9	GND	19	+3.3V
10	HPD		

Table 3-8: DisplayPort Connector Pinouts

3.2.7 Digital I/O Connector

CN Label: **DIO1**

CN Type: 10-pin header, p=2.54 mm

CN Location: See **Figure 3-8**

CN Pinouts: See **Table 3-9**

The digital I/O connector provides programmable input and output for external devices. The digital I/O provides 4-bit output and 4-bit input.

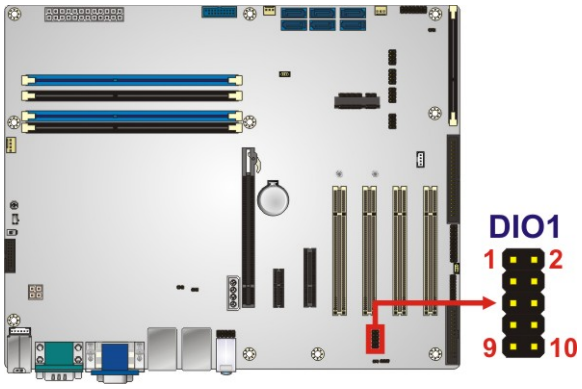


Figure 3-8: Digital I/O Connector Location

Pin	Description	Pin	Description
1	GND	2	VCC
3	Output 3	4	Output 2
5	Output 1	6	Output 0
7	Input 3	8	Input 2
9	Input 1	10	Input 0

Table 3-9: Digital I/O Connector Pinouts

3.2.8 EC Debug Connector

CN Label: **CN3**

CN Type: 18-pin header, p=2.0 mm

CN Location: See **Figure 3-9**

CN Pinouts: See **Table 3-10**

The EC debug connector is used for EC debug.

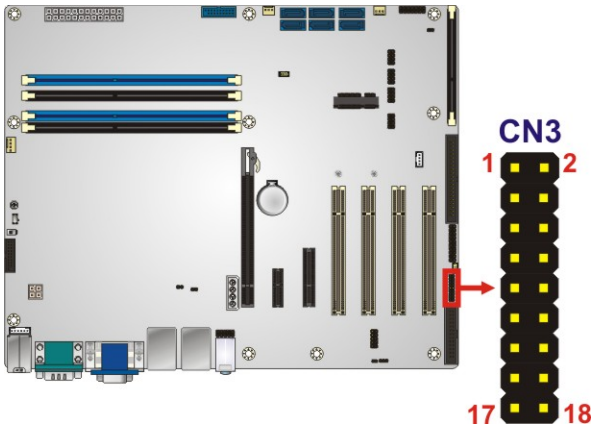


Figure 3-9: EC Debug Connector Location

Pin	Description	Pin	Description
1	EC_EPP_STB#	2	EC_EPP_AFD#
3	EC_EPP_PD0	4	NC
5	EC_EPP_PD1	6	EC_EPP_INIT#
7	EC_EPP_PD2	8	EC_EPP_SLIN#
9	EC_EPP_PD3	10	GND
11	EC_EPP_PD4	12	NC
13	EC_EPP_PD5	14	EC_EPP_BUSY
15	EC_EPP_PD6	16	EC_EPP_KSI5
17	EC_EPP_PD7	18	EC_EPP_KSI4

Table 3-10: EC Debug Connector Pinouts

3.2.9 Fan Connector (CPU)

CN Label: **CPU_FAN1**

CN Type: 4-pin wafer, p=2.54 mm

CN Location: See **Figure 3-10**

CN Pinouts: See **Table 3-11**

The fan connector attaches to a CPU cooling fan.

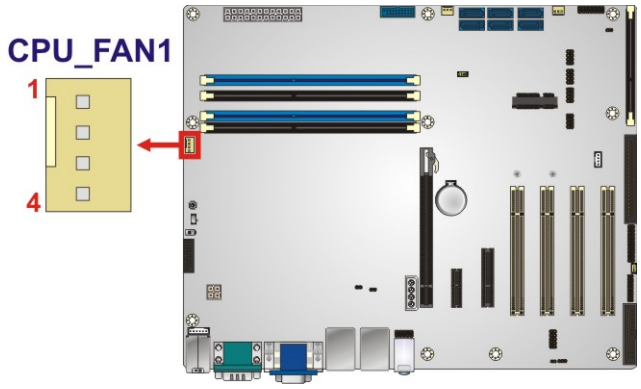


Figure 3-10: CPU Fan Connector Location

Pin	Description
1	GND
2	+12 V
3	FANIO
4	PWM

Table 3-11: CPU Fan Connector Pinouts

3.2.10 Fan Connectors (System)

CN Label: **SYS_FAN1, SYS_FAN2**

CN Type: 3-pin wafer, p=2.54 mm

CN Location: See **Figure 3-11**

CN Pinouts: See **Table 3-12 and Table 3-13**

Each fan connector attaches to a system cooling fan.

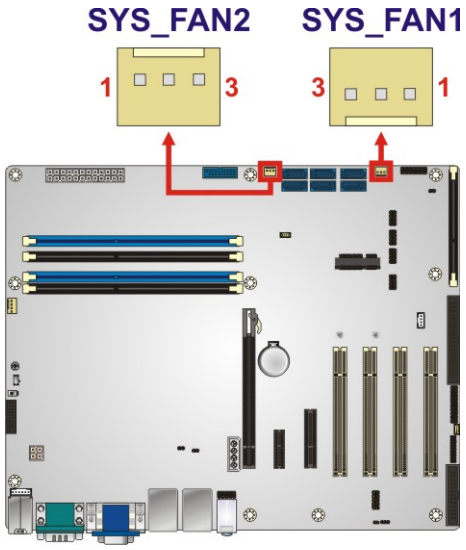


Figure 3-11: System Fan Connector Locations

Pin	Description
1	FANIO
2	+12 V (PWM)
3	GND

Table 3-12: System Fan Connector 1 Pinouts (SYS_FAN1)

Pin	Description
1	NC
2	+12 V
3	GND

Table 3-13: System Fan Connector 2 Pinouts (SYS_FAN2)

3.2.11 Front Panel Audio Connector

CN Label: **FRONT-PANEL1**

CN Type: 10-pin header, p=2.54 mm

CN Location: See **Figure 3-12**

CN Pinouts: See **Table 3-14**

This connector connects to speakers, a microphone and an audio input.

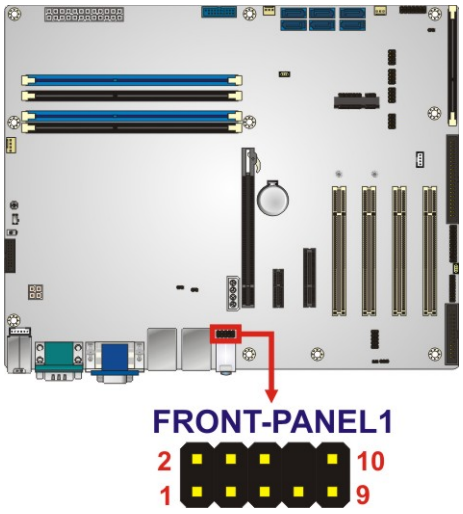


Figure 3-12: Front Panel Audio Connector Location

Pin	Description	Pin	Description
1	MIC2-L	2	GND
3	MIC2-R	4	Presence#
5	LINE2-R	6	MIC2-JD
7	FRONT-IO	8	NC
9	LINE2-L	10	LINE2-JD

Table 3-14: Front Panel Audio Connector Pinouts

3.2.12 Front Panel Connector

CN Label: **F_PANEL1**

CN Type: 14-pin header, p=2.54 mm

CN Location: See **Figure 3-13**

CN Pinouts: See **Table 3-15**

The front panel connector connects to the indicator LEDs and buttons on the computer's front panel.

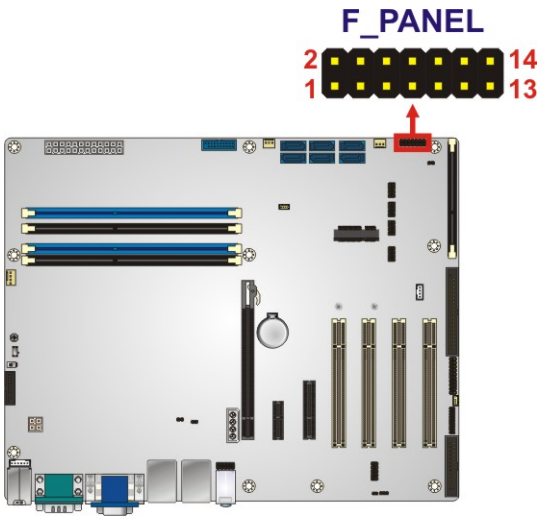


Figure 3-13: Front Panel Connector Location

Function	Pin	Description	Function	Pin	Description
Power LED	1	+5V	Speaker	2	Beep Power
	3	NC	IPMI LED	4	IPMI ID_LED+
	5	GND		6	IPMI ID_LED-
Power Button	7	PWRBT_SW#	Speaker	8	PC Beep
	9	GND		10	NC
HDD LED	11	+5V	Reset	12	EXTRST-
	13	SATA_LED#		14	GND

Table 3-15: Front Panel Connector Pinouts

3.2.13 iRIS Module Slot

CN Label:	**IPMI1**
CN Type:	204-pin DDR3 SO-DIMM slot
CN Location:	See **Figure 3-14**

The iRIS module slot is used to install the IEI iRIS-2400 IPMI 2.0 module. Please refer to **Section 4.6** for IPMI setup procedure.

WARNING:

The iRIS module slot is designed to install the IEI iRIS-2400 IPMI 2.0 module only. DO NOT install other modules into the iRIS module slot. Doing so may cause damage to the IMBA-Q870-i2.

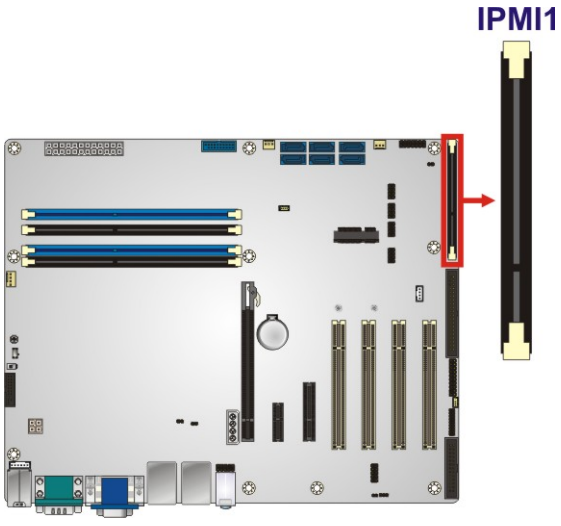


Figure 3-14: iRIS Module Slot Location

3.2.14 Keyboard and Mouse Connector

CN Label:	**KB_MS1**
CN Type:	6-pin wafer, p=2.0 mm
CN Location:	See **Figure 3-15**
CN Pinouts:	See **Table 3-16**

The keyboard/mouse connector connects to a PS/2 Y-cable that can be connected to a PS/2 keyboard and mouse.

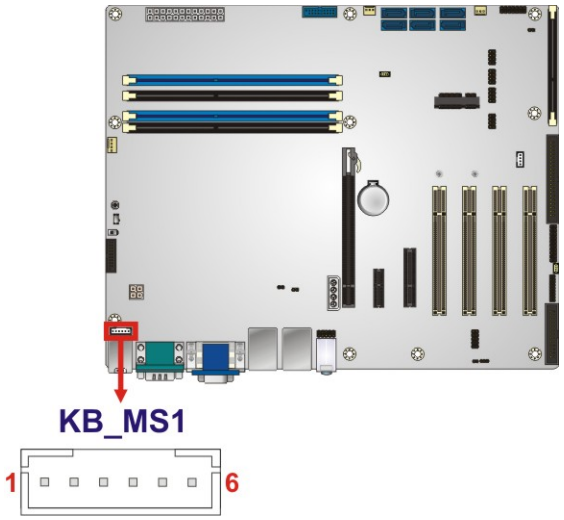


Figure 3-15: Keyboard and Mouse Location

Pin	Description
1	VCC
2	Mouse Data
3	Mouse Clock
4	Keyboard Data
5	Keyboard Clock
6	GND

Table 3-16: Keyboard and Mouse Connector Pinouts

3.2.15 LAN LED Connectors

CN Label:	**LED_LAN1, LED_LAN2**
CN Type:	2-pin header, p=2.54 mm
CN Location:	See **Figure 3-16**
CN Pinouts:	See **Table 3-17** and **Table 3-18**

The LAN LED connectors are used to connect to the LAN LED indicators on the chassis to indicate users the link activities of the two LAN ports.

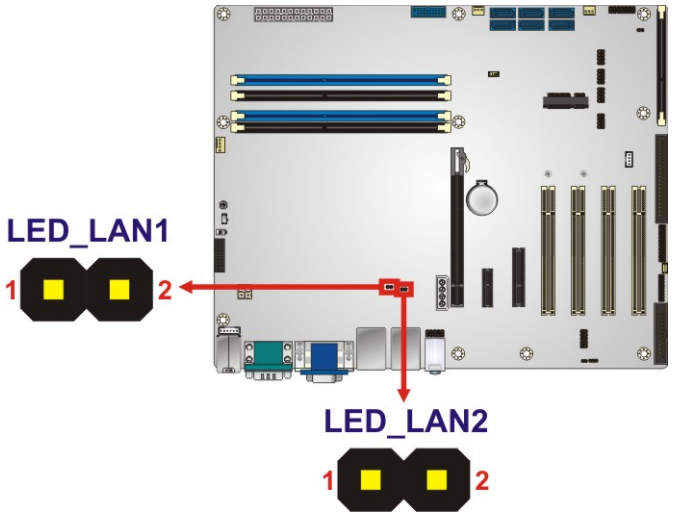


Figure 3-16: LAN LED Connector Locations

Pin	Description
1	+3.3V
2	LAN1_LED_LINK#_ACT

Table 3-17: LAN1 LED Connector (LED_LAN1) Pinouts

Pin	Description
1	+3.3V
2	LAN2_LED_LINK#_ACT

Table 3-18: LAN2 LED Connector (LED_LAN2) Pinouts

3.2.16 Memory Card Slots

CN Label:	**CHA_DIMM1, CHA_DIMM2, CHB_DIMM1, CHB_DIMM2**
CN Type:	DDR3 DIMM slot
CN Location:	See **Figure 3-17**

The DIMM slots are for DDR3 DIMM memory modules.

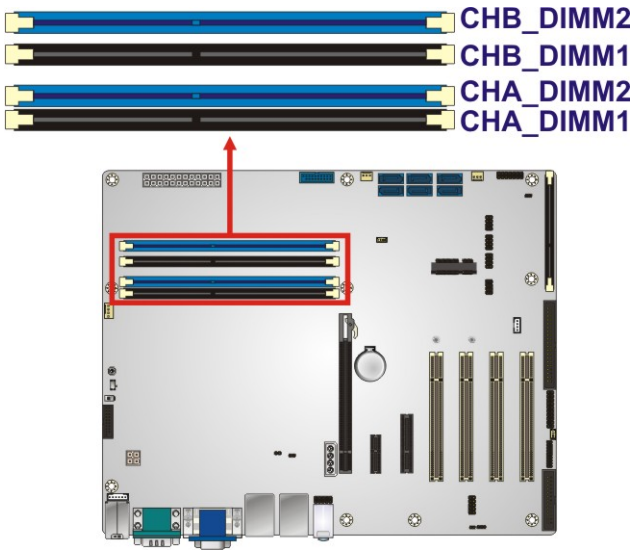


Figure 3-17: Memory Card Slot Locations

3.2.17 mSATA Card Slot

CN Label:	**CN2**
CN Type:	PCIe Mini slot
CN Location:	See **Figure 3-18**
CN Pinouts:	See **Table 3-19**

The mSATA card slot is for installing mSATA cards or USB devices only.

NOTE:

If the user shorts the mSATA Slot Setup jumper (MSATA_SW1) to force the system to enable mSATA device, the S_ATA6 connector will be disabled. Please refer to **Section 4.3.4**.

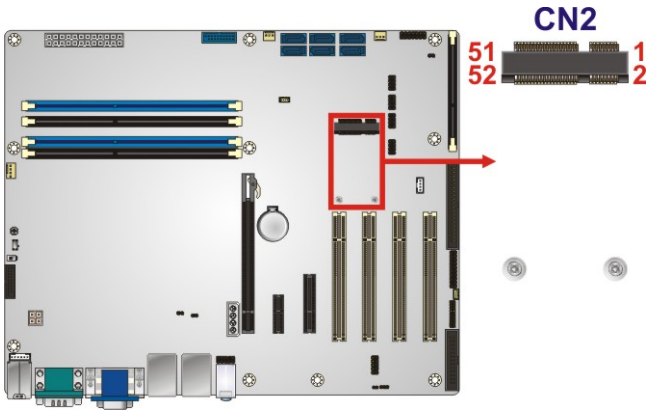


Figure 3-18: mSATA Card Slot Location

Pin	Description	Pin	Description
1	PCIE_WAKE#	2	+3.3V
3	N/C	4	GND
5	N/C	6	1.5V
7	N/C	8	N/C
9	GND	10	N/C
11	MSATA_CLK#	12	N/C
13	MSATA _CLK	14	N/C
15	GND	16	N/C
17	PLTRST_N	18	GND
19	N/C	20	+3.3V
21	GND	22	PLTRST_N
23	SATA_RX+	24	+3.3V
25	SATA_RX-	26	GND
27	GND	28	1.5V

Pin	Description	Pin	Description
29	GND	30	SMB_CLK
31	SATA_TX-	32	SMB_DATA
33	SATA_TX+	34	GND
35	GND	36	USB_DATA-
37	GND	38	USB_DATA+
39	+3.3V	40	GND
41	+3.3V	42	N/C
43	+3.3V	44	N/C
45	CLINK_CLK	46	N/C
47	CLINK_DATA	48	1.5V
49	CLINK_RST#	50	GND
51	MSATA_DET	52	+3.3V

Table 3-19: mSATA Card Slot Pinouts

3.2.18 Parallel Port Connector

CN Label: **LPT1**

CN Type: 26-pin box header, p=2.54 mm

CN Location: See **Figure 3-19**

CN Pinouts: See **Table 3-20**

The parallel port connector connects to a parallel port connector interface or some other parallel port device such as a printer.

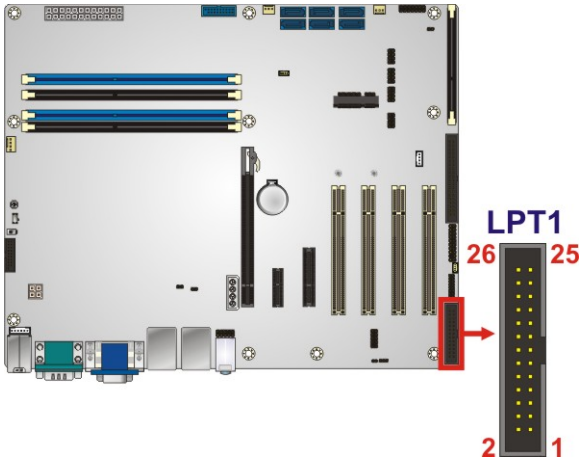


Figure 3-19: Parallel Port Connector Location

Pin	Description	Pin	Description
1	STROBE#	2	DATA 0
3	DATA 1	4	DATA 2
5	DATA 3	6	DATA 4
7	DATA 5	8	DATA 6
9	DATA 7	10	ACKNOWLEDGE#
11	BUSY	12	PAPER EMPTY
13	PRINTER SELECT	14	AUTO FORM FEED #
15	ERROR#	16	INITIALIZE#
17	PRINTER SELECT LN#	18	GROUND
19	GROUND	20	GROUND
21	GROUND	22	GROUND
23	GROUND	24	GROUND
25	GROUND	26	NC

Table 3-20: Parallel Port Connector Pinouts

3.2.19 Power Button

CN Label: **PWR_SW1**

CN Type: Push button

CN Location: See **Figure 3-20**

The on-board power button controls system power.

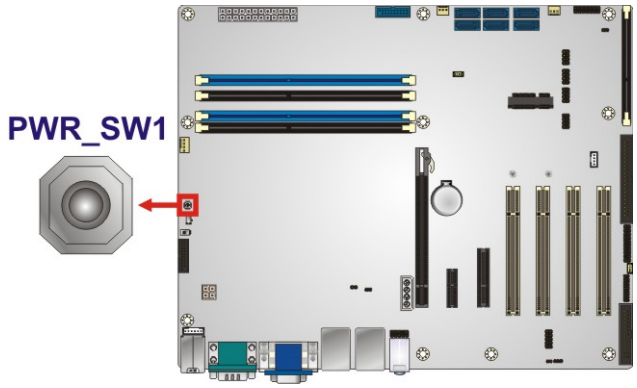


Figure 3-20: Power Button Location

3.2.20 SATA 6Gb/s Drive Connectors

CN Label:	**S_ATA1, S_ATA2, S_ATA3, S_ATA4, S_ATA5, S_ATA6**
CN Type:	7-pin SATA drive connectors
CN Location:	See **Figure 3-21**
CN Pinouts:	See **Table 3-21**

The SATA drive connectors can be connected to SATA drives.

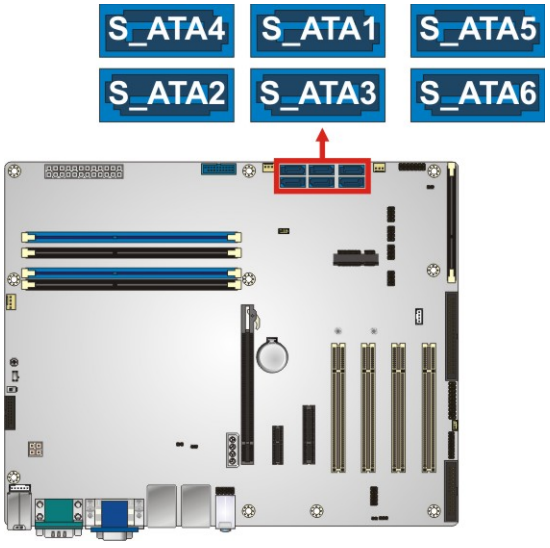


Figure 3-21: SATA 6Gb/s Drive Connector Locations

Pin	Description
1	GND
2	SATA_TX+
3	SATA_TX-
4	GND
5	SATA_RX-
6	SATA_RX+
7	GND

Table 3-21: SATA 6Gb/s Drive Connector Pinouts

NOTE:

If the user shorts the mSATA Slot Setup jumper (MSATA_SW1) to force the system to enable mSATA device, the S_ATA6 connector will be disabled. Please refer to **Section 4.3.4**.

3.2.21 Serial Port Connectors, RS-232

CN Label: **COM2-6**

CN Type: 40-pin box header, p=2.54 mm

CN Location: See **Figure 3-22**

CN Pinouts: See **Table 3-22**

The connector provides four RS-232 ports connection.

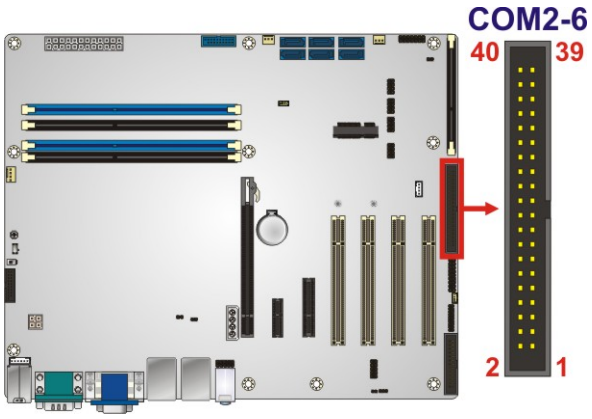


Figure 3-22: Serial Port Connector Location

	Pin	Description	Pin	Description
COM2	1	DCD	2	DSR
	3	RXD	4	RTS
	5	TXD	6	CTS
	7	DTR	8	RI
	9	GND	10	GND
COM3	11	DCD	12	DSR
	13	RXD	14	RTS
	15	TXD	16	CTS
	17	DTR	18	RI
	19	GND	20	GND
COM5	21	DCD	22	DSR
	23	RXD	24	RTS
	25	TXD	26	CTS
	27	DTR	28	RI
	29	GND	30	GND
COM6	31	DCD	32	DSR
	33	RXD	34	RTS
	35	TXD	36	CTS
	37	DTR	38	RI
	39	GND	40	GND

Table 3-22: COM3~6 Serial Port Connector Pinouts

3.2.22 Serial Port Connector, RS-422/485

CN Label:	**COM4**
CN Type:	4-pin wafer, p=2.0 mm
CN Location:	See **Figure 3-23**
CN Pinouts:	See **Table 3-23**

Used for RS-422/485 communications.

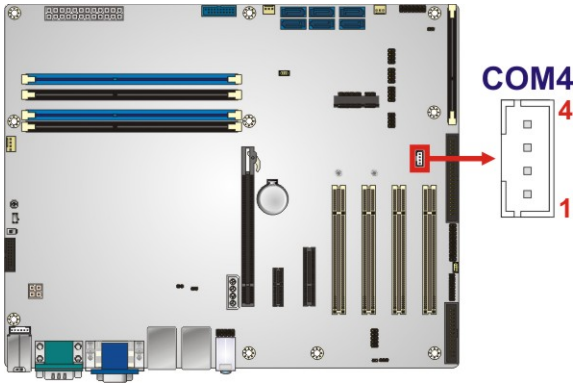


Figure 3-23: RS-422/485 Connector Location

Pin	Description
1	RXD422-
2	RXD422+
3	TXD422+/TXD485+
4	TXD422-/TXD485-

Table 3-23: RS-422/485 Connector Pinouts

Use the optional RS-422/485 cable to connect to a serial device. The pinouts of the DB-9 connector are listed below.

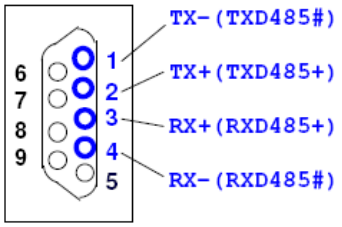
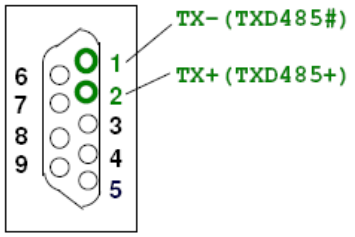
RS-422 Pinouts	RS-485 Pinouts
	

Table 3-24: DB-9 RS-422/485 Pinouts

3.2.23 SMBus Connector

CN Label: **CN1**

CN Type: 4-pin wafer, p=1.25 mm

CN Location: See **Figure 3-24**

CN Pinouts: See **Table 3-25**

The SMBus (System Management Bus) connector provides low-speed system management communications.

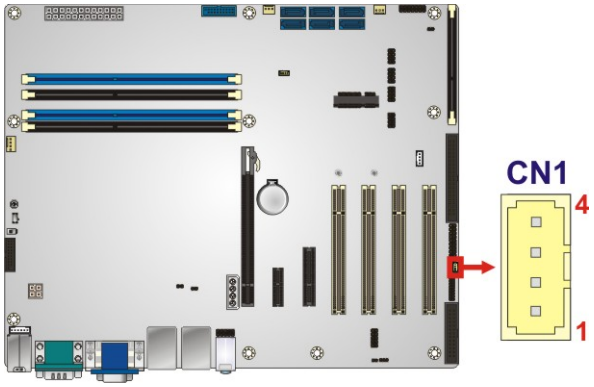


Figure 3-24: SMBus Connector Location

Pin	Description
1	GND
2	SMB_DATA
3	SMB_CLK
4	+5V

Table 3-25: SMBus Connector Pinouts

3.2.24 SPI Flash Connector

CN Label: **JSPI1**

CN Type: 8-pin header, p=2.54 mm

CN Location: See **Figure 3-25**

CN Pinouts: See **Table 3-26**

The SPI flash connector is used to flash the SPI ROM.

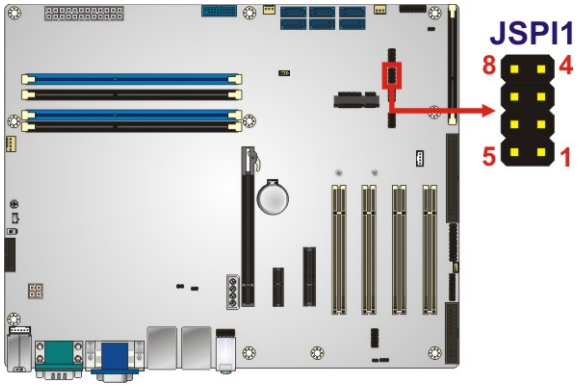


Figure 3-25: SPI Flash Connector Location

Pin	Description	Pin	Description
1	+3.3V	2	SPI_CS#
3	SPI_SO	4	NC
5	GND	6	SPI_CLK
7	SPI_SI	8	NC

Table 3-26: SPI Flash Connector Pinouts

3.2.25 SPI Flash Connector, EC

CN Label: **JSPI2**

CN Type: 8-pin header, p=2.54 mm

CN Location: See **Figure 3-26**

CN Pinouts: See **Table 3-27**

The SPI flash connector is used to flash the EC ROM.

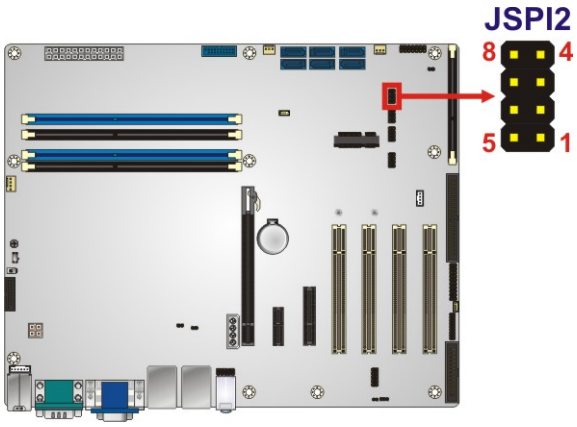


Figure 3-26: SPI EC Flash Connector Location

Pin	Description	Pin	Description
1	+3.3V	2	SPI_CS#
3	SPI_SO	4	NC
5	GND	6	SPI_CLK
7	SPI_SI	8	NC

Table 3-27: SPI EC Flash Connector Pinouts

3.2.26 TPM Connector

CN Label: **TPM1**

CN Type: 20-pin header, p=2.54 mm

CN Location: See **Figure 3-27**

CN Pinouts: See **Table 3-28**

The TPM connector connects to a TPM module.

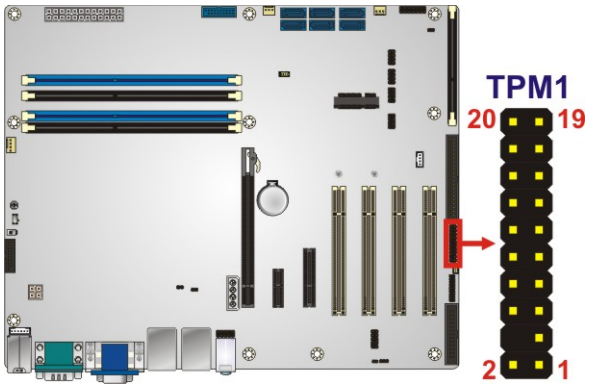


Figure 3-27: TPM Connector Location

Pin	Description	Pin	Description
1	LCLK	2	GND
3	LFRAME#	4	KEY
5	LRERST#	6	+5V
7	LAD3	8	LAD2
9	+3.3V	10	LAD1
11	LAD0	12	GND
13	SCL	14	SDA
15	SB3V	16	SERIRQ
17	GND	18	GLKRUN#
19	LPCPD#	20	LDRQ#

Table 3-28: TPM Connector Pinouts

3.2.27 USB 2.0 Connectors

CN Label: **USB1, USB2**

CN Type: 8-pin header, p=2.54 mm

CN Location: See **Figure 3-28**

CN Pinouts: See **Table 3-29**

The USB 2.0 connectors connect to USB 2.0 devices. Each pin header provides two USB 2.0 ports.

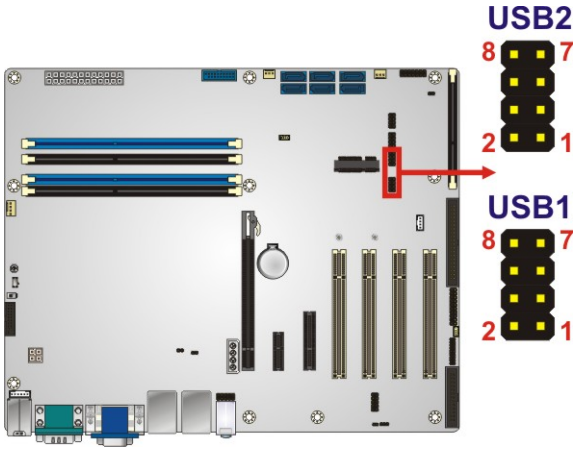


Figure 3-28: USB 2.0 Connector Locations

Pin	Description	Pin	Description
1	VCC	2	GND
3	USB_DATA-	4	USB_DATA+
5	USB_DATA+	6	USB_DATA-
7	GND	8	VCC

Table 3-29: USB 2.0 Connector Pinouts

3.2.28 USB 3.1 Gen 1 Connector

CN Label: **CN4**

CN Type: 19-pin box header, p=2.0 mm

CN Location: See **Figure 3-29**

CN Pinouts: See **Table 3-30**

The USB 3.1 Gen 1 (5 Gb/s) connector connects to USB 3.1 Gen 1 devices. This connector provides two USB 3.1 Gen 1 ports.

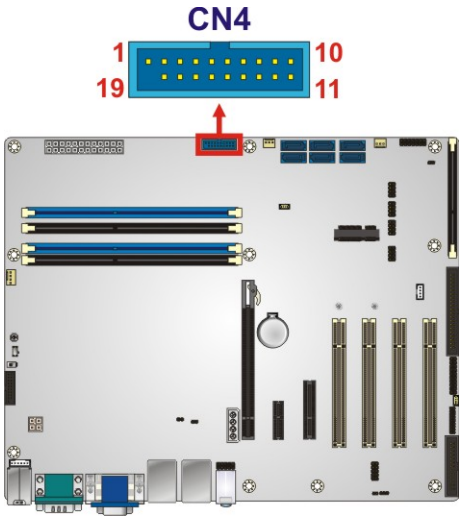


Figure 3-29: USB 3.1 Gen 1 Connector Location

Pin	Description	Pin	Description
1	VCC	11	USB_DATA+
2	USB3_RX-	12	USB_DATA-
3	USB3_RX+	13	GND
4	GND	14	USB3_TX+
5	USB3_TX-	15	USB3_TX-
6	USB3_TX+	16	GND
7	GND	17	USB3_RX+
8	USB_DATA-	18	USB3_RX-
9	USB_DATA+	19	VCC
10	NC		

Table 3-30: USB 3.1 Gen 1 Connector Pinouts

3.3 External Peripheral Interface Connector Panel

The figure below shows the external peripheral interface connector (EPIC) panel. The EPIC panel consists of the following:

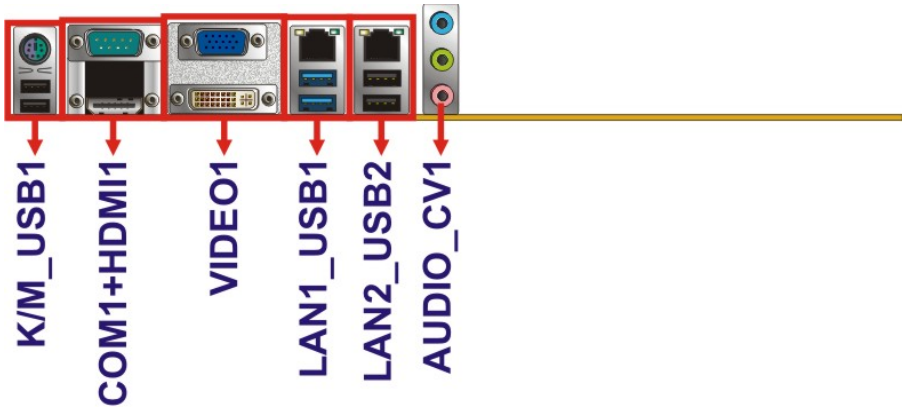


Figure 3-30: External Peripheral Interface Connector

3.3.1 Audio Connector

CN Label:	**AUDIO_CV1**
CN Type:	Audio jack
CN Location:	See **Figure 3-30**

The audio jacks connect to external audio devices.

- **Line In port (Light Blue):** Connects a CD-ROM, DVD player, or other audio devices.
- **Line Out port (Lime):** Connects to a headphone or a speaker. With multi-channel configurations, this port can also connect to front speakers.
- **Microphone (Pink):** Connects a microphone.

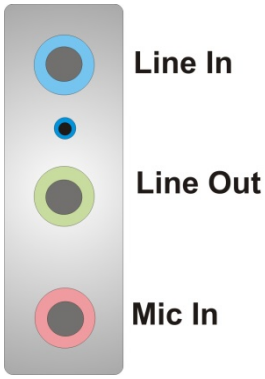


Figure 3-31: Audio Connector

3.3.2 Keyboard/Mouse and USB 2.0 Connectors

CN Label: **K/M_USB1**

CN Type: PS/2, USB 2.0

CN Location: See **Figure 3-30**

CN Pinouts: See **Table 3-31** and **Table 3-32**

The USB 2.0 connector can be connected to a USB device.

Pin	Description	Pin	Description
1	VCC	5	VCC
2	USB_DATA-	6	USB_DATA-
3	USB_DATA+	7	USB_DATA+
4	GND	8	GND

Table 3-31: USB 2.0 Port Pinouts

The PS/2 port is for connecting a PS/2 mouse and a PS/2 keyboard.

Pin	Description
9	GND
10	Keyboard Data
11	Mouse Data
12	VCC
13	Keyboard Clock
14	Mouse Clock

Table 3-32: PS/2 Connector Pinouts

3.3.3 Ethernet and USB 2.0 Connectors

CN Label: **LAN2_USB2**

CN Type: RJ-45, USB 3.1 Gen 1

CN Location: See **Figure 3-30**

CN Pinouts: See **Table 3-33** and **Table 3-34**

The USB 2.0 connector can be connected to a USB device.

Pin	Description	Pin	Description
1	VCC	5	VCC
2	USB_DATA-	6	USB_DATA-
3	USB_DATA+	7	USB_DATA+
4	GND	8	GND

Table 3-33: USB 2.0 Port Pinouts

A 10/100/1000 Mb/s connection can be made to a Local Area Network.

Pin	Description	Pin	Description
P2	TRD2P0	P6	TRD2P2
P3	TRD2N0	P7	TRD2N2
P4	TRD2P1	P8	TRD2P3
P5	TRD2N1	P9	TRD2N3

Table 3-34: LAN2 Pinouts

3.3.4 Ethernet and USB 3.1 Gen 1 Connectors

CN Label: **LAN1_USB1**

CN Type: RJ-45, USB 3.0

CN Location: See **Figure 3-30**

CN Pinouts: See **Table 3-35** and **Table 3-36**

There are two external USB 3.1 Gen 1 (5 Gb/s) connectors on the IMBA-Q870-i2.

Pin	Description	Pin	Description
1	VCC	10	VCC
2	USB_DATA-	11	USB_DATA-
3	USB_DATA+	12	USB_ DATA+
4	GND	13	GND
5	USB3_RX-	14	USB3_RX-
6	USB3_RX+	15	USB3_ RX+

Pin	Description	Pin	Description
7	GND	16	GND
8	USB3_TX-	17	USB3_TX-
9	USB3_TX+	18	USB3_TX+

Table 3-35: USB 3.1 Gen 1 Port Pinouts

A 10/100/1000 Mb/s connection can be made to a Local Area Network. LAN1 also supports Intel® AMT 9.0.

Pin	Description	Pin	Description
20	LAN1_MDI0P	24	LAN1_MDI2P
21	LAN1_MDI0N	25	LAN1_MDI2N
22	LAN1_MDI1P	26	LAN1_MDI3P
23	LAN1_MDI1N	27	LAN1_MDI3N

Table 3-36: LAN1 Pinouts

3.3.5 HDMI Port Connector

CN Label: **HDMI1**

CN Type: HDMI connector

CN Location: See **Figure 3-30**

CN Pinouts: See **Table 3-37**

The HDMI port connects to an HDMI device.

Pin	Description	Pin	Description
1	HDMI_DATA2	13	N/C
2	GND	14	N/C
3	HDMI_DATA2#	15	HDMI_SCL
4	HDMI_DATA1	16	HDMI_SDA
5	GND	17	GND
6	HDMI_DATA1#	18	+5V
7	HDMI_DATA0	19	HDMI_HPD

Pin	Description	Pin	Description
8	GND	20	HDMI_GND
9	HDMI_DATA0#	21	HDMI_GND
10	HDMI_CLK	22	HDMI_GND
11	GND	23	HDMI_GND
12	HDMI_CLK#		

Table 3-37: HDMI Connector Pinouts

3.3.6 Serial Port Connector (COM1)

CN Label: **COM1**

CN Type: DB-9 connector

CN Location: See **Figure 3-30**

CN Pinouts: See **Table 3-38**

The serial port connects to a RS-232 serial communications device.

Pin	Description	Pin	Description
1	DCD1	6	DSR1
2	RXD1	7	RTS1
3	TXD1	8	CTS1
4	DTR1	9	RI1
5	GND		

Table 3-38: Serial Port Connector Pinouts

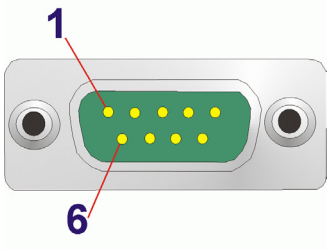


Figure 3-32: Serial Port Connector Pinouts

3.3.7 VGA and DVI Connectors

CN Label: **VIDEO1**

CN Type: 15-pin Female, 24-pin header

CN Location: See **Figure 3-30**

CN Pinouts: See **Table 3-39** and **Table 3-40**

The VGA connector connects to a monitor that accepts a standard VGA input.

Pin	Description	Pin	Description
V1	RED	V2	GREEN
V3	BLUE	V4	NC
V5	GND	V6	GND
V7	GND	V8	GND
V9	VCC	V10	GND
V11	NC	V12	DDCDA
V13	HSYNC	V14	VSYNC
V15	DDCCLK		

Table 3-39: VGA Connector Pinouts

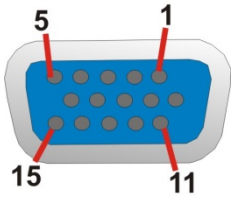


Figure 3-33: VGA Connector

The DVI connector connects to a monitor that supports DVI video input.

Pin	Description	Pin	Description
C1	RED	10	DVI_DATA1
C2	GREEN	11	GND
C3	BLUE	12	N/C
C4	HS	13	N/C

Pin	Description	Pin	Description
C5	GND	14	+5V
C6	NC	15	Hot Plug Detect
1	DVI_DATA2#	16	HPDET
2	DVI_DATA2	17	DVI_DATA0#
3	GND	18	DVI_DATA0
4	N/C	19	GND
5	N/C	20	N/C
6	DDC CLK	21	N/C
7	DDC DATA	22	N/C
8	VS	23	DVI_CLK
9	DVI_DATA1#	24	DVI_CLK#

Table 3-40: DVI Connector Pinouts

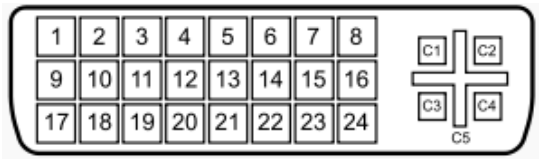


Figure 3-34: DVI-I Connector

Chapter

4

Installation

4.1 Anti-static Precautions

WARNING:

Failure to take ESD precautions during the installation of the IMBA-Q870-i2 may result in permanent damage to the IMBA-Q870-i2 and severe injury to the user.

Electrostatic discharge (ESD) can cause serious damage to electronic components, including the IMBA-Q870-i2. Dry climates are especially susceptible to ESD. It is therefore critical that whenever the IMBA-Q870-i2 or any other electrical component is handled, the following anti-static precautions are strictly adhered to.

- ***Wear an anti-static wristband***: - Wearing a simple anti-static wristband can help to prevent ESD from damaging the board.
- ***Self-grounding***:- Before handling the board touch any grounded conducting material. During the time the board is handled, frequently touch any conducting materials that are connected to the ground.
- ***Use an anti-static pad***: When configuring the IMBA-Q870-i2, place it on an anti-static pad. This reduces the possibility of ESD damaging the IMBA-Q870-i2.
- ***Only handle the edges of the PCB***:-: When handling the PCB, hold the PCB by the edges.

4.2 Installation Considerations

NOTE:

The following installation notices and installation considerations should be read and understood before installation. All installation notices must be strictly adhered to. Failing to adhere to these precautions may lead to severe damage and injury to the person performing the installation.

WARNING:

The installation instructions described in this manual should be carefully followed in order to prevent damage to the components and injury to the user.

Before and during the installation please **DO** the following:

- Read the user manual:
  - The user manual provides a complete description of the IMBA-Q870-i2 installation instructions and configuration options.
- Wear an electrostatic discharge cuff (ESD):
  - Electronic components are easily damaged by ESD. Wearing an ESD cuff removes ESD from the body and helps prevent ESD damage.
- Place the IMBA-Q870-i2 on an antistatic pad:
  - When installing or configuring the motherboard, place it on an antistatic pad. This helps to prevent potential ESD damage.
- Turn all power to the IMBA-Q870-i2 off:
  - When working with the IMBA-Q870-i2, make sure that it is disconnected from all power supplies and that no electricity is being fed into the system.

Before and during the installation of the IMBA-Q870-i2 **DO NOT:**

- Remove any of the stickers on the PCB board. These stickers are required for warranty validation.
- Use the product before verifying all the cables and power connectors are properly connected.
- Allow screws to come in contact with the PCB circuit, connector pins, or its components.

4.2.1 Socket LGA1150 CPU Installation

WARNING:

CPUs are expensive and sensitive components. When installing the CPU please be careful not to damage it in anyway. Make sure the CPU is installed properly and ensure the correct cooling kit is properly installed.

DO NOT touch the pins at the bottom of the CPU. When handling the CPU, only hold it on the sides.

To install the CPU, follow the steps below.

Step 1: **Disengage the load lever** by pressing the lever down and slightly outward to clear the retention tab. Fully open the lever. See **Figure 4-1**.

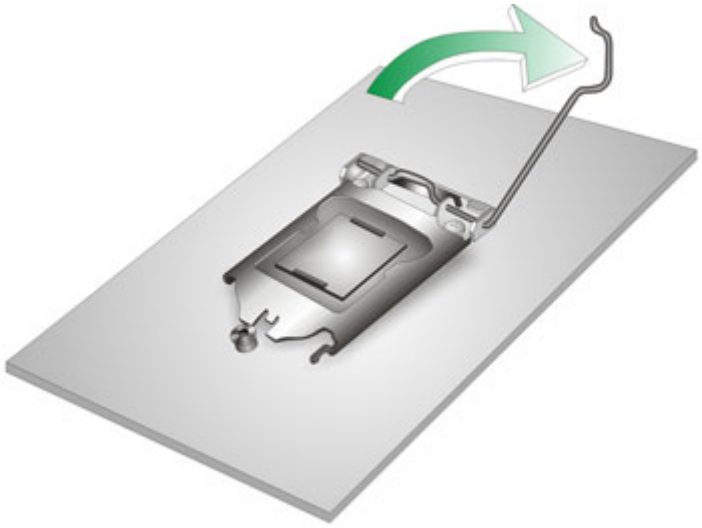


Figure 4-1: Disengage the CPU Socket Load Lever

Step 2: **Open the socket and remove the protective cover.** The black protective cover can be removed by pulling up on the tab labeled "Remove". See **Figure 4-2**.

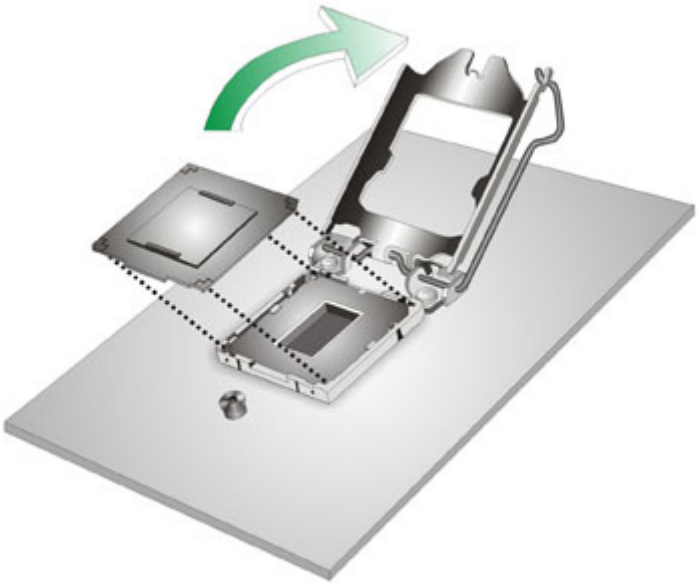


Figure 4-2: Remove Protective Cover

Step 3: **Inspect the CPU socket.** Make sure there are no bent pins and make sure the socket contacts are free of foreign material. If any debris is found, remove it with compressed air.

Step 4: **Orientate the CPU properly.** The contact array should be facing the CPU socket.

WARNING:

DO NOT touch the pins at the bottom of the CPU. When handling the CPU, only hold it on the sides.

Step 5: **Correctly position the CPU.** Match the Pin 1 mark with the cut edge on the CPU socket.

Step 6: **Align the CPU pins.** Locate pin 1 and the two orientation notches on the CPU. Carefully match the two orientation notches on the CPU with the socket alignment keys.

Step 7: **Insert the CPU.** Gently insert the CPU into the socket. If the CPU pins are properly aligned, the CPU should slide into the CPU socket smoothly. See **Figure 4-3**.

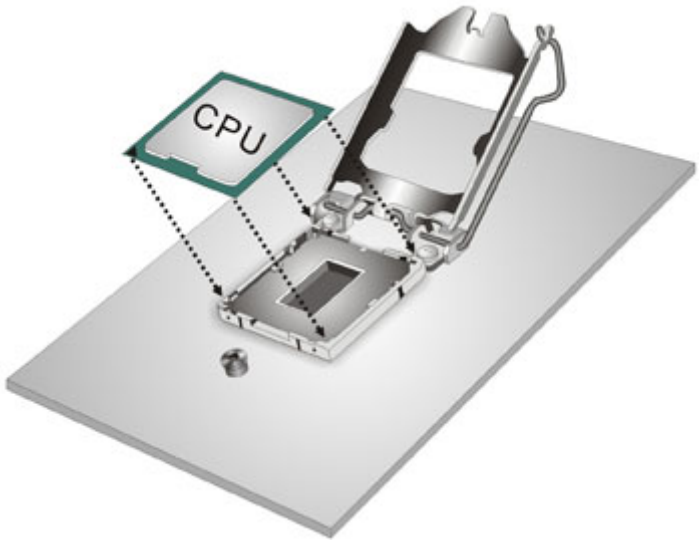


Figure 4-3: Insert the Socket LGA1150 CPU

Step 8: **Close the CPU socket.** Close the load plate and pull the load lever back a little to have the load plate be able to secure to the knob. Engage the load lever by pushing it back to its original position (**Figure 4-4**). There will be some resistance, but will not require extreme pressure.

Figure 4-4: Close the Socket LGA1150

Step 9: **Connect the 12 V power to the board.** Connect the 12 V power from the power supply to the board.

4.2.2 Socket LGA1150 Cooling Kit Installation

WARNING:

DO NOT attempt to install a push-pin cooling fan.

The pre-installed support bracket prevents the board from bending and is ONLY compatible with captive screw type cooling fans.

Figure 4-5: Cooling Kits (CF-115XA-R10 and CF-115XE-R10)

The cooling kit can be bought from IEI. The cooling kit has a heatsink and fan.

WARNING:

Do not wipe off (accidentally or otherwise) the pre-sprayed layer of thermal paste on the bottom of the heat sink. The thermal paste between the CPU and the heat sink is important for optimum heat dissipation.

To install the cooling kit, follow the instructions below.

Step 1: A cooling kit bracket is pre-installed on the rear of the motherboard. See **Figure 4-6**.

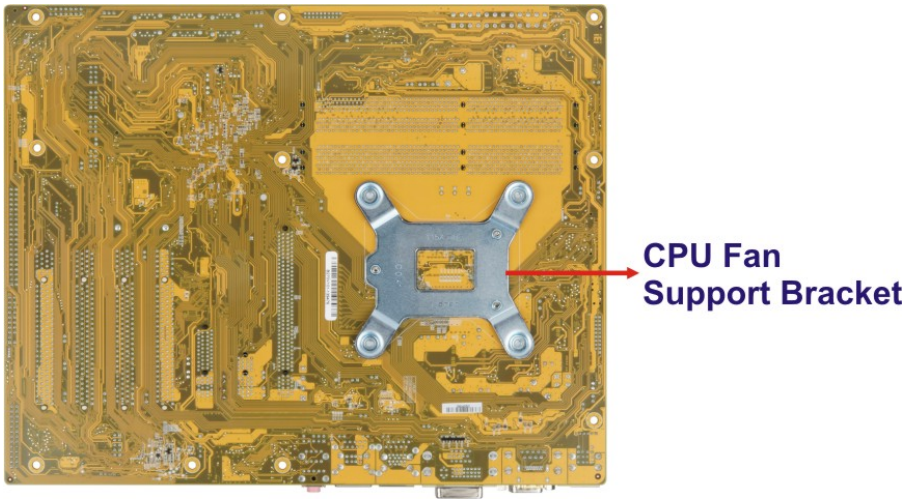


Figure 4-6: Cooling Kit Support Bracket

Step 2: **Place the cooling kit onto the socket LGA1150 CPU**. Make sure the CPU cable can be properly routed when the cooling kit is installed.

Step 3: **Mount the cooling kit**. Gently place the cooling kit on top of the CPU. Make sure the four threaded screws on the corners of the cooling kit properly pass through the holes of the cooling kit bracket.

Step 4: **Secure the cooling kit** by fastening the four retention screws of the cooling kit.

Step 5: **Connect the fan cable**. Connect the cooling kit fan cable to the fan connector on the IMBA-Q870-i2. Carefully route the cable and avoid heat generating chips and fan blades.

4.2.3 DIMM Installation

To install a DIMM, please follow the steps below and refer to **Figure 4-7**.

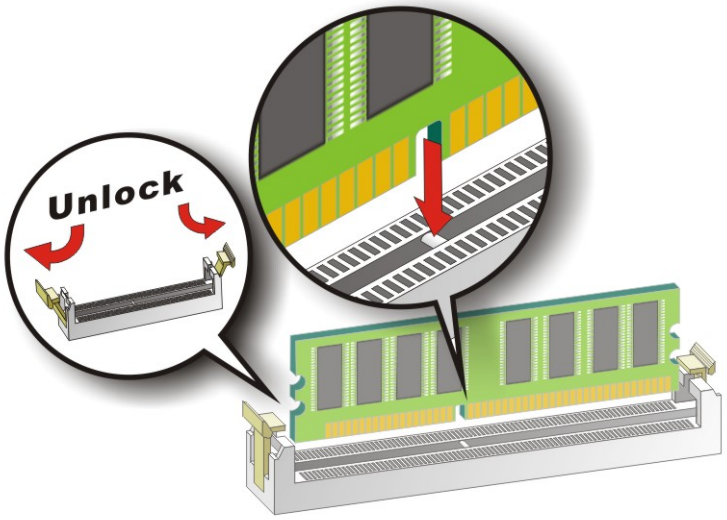


Figure 4-7: DIMM Installation

Step 1: **Open the DIMM socket handles**. Open the two handles outwards as far as they can. See **Figure 4-7**.

Step 2: **Align the DIMM with the socket**. Align the DIMM so the notch on the memory lines up with the notch on the memory socket. See **Figure 4-7**.

Step 3: **Insert the DIMM**. Once aligned, press down until the DIMM is properly seated. Clip the two handles into place. See **Figure 4-7**.

Step 4: **Removing a DIMM**. To remove a DIMM, push both handles outward. The memory module is ejected by a mechanism in the socket.

4.2.4 iRIS-2400 Module Installation

WARNING:

The iRIS module slot is designed to install the IEI iRIS-2400 IPMI 2.0 module only. DO NOT install other modules into the iRIS module slot. Doing so may cause damage to the IMBA-Q870-i2.

To install the iRIS-2400 module, please follow the steps below and refer to Figure 4-7.

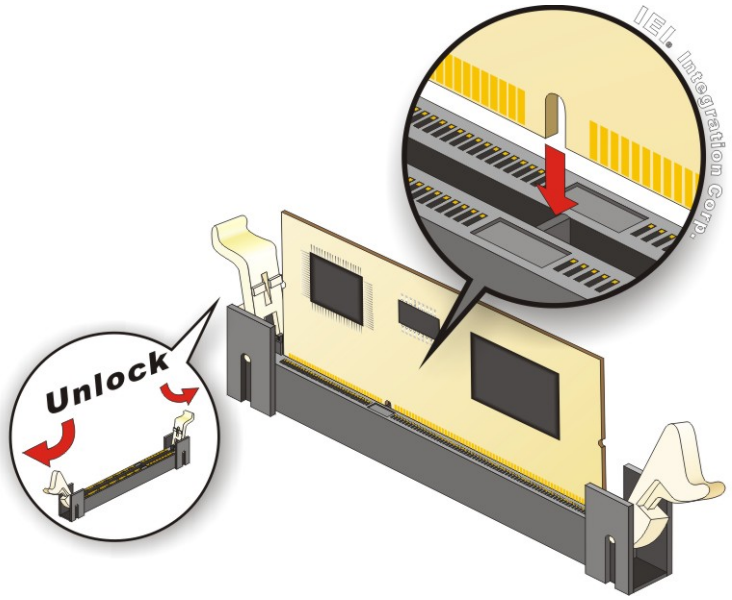


Figure 4-8: iRIS-2400 Module Installation

Step 1: **Open the socket handles**. Open the two handles outwards as far as they can. See Figure 4-7.

Step 2: **Align the iRIS-2400 module with the socket**. Align the iRIS-2400 module so the notch on the module lines up with the notch on the socket. See Figure 4-7.

Step 3: **Insert the iRIS-2400 module**. Once aligned, press down until the iRIS-2400 module is properly seated. Clip the two handles into place. See Figure 4-7.

Step 4: **Removing the iRIS-2400 module**. To remove the iRIS-2400 module, push both handles outward. The module is ejected by a mechanism in the socket.

NOTE:

After installing the iRIS-2400 module, use **LAN2** port to establish a network connection. Please refer to **Section 4.6** for IPMI setup procedures.

4.2.5 mSATA Card Installation

To install a mSATA card, please follow the steps below.

Step 1: **Locate the mSATA card slot**. The location of the mSATA card slot is shown in **Chapter 3**.

Step 2: **Remove the retention screws**. Remove the two retention screws secured on the motherboard as shown in **Figure 4-9**.

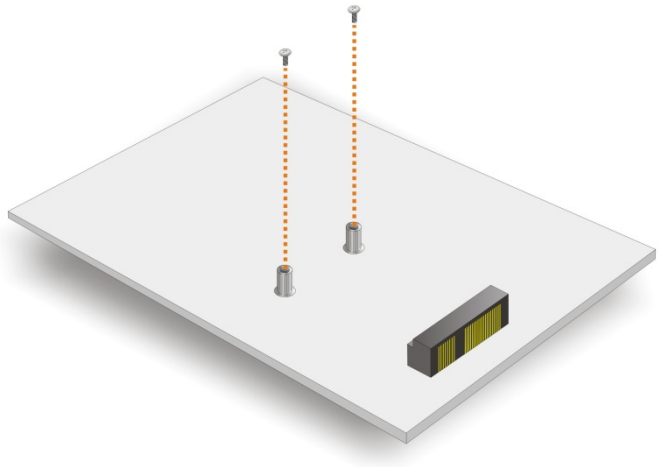


Figure 4-9: Remove the Retention Screws for the mSATA Card

Step 3: **Insert into the socket at an angle**. Line up the notch on the card with the notch on the connector. Slide the mSATA card into the socket at an angle of about 20º (**Figure 4-10**).

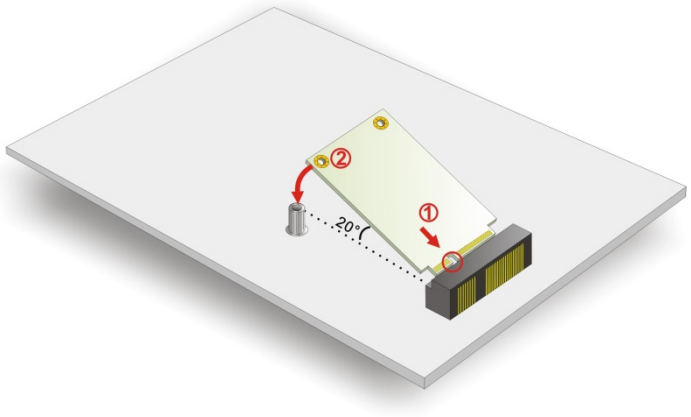


Figure 4-10: Insert the mSATA Card into the Socket at an Angle

Step 4: **Secure the mSATA card**. Secure the mSATA card with the retention screws previously removed (**Figure 4-11**).

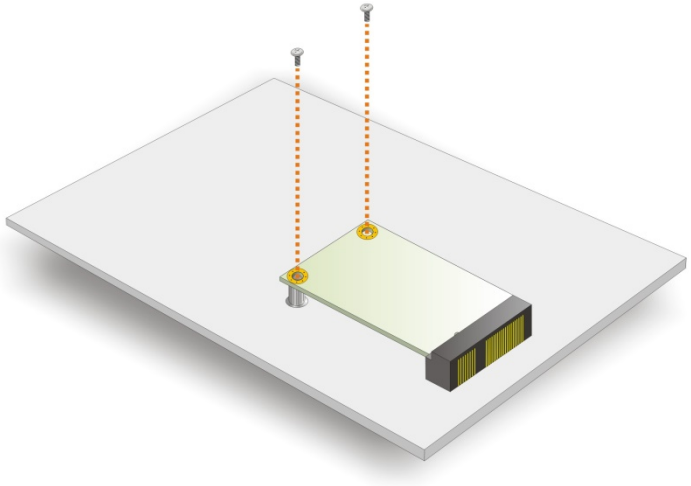


Figure 4-11: Secure the mSATA Card

4.3 System Configuration

The system configuration is controlled by buttons, switches, jumpers and BIOS options. The system configuration must be performed before installation.

4.3.1 AT/ATX Power Mode Setting

The AT and ATX power mode selection is made through the AT/ATX power mode switch which is shown in **Figure 4-12**.

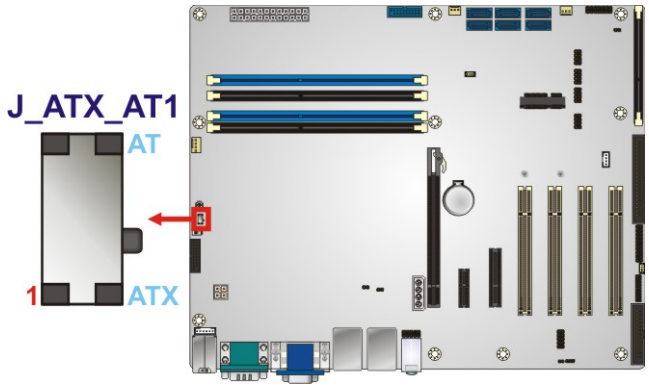


Figure 4-12: AT/ATX Power Mode Switch Location

4.3.2 Clear CMOS Button

To reset the BIOS, remove the on-board battery and press the clear CMOS button for three seconds or more. The clear CMOS button location is shown in **Figure 4-13**.

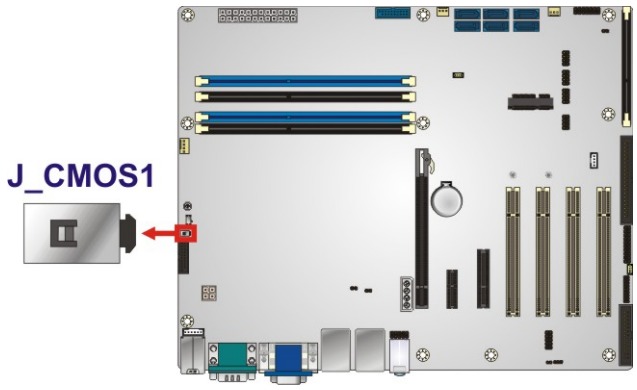


Figure 4-13: Clear CMOS Button Location

4.3.3 Flash Descriptor Security Override

The Flash Descriptor Security Override jumper (p=2.0 mm) specifies whether to override the flash descriptor.

Setting	Description
Short 1-2	No override (Default)
Short 2-3	Override

Table 4-1: Flash Descriptor Security Override Jumper Settings

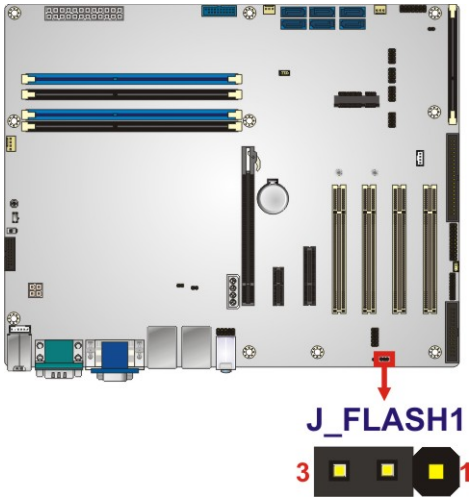


Figure 4-14: Flash Descriptor Security Override Jumper Location

4.3.4 mSATA Slot Setup

The mSATA Slot Setup jumper (p=2.0 mm) specifies whether to automatically detect the mSATA device installed in the mSATA card slot (CN2). If the user shorts the mSATA Slot Setup jumper to force the system to enable mSATA device, the S_ATA6 connector will be disabled.

Setting	Description
Open	Automatically detect mSATA device (Default)
Short	Force to enable mSATA device (The S_ATA6 connector will be disabled)

Table 4-2: mSATA Slot Setup Jumper Settings

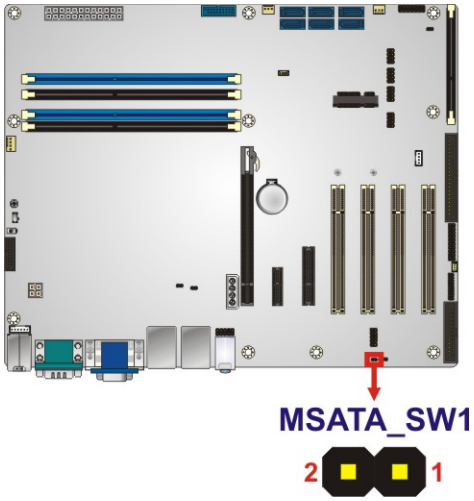


Figure 4-15: mSATA Slot Setup Jumper Location

4.3.5 PCIe x16 Interface Setup

The PCIe x16 interface setup is made through the BIOS options in "Chipset → PCH-IO Configuration" BIOS menu. Use the **PCIEX16 Power** option to configure the PCIe x16 channel mode.

BIOS Options	Description
1 x16 PCIE	Sets the PCIe x16 slot as one PCIe x16. (Default)

Table 4-3: PCIe x16 Interface Setup

Please refer to **Section 5.4.1** for detailed information.

4.3.6 USB Power Select

The USB power selection is made through the BIOS options in "Chipset → PCH-IO Configuration" BIOS menu. Use the **USB SW1 Power** and the **USB SW2 Power** BIOS options to configure the power source to the corresponding USB ports (see **Table 4-4**).

BIOS Options	Configured USB Ports
USB SW1 Power	K/M_USB1 (external USB 2.0 ports) LAN1_USB1 (external USB 3.1 Gen 1 ports)

BIOS Options	Configured USB Ports
USB SW2 Power	LAN2_USB2 (external USB 2.0 ports) USB1 (internal USB 2.0 ports) USB2 (internal USB 2.0 ports) CN4 (internal USB 3.1 Gen 1 ports)

Table 4-4: BIOS Options and Configured USB Ports

Please refer to **Section 5.4.1** for detailed information.

4.4 Internal Peripheral Device Connections

This section outlines the installation of peripheral devices to the onboard connectors.

4.4.1 SATA Drive Connection

The IMBA-Q870-i2 is shipped with two SATA drive cables. To connect the SATA drives to the connectors, please follow the steps below.

Step 1: **Locate the connectors**. The locations of the SATA drive connectors are shown in **Chapter 3**.

Step 2: **Insert the cable connector**. Insert the cable connector into the on-board SATA drive connector. See **Figure 4-16**.

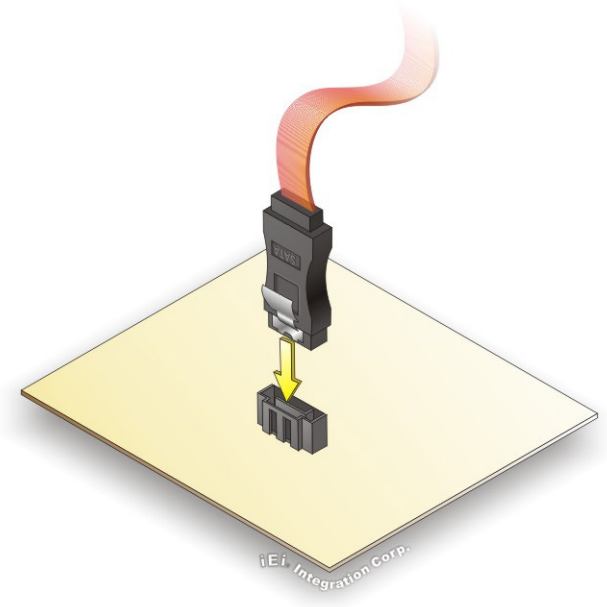


Figure 4-16: SATA Drive Cable Connection

Step 3: **Connect the cable to the SATA disk**. Connect the connector on the other end of the cable to the connector at the back of the SATA drive. See **Figure 4-17**.

Step 4: **Connect the SATA power cable (optional)**. Connect the SATA power connector to the back of the SATA drive. See **Figure 4-17**.

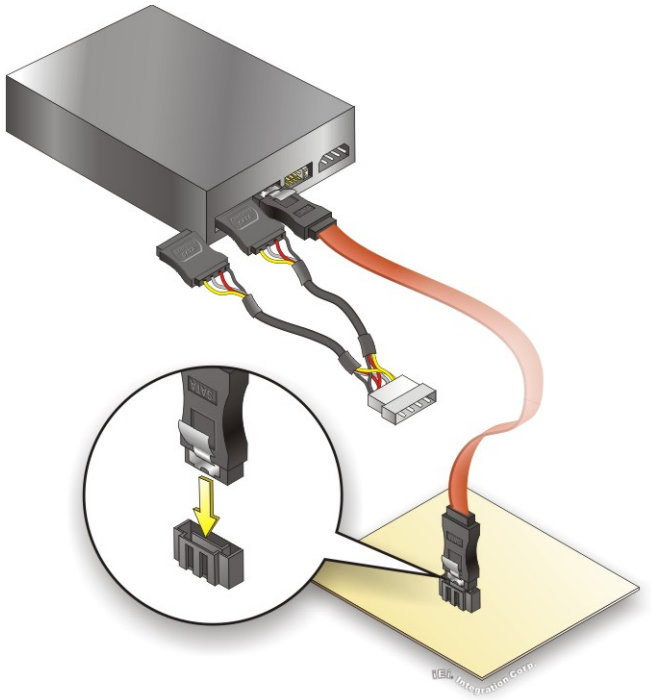


Figure 4-17: SATA Power Drive Connection

The SATA power cable can be bought from IEI. See Optional Items in Section 2.4.

4.5 Intel® AMT Setup Procedure

The IMBA-Q870-i2 is featured with the Intel® Active Management Technology (AMT). To enable the Intel® AMT function, follow the steps below.

Step 1: Make sure at least one of the memory sockets is installed with a DDR3 DIMM.

Step 2: Connect an Ethernet cable to the RJ-45 connector labeled **LAN1**.

Step 3: The AMI BIOS options regarding the Intel® ME or Intel® AMT must be enabled,

Step 4: Properly install the Intel® Management Engine Components drivers by double-clicking the setup file in the **iAMT Driver & Utility → ME_SW** driver folder. See **Chapter 6** for how to download the driver.

Step 5: Configure the Intel® Management Engine BIOS extension (MEBx). To get into the Intel® MEBx settings, press <Ctrl+P> after a single beep during boot-up

process. Enter the Intel® current ME password as it requires (the Intel® default password is **admin**).

NOTE:

To change the password, enter a new password following the strong password rule (containing at least one upper case letter, one lower case letter, one digit and one special character, and be at least eight characters).

4.6 IPMI Setup Procedure

The IMBA-Q870-i2 features Intelligent Platform Management Interface (IPMI) that helps lower the overall costs of server management by enabling users to maximize IT resources, save time and manage multiple systems. The IMBA-Q870-i2 supports IPMI 2.0 through the optional iRIS-2400 module. Follow the steps below to setup IPMI.

4.6.1 Managed System Hardware Setup

The hardware configuration of the managed system (IMBA-Q870-i2) is described below.

Step 1: Install an iRIS-2400 module to the IPMI module socket (refer to **Section 4.2.4**).

Step 2: Make sure at least one DDR3 DIMM is installed in one of the DIMM sockets. If multiple DIMMs are installed, all of the DIMMs must be same size, same speed and same brand to get the best performance.

Step 3: Connect an Ethernet cable to the RJ-45 connector labeled **LAN2_USB2** (**Figure 3-30**).

4.6.2 Using the IEI iMAN Web GUI

To manage a client system from a remote console using IEI iMAN Web GUI, follow the steps below.

Step 1: Obtain the IP address of the managed system. It is recommended to use the IPMI Tool on the managed system to obtain the IP address. To use IPMI Tool to obtain IP address, follow the steps below:

a. Copy the **Ipmitool.exe** file to a bootable USB flash drive.

b. Insert the USB flash drive to the IMBA-Q870-i2

c. The IMBA-Q870-i2 boots from the USB flash drive

d. Enter the following command: **ipmitool 20 30 02 01 03 00 00**

(there is a space between each two-digit number)

e. A serial of number shows. The last four two-digit hexadecimal numbers are the IP address. Convert the hexadecimal numbers to decimal numbers.

Step 2: On the remote management console, open a web browser. Enter the managed system IP address in the web browser (**Figure 4-18**).

Figure 4-18: IEI iMAN Web Address

Step 3: The login page appears in the web browser.

Step 4: Enter the user name and password to login the system. The default login username and password are:

-Username: **admin**

-Password: **admin**

Step 5: Press the login button to login the system.

Step 6: The IEI iMAN Web GUI appears (**Figure 4-19**).

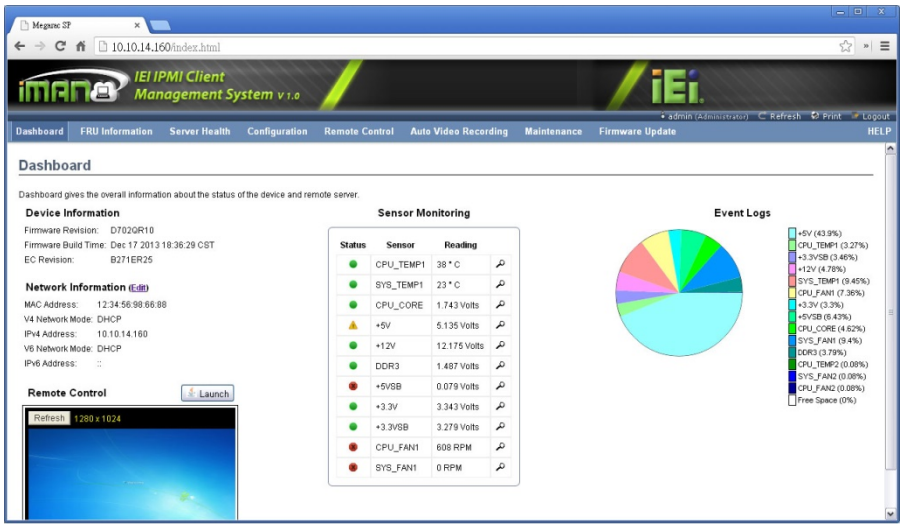


Figure 4-19: IEI iMAN Web GUI

NOTE:

To understand how to use the IEI iMAN Web GUI, please refer to the iRIS-2400 Web GUI user manual which can be downloaded from IEI Resource Download center (see Chapter 6). The user manual describes each function in detail.

Chapter

5

BIOS

5.1 Introduction

The BIOS is programmed onto the BIOS chip. The BIOS setup program allows changes to certain system settings. This chapter outlines the options that can be changed.

NOTE:

Some of the BIOS options may vary throughout the life cycle of the product and are subject to change without prior notice.

5.1.1 Starting Setup

The UEFI BIOS is activated when the computer is turned on. The setup program can be activated in one of two ways.

1. Press the **DEL** or **F2** key as soon as the system is turned on or
2. Press the **DEL** or **F2** key when the "**Press DEL or F2 to enter SETUP**" message appears on the screen.

If the message disappears before the **DEL or F2** key is pressed, restart the computer and try again.

5.1.2 Using Setup

Use the arrow keys to highlight items, press **ENTER** to select, use the PageUp and PageDown keys to change entries, press **F1** for help and press **Esc** to quit. Navigation keys are shown in **Table 5-1**.

Key	Function
Up arrow	Move to previous item
Down arrow	Move to next item
Left arrow	Move to the item on the left hand side
Right arrow	Move to the item on the right hand side

Key	Function
+	Increase the numeric value or make changes
-	Decrease the numeric value or make changes
Esc key	Main Menu – Quit and not save changes into CMOS Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
F1 key	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2 key	Load previous values
F3 key	Load optimized defaults
F4 key	Save changes and Exit BIOS

Table 5-1: BIOS Navigation Keys

When **F1** is pressed a small help window describing the appropriate keys to use and the possible selections for the highlighted item appears. To exit the Help Window press **Esc** or the **F1** key again.

5.1.4 Unable to Reboot after Configuration Changes

If the computer cannot boot after changes to the system configuration is made, CMOS defaults. Use the clear CMOS button described in **Chapter 4**.

5.1.5 BIOS Menu Bar

The **menu bar** on top of the BIOS screen has the following main items:

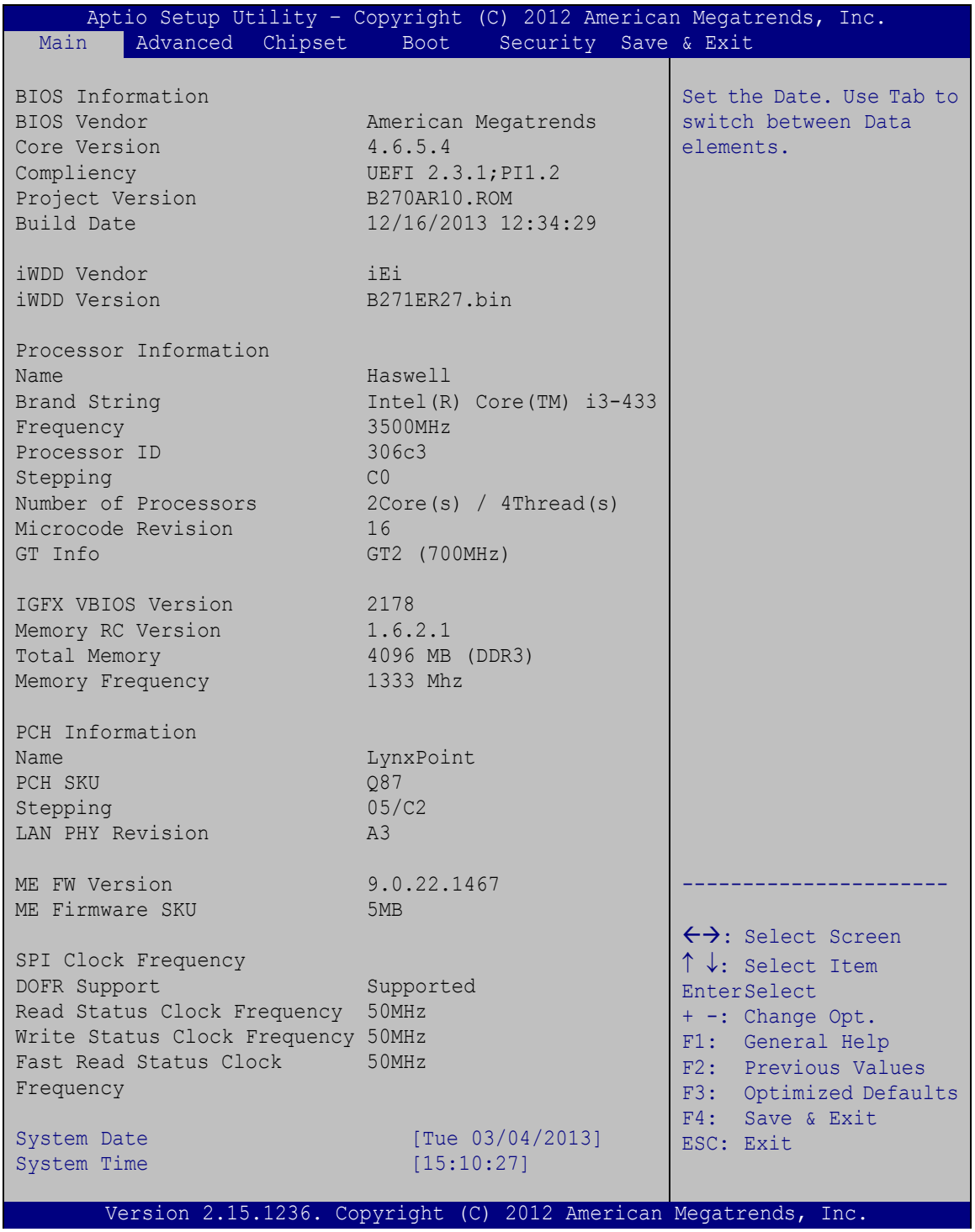
- Main – Changes the basic system configuration.
- Advanced – Changes the advanced system settings.
- Chipset – Changes the chipset settings.
- Boot – Changes the system boot configuration.

- Security – Sets User and Supervisor Passwords.
- Save & Exit – Selects exit options and loads default settings

The following sections completely describe the configuration options found in the menu items at the top of the BIOS screen and listed above.

5.2 Main

The **Main** BIOS menu (**BIOS Menu 1**) appears when the **BIOS Setup** program is entered. The **Main** menu gives an overview of the basic system information.



BIOS Menu 1: Main

The System Overview field has two user configurable fields:

➔ **System Date [xx/xx/xx]**

Use the **System Date** option to set the system date. Manually enter the day, month and year.

➔ **System Time [xx:xx:xx]**

Use the **System Time** option to set the system time. Manually enter the hours, minutes and seconds.

5.3 Advanced

Use the **Advanced** menu (**BIOS Menu 2**) to configure the CPU and peripheral devices through the following sub-menus:

WARNING!

Setting the wrong values in the sections below may cause the system to malfunction. Make sure that the settings made are compatible with the hardware.

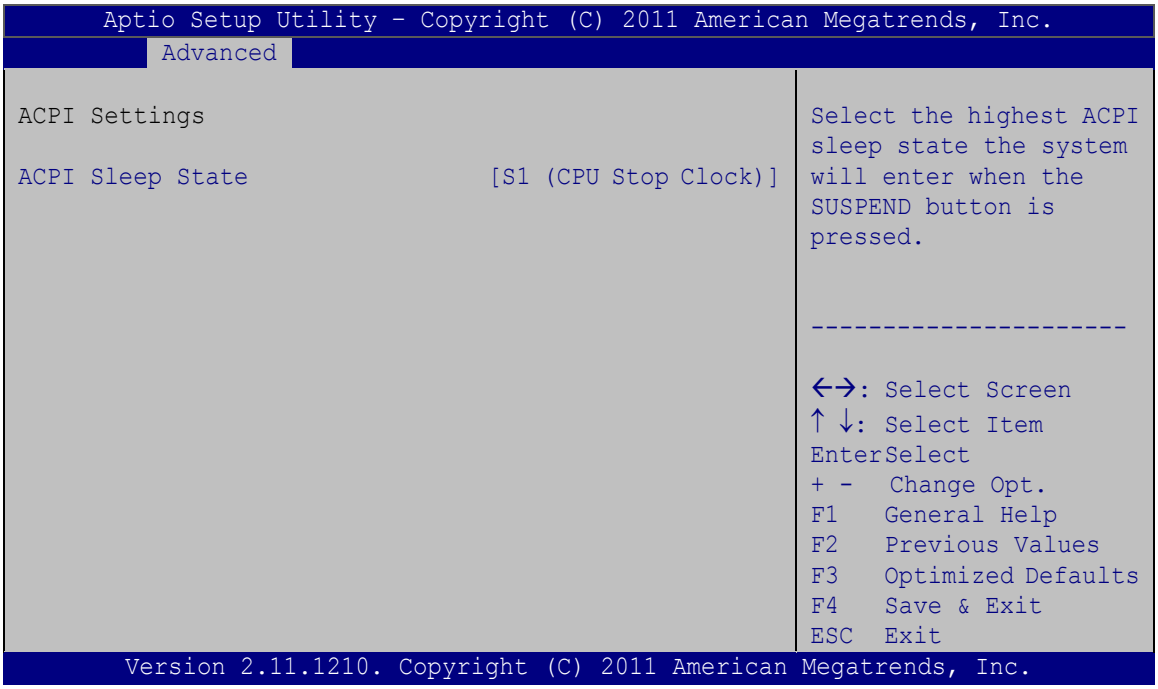
```
        Aptio Setup Utility – Copyright (C) 2012 American Megatrends, Inc.
  Main    Advanced   Chipset    Boot    Security   Save & Exit

> ACPI Settings                                   System ACPI Parameters
> RTC Wake Settings
> Trusted Computing
> CPU Configuration
> SATA Configuration                              ----------------------
> Intel(R) Rapid Start Technology
> AMT Configuration                               ←→: Select Screen
> USB Configuration                               ↑ ↓: Select Item
> F81866 Super IO Configuration                   EnterSelect
> iWDD H/W Monitor                                + -   Change Opt.
> Serial Port Console Configuration               F1    General Help
> iEi Feature                                     F2    Previous Values
                                                  F3    Optimized Defaults
                                                  F4    Save & Exit
                                                  ESC   Exit
        Version 2.15.1236. Copyright (C) 2012 American Megatrends, Inc.
```

BIOS Menu 2: Advanced

5.3.1 ACPI Settings

The **ACPI Settings** menu (**BIOS Menu 3**) configures the Advanced Configuration and Power Interface (ACPI) options.



BIOS Menu 3: ACPI Configuration

➔ **ACPI Sleep State [S1 (CPU Stop Clock)]**

Use the **ACPI Sleep State** option to specify the sleep state the system enters when it is not being used.

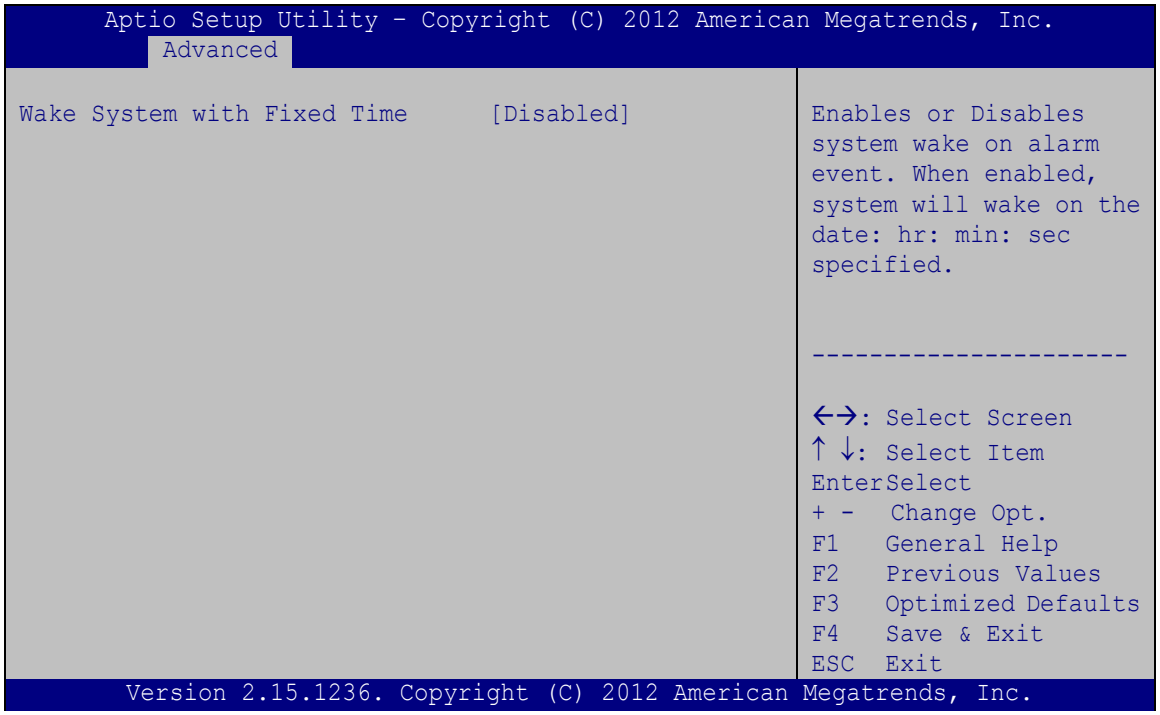
➔ **Suspend Disabled**

➔ **S1 (CPU Stop Clock)** **DEFAULT** The system enters S1(POS) sleep state. The system appears off. The CPU is stopped; RAM is refreshed; the system is running in a low power mode.

➔ **S3 (Suspend to RAM)** The caches are flushed and the CPU is powered off. Power to the RAM is maintained. The computer returns slower to a working state, but more power is saved.

5.3.2 RTC Wake Settings

The **RTC Wake Settings** menu (**BIOS Menu 4**) configures RTC wake event. The RTC wake function is supported in ACPI (S3/S4/S5) and APM soft off modes.



BIOS Menu 4: RTC Wake Settings

➔ **Wake System with Fixed Time [Disabled]**

Use the **Wake System with Fixed Time** option to specify the time the system should be roused from a suspended state.

➔ **Disabled** **DEFAULT** The real time clock (RTC) cannot generate a wake event

➔ **Enabled**

If selected, the following appears with values that can be selected:

*Wake up every day

*Wake up date

*Wake up hour

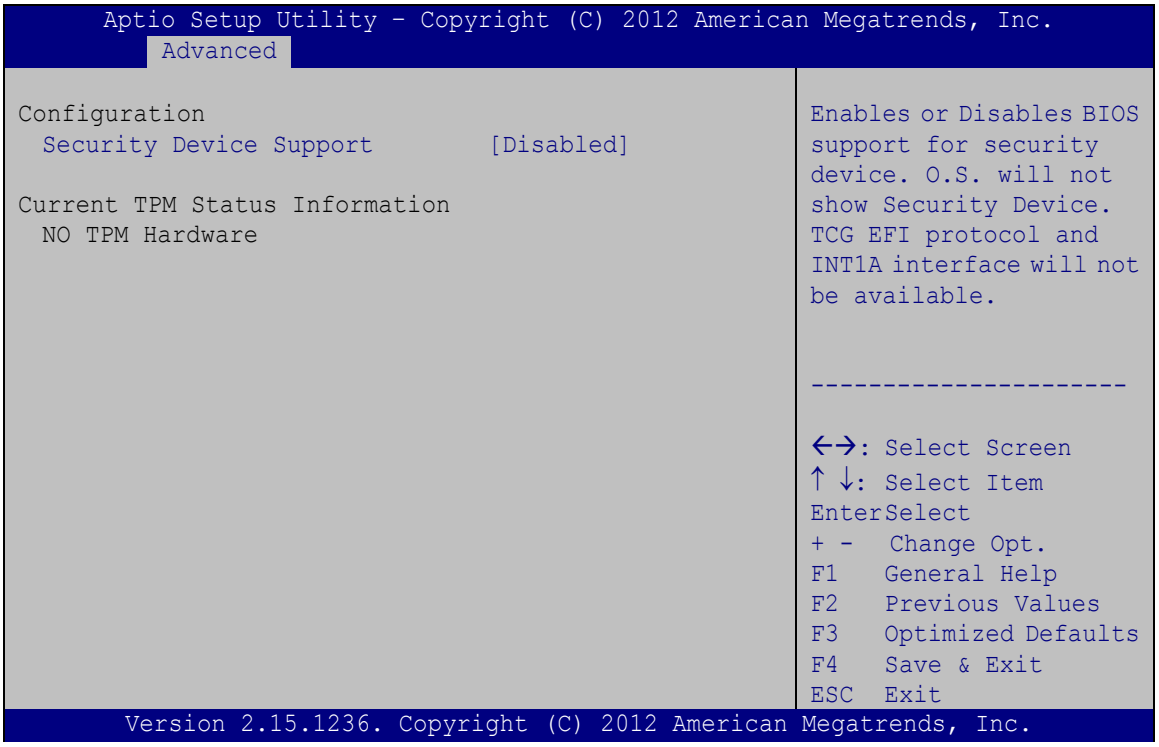
*Wake up minute

*Wake up second

After setting the alarm, the computer turns itself on from a suspend state when the alarm goes off.

5.3.3 Trusted Computing

Use the **Trusted Computing** menu (**BIOS Menu 5**) to configure settings related to the Trusted Computing Group (TCG) Trusted Platform Module (TPM).



BIOS Menu 5: Trusted Computing

➔ **Security Device Support [Disable]**

Use the **Security Device Support** option to configure support for security devices.

➔ **Disable** **DEFAULT** Security Device support is disabled.

➔ **Enable** Security Device support is enabled.

5.3.4 CPU Information

Use the **CPU Information** submenu (**BIOS Menu 6**) to view detailed CPU specifications and configure the CPU.

```
      Aptio Setup Utility – Copyright (C) 2012 American Megatrends, Inc.
          Advanced
 CPU Information                                  | Enable for Windows XP and
                                                  | Linux (OS optimized for
 Intel® COR(TM) i3-4330 CPU @ 3.50GHz             | Hyper-Threading
 Signature                        306c3           | Technology and Disabled
 Microcode Patch                  16              | for other OS (OS not
 Max CPU Speed                    3500 MHz        | optimized for
 Min CPU Speed                    800 MHz         | Hyper-Threading
 CPU Speed                        3500 MHz        | Technology). When
 Processor Cores                  2               | Disabled only one thread
 Intel HT Technology              Supported       | per enabled core is
 Intel VT-x Technology            Supported       | enabled.
 Intel SMX Technology             Not Supported   |
 64-bit                           Supported       |
 EIST Technology                  Supported       | ----------------------
                                                  |
 L1 Data ache                     32 kB x 2       | ←→: Select Screen
 L1 Code Cache                    32 kB x 2       | ↑ ↓: Select Item
 L2 Cache                         256 kB x 2      | EnterSelect
 L3 Cache                         4096 kB         | + -   Change Opt.
                                                  | F1    General Help
 Hyper-Threading                  [Enabled]       | F2    Previous Values
 Active Processor Cores           [All]           | F3    Optimized Defaults
 Intel Virtualization Technology  [Disabled]      | F4    Save & Exit
 EIST                             [Enabled]       | ESC   Exit
      Version 2.15.1236. Copyright (C) 2012 American Megatrends, Inc.
```

BIOS Menu 6: CPU Information

The CPU Configuration menu (**BIOS Menu 6**) lists the following CPU details:

- Processor Type: Lists the brand name of the CPU being used
- CPU Signature: Lists the CPU signature value.

- Microcode Patch: Lists the microcode patch being used.
- Max CPU Speed: Lists the maximum CPU processing speed.
- Min CPU Speed: Lists the minimum CPU processing speed.
- CPU Speed: Lists the CPU processing speed.
- Processor Cores: Lists the number of the processor core
- Intel HT Technology: Indicates if Intel HT Technology is supported by the CPU.
- Intel VT-x Technology: Indicates if Intel VT-x Technology is supported by the CPU.
- Intel SMX Technology: Indicates if Intel SMX Technology is supported by the CPU.
- EIST Technology: Indicates if Enhanced Intel SpeedStep® Techonology is supported by the CPU.
- L1 Data Cache: Lists the amount of data storage space on the L1 cache.
- L1 Code Cache: Lists the amount of code storage space on the L1 cache.
- L2 Cache: Lists the amount of storage space on the L2 cache.
- L3 Cache: Lists the amount of storage space on the L3 cache.

➔ **Hyper-threading [Enabled]**

Use the **Hyper-threading** BIOS option to enable or disable the Intel Hyper-Threading Technology.

➔	**Disabled**		Disables the Intel Hyper-Threading Technology.
➔	**Enabled**	**DEFAULT**	Enables the Intel Hyper-Threading Technology.

➔ **Active Processor Cores [All]**

Use the **Active Processor Cores** BIOS option to enable numbers of cores in the processor package.

➔	**All**	**DEFAULT**	Enable all cores in the processor package.
➔	**1**		Enable one core in the processor package.

➔ **Intel Virtualization Technology [Disabled]**

Use the **Intel Virtualization Technology** option to enable or disable virtualization on the system. When combined with third party software, Intel® Virtualization technology allows several OSs to run on the same system at the same time.

➔	**Disabled**	**DEFAULT**	Disables Intel Virtualization Technology.
➔	**Enabled**		Enables Intel Virtualization Technology.

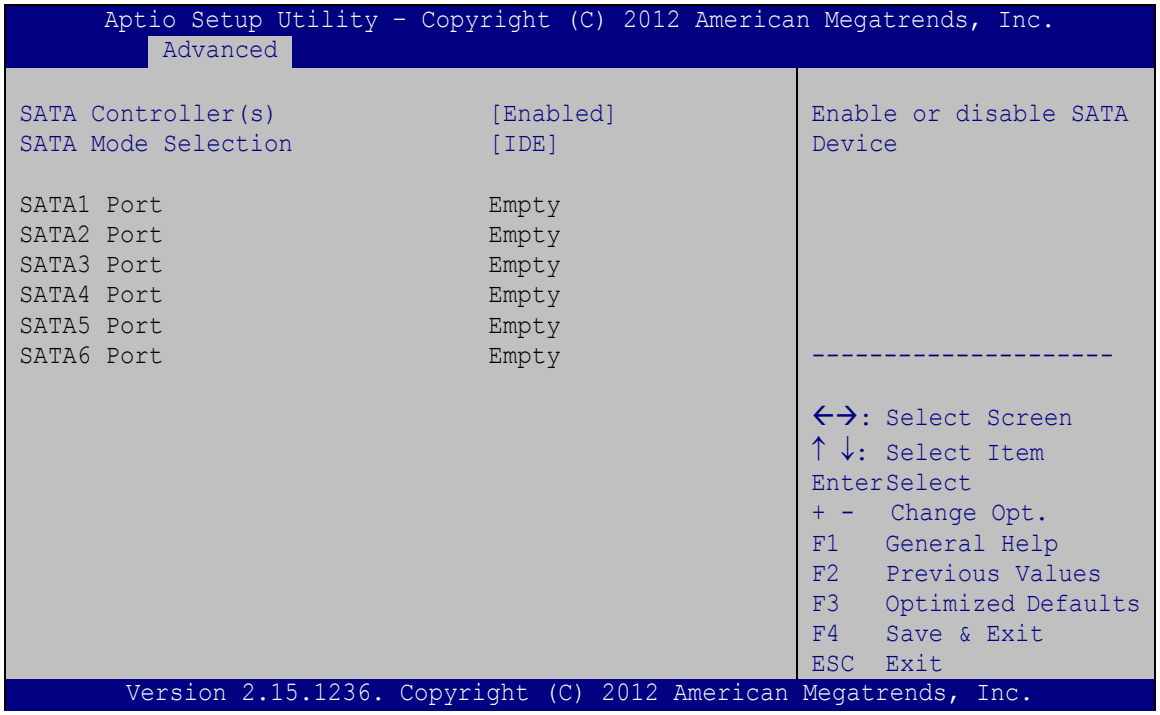
➔ **EIST [Enabled]**

Use the **EIST** option to enable or disable Enhanced Intel SpeedStep® Techonology (EIST).

➔	**Disabled**		Disables Enhanced Intel SpeedStep® Techonology.
➔	**Enabled**	**DEFAULT**	Enables Enhanced Intel SpeedStep® Techonology.

5.3.5 SATA Configuration

Use the **SATA Configuration** menu (**BIOS Menu 7**) to change and/or set the configuration of the SATA devices installed in the system.



BIOS Menu 7: SATA Configuration

➔ **SATA Controller(s) [Enabled]**

Use the **SATA Controller(s)** option to enable or disable the serial ATA controller.

- ➔ **Enabled** **DEFAULT** Enables the on-board SATA controller.
- ➔ **Disabled** Disables the on-board SATA controller.

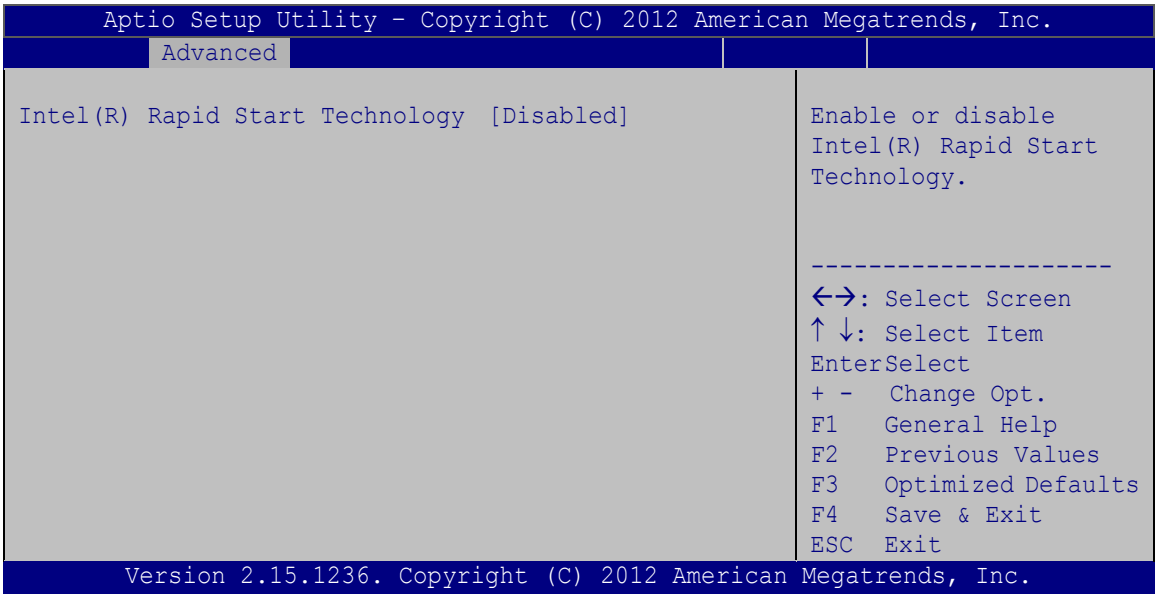
➔ **SATA Mode Selection [IDE]**

Use the **SATA Mode Selection** option to configure SATA devices as normal IDE devices.

- ➔ **IDE** **DEFAULT** Configures SATA devices as normal IDE device.
- ➔ **AHCI** Configures SATA devices as AHCI device.
- ➔ **RAID** Configures SATA devices as RAID device.

5.3.6 Intel(R) Rapid Start Technology

Use the **Intel(R) Rapid Start Technology** menu to configure Intel® Rapid Start Technology support.



BIOS Menu 8: Intel(R) Rapid Start Technology

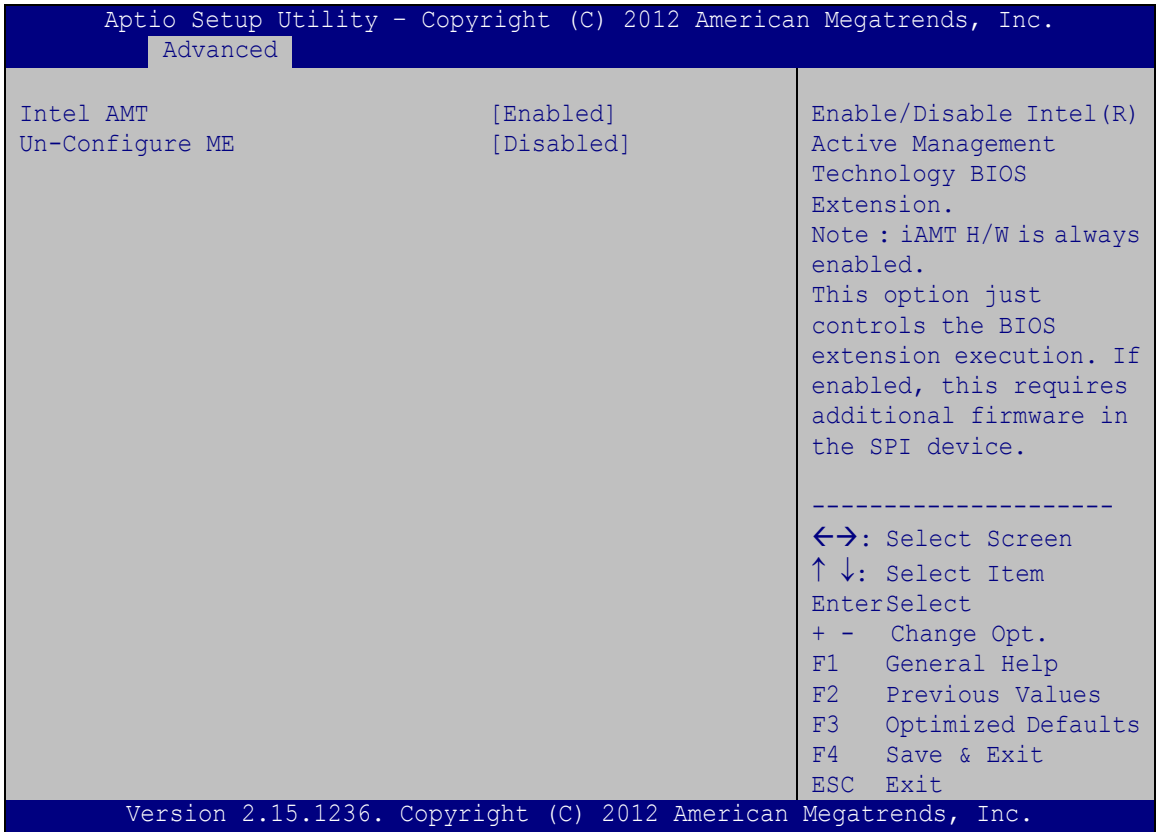
➔ **Intel(R) Rapid Start Technology [Disabled]**

Use **Intel(R) Rapid Start Technology** option to enable or disable the Intel® Rapid Start Technology function.

➔	**Disabled**	**DEFAULT**	Intel® Rapid Start Technology is disabled
➔	**Enabled**		Intel® Rapid Start Technology is enabled

5.3.7 AMT Configuration

The **AMT Configuration** submenu (**BIOS Menu 9**) allows Intel® Active Management Technology (AMT) options to be configured.



BIOS Menu 9: AMT Configuration

➔ **Intel AMT [Enabled]**

Use **Intel AMT** option to enable or disable the Intel® AMT BIOS Extension.

➔ **Disabled** Intel® AMT BIOS Extension is disabled

➔ **Enabled** **DEFAULT** Intel® AMT BIOS Extension is enabled

➔ **Unconfigure ME [Disabled]**

Use the **Unconfigure ME** option to perform ME unconfigure without password operation.

➔ **Disabled** **DEFAULT** Disable ME unconfigure

➔ **Enabled** Enable ME unconfigure

5.3.8 USB Configuration

Use the **USB Configuration** menu (**BIOS Menu 10**) to read USB configuration information and configure the USB settings.

BIOS Menu 10: USB Configuration

➔ **USB Devices**

The **USB Devices Enabled** field lists the USB devices that are enabled on the system

➔ **Legacy USB Support [Enabled]**

Use the **Legacy USB Support** BIOS option to enable USB mouse and USB keyboard support. Normally if this option is not enabled, any attached USB mouse or USB keyboard does not become available until a USB compatible operating system is fully booted with all USB drivers loaded. When this option is enabled, any attached USB mouse or USB keyboard can control the system even when there is no USB driver loaded onto the system.

➔	**Enabled**	**DEFAULT**	Legacy USB support enabled
➔	**Disabled**		Legacy USB support disabled
➔	**Auto**		Legacy USB support disabled if no USB devices are connected

5.3.9 F81866 Super IO Configuration

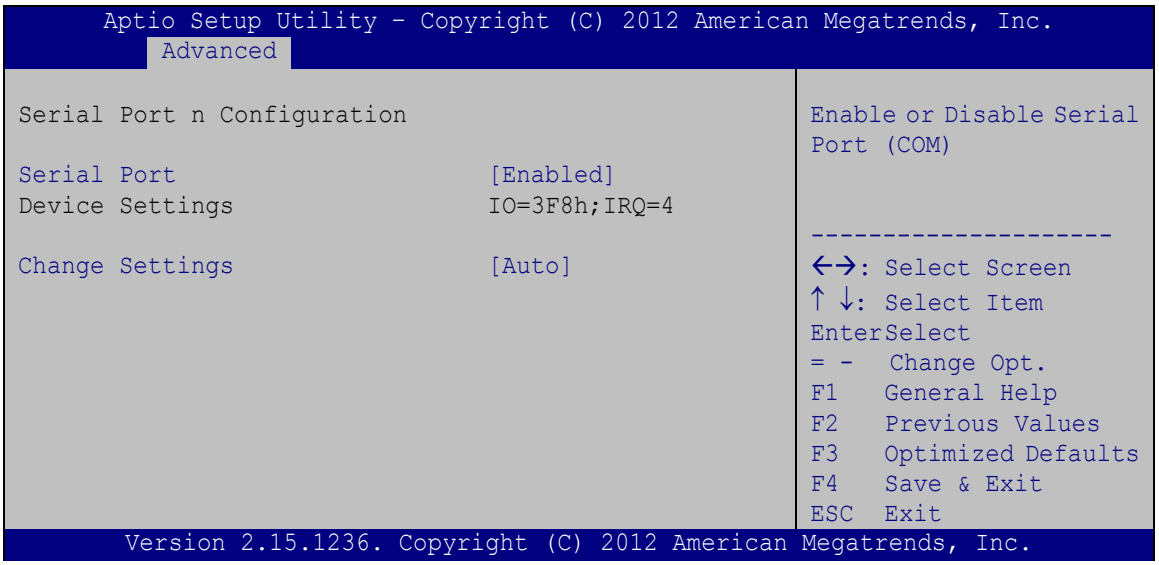
Use the **F81866 Super IO Configuration** menu (**BIOS Menu 11**) to set or change the configurations for the FDD controllers, parallel ports and serial ports.

```
      Aptio Setup Utility – Copyright (C) 2012 American Megatrends, Inc.
          Advanced
 F81866 Super IO Configuration                  | Set Parameters of Serial
                                                | Port 1 (COMA)
 F81866 Super IO Chip              F81866       |
 > Serial Port 1 Configuration                  |
 > Serial Port 2 Configuration                  | ---------------------
 > Serial Port 3 Configuration                  | ←→: Select Screen
 > Serial Port 4 Configuration                  | ↑ ↓: Select Item
 > Serial Port 5 Configuration                  | EnterSelect
 > Serial Port 6 Configuration                  | + -   Change Opt.
 > Parallel Port Configuration                  | F1    General Help
                                                | F2    Previous Values
                                                | F3    Optimized Defaults
                                                | F4    Save & Exit
                                                | ESC   Exit
      Version 2.15.1236. Copyright (C) 2012 American Megatrends, Inc.
```

BIOS Menu 11: F81866 Super IO Configuration

5.3.9.1 Serial Port n Configuration

Use the **Serial Port n Configuration** menu (**BIOS Menu 12**) to configure the serial port n.



BIOS Menu 12: Serial Port n Configuration Menu

5.3.9.1.1 Serial Port 1 Configuration

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

➔ **Disabled**		Disable the serial port
➔ **Enabled**	**DEFAULT**	Enable the serial port

➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

➔ **Auto**	**DEFAULT**	The serial port IO port address and interrupt address are automatically detected.
➔ **IO=3F8h; IRQ=4**		Serial Port I/O port address is 3F8h and the interrupt address is IRQ4

- **IO=3F8h; IRQ=3, 4** — Serial Port I/O port address is 3F8h and the interrupt address is IRQ3,4
- **IO=2C0h; IRQ=3, 4** — Serial Port I/O port address is 2C0h and the interrupt address is IRQ3, 4
- **IO=2C8h; IRQ=3, 4** — Serial Port I/O port address is 2C8h and the interrupt address is IRQ3, 4

5.3.9.1.2 Serial Port 2 Configuration

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

- **Disabled** — Disable the serial port
- **Enabled** **DEFAULT** — Enable the serial port

➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

- **Auto** **DEFAULT** — The serial port IO port address and interrupt address are automatically detected.
- **IO=2F8h; IRQ=3** — Serial Port I/O port address is 2F8h and the interrupt address is IRQ3
- **IO=3F8h; IRQ=3, 4** — Serial Port I/O port address is 3F8h and the interrupt address is IRQ3, 4
- **IO=2F8h; IRQ=3, 4** — Serial Port I/O port address is 2F8h and the interrupt address is IRQ3, 4
- **IO=2C0h; IRQ=3, 4** — Serial Port I/O port address is 2C0h and the interrupt address is IRQ3, 4
- **IO=2C8h; IRQ=3, 4** — Serial Port I/O port address is 2C8h and the interrupt address is IRQ3, 4

➔ **Device Mode [RS422/485]**

Use the **Device Mode** option to configure the COM2 serial port.

➔ **RS422/485**	**DEFAULT**	Enables serial port RS422/485 support.

5.3.9.1.3 Serial Port 3 Configuration

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

➔ **Disabled**		Disable the serial port
➔ **Enabled**	**DEFAULT**	Enable the serial port

➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

➔ **Auto**	**DEFAULT**	The serial port IO port address and interrupt address are automatically detected.
➔ **IO=2D0h; IRQ=10**		Serial Port I/O port address is 2D0h and the interrupt address is IRQ10
➔ **IO=2D0h; IRQ=10, 11**		Serial Port I/O port address is 2D0h and the interrupt address is IRQ10, 11
➔ **IO=2E8h; IRQ=10, 11**		Serial Port I/O port address is 2E8h and the interrupt address is IRQ10, 11
➔ **IO=2D8h; IRQ=10, 11**		Serial Port I/O port address is 2D8h and the interrupt address is IRQ10, 11

5.3.9.1.4 Serial Port 4 Configuration

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

➔ **Disabled**		Disable the serial port
➔ **Enabled**	**DEFAULT**	Enable the serial port

➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

➔ **Auto**	**DEFAULT**	The serial port IO port address and interrupt address are automatically detected.
➔ **IO=2E8h; IRQ=10**		Serial Port I/O port address is 2E8h and the interrupt address is IRQ10
➔ **IO=3E8h; IRQ=10, 11**		Serial Port I/O port address is 3E8h and the interrupt address is IRQ10, 11
➔ **IO=2E8h; IRQ=10, 11**		Serial Port I/O port address is 2E8h and the interrupt address is IRQ10, 11
➔ **IO=2D0h; IRQ=10, 11**		Serial Port I/O port address is 2D0h and the interrupt address is IRQ10, 11
➔ **IO=2D8h; IRQ=10, 11**		Serial Port I/O port address is 2D8h and the interrupt address is IRQ10, 11

➔ **Device Mode [RS422/485]**

Use the **Device Mode** option to configure the serial port 4.

➔ **RS422/485**	**DEFAULT**	COM4 is configured as RS-422/485 serial port.

5.3.9.1.5 Serial Port 5 Configuration

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

➔ **Disabled**		Disable the serial port

➔ **Enabled** **DEFAULT** Enable the serial port

➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

➔ **Auto** **DEFAULT** The serial port IO port address and interrupt address are automatically detected.

➔ **IO=2D0h; IRQ=10** Serial Port I/O port address is 2D0h and the interrupt address is IRQ10

➔ **IO=2C0h; IRQ=10, 11** Serial Port I/O port address is 2C0h and the interrupt address is IRQ10, 11

➔ **IO=2C8h; IRQ=10, 11** Serial Port I/O port address is 2C8h and the interrupt address is IRQ10, 11

➔ **IO=2D0h; IRQ=10, 11** Serial Port I/O port address is 2D0h and the interrupt address is IRQ10, 11

➔ **IO=2D8h; IRQ=10, 11** Serial Port I/O port address is 2D8h and the interrupt address is IRQ10, 11

➔ **IO=2E0h; IRQ=10, 11** Serial Port I/O port address is 2E0h and the interrupt address is IRQ10, 11

5.3.9.1.6 Serial Port 6 Configuration

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

➔ **Disabled** Disable the serial port

➔ **Enabled** **DEFAULT** Enable the serial port

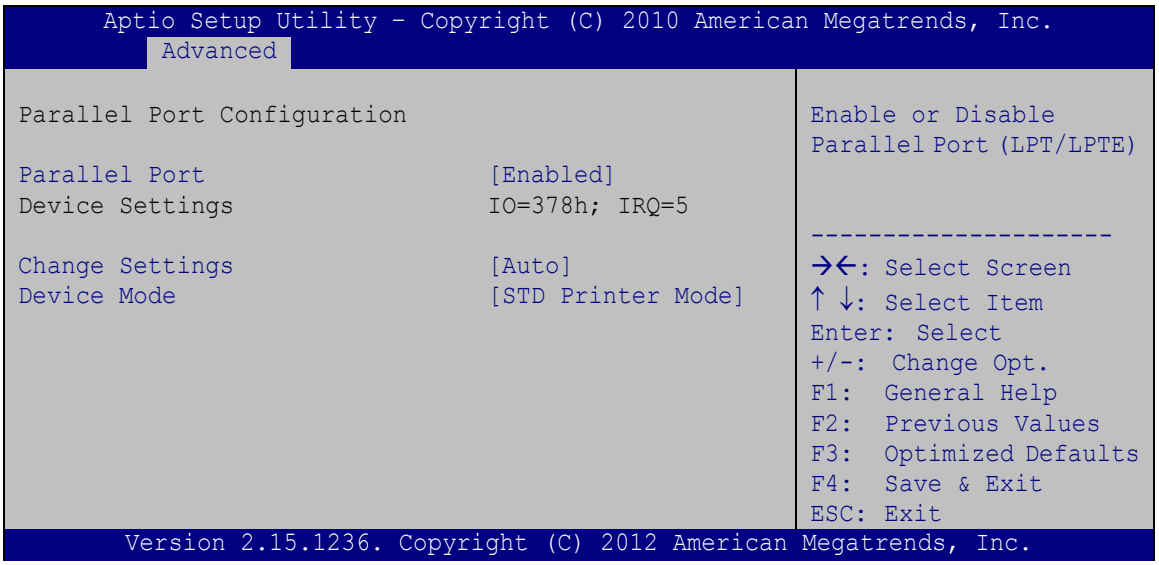
➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

➔ **Auto**	**DEFAULT**	The serial port IO port address and interrupt address are automatically detected.
➔ **IO=2D8h; IRQ=10**		Serial Port I/O port address is 2D8h and the interrupt address is IRQ10
➔ **IO=2C0h; IRQ=10, 11**		Serial Port I/O port address is 2C0h and the interrupt address is IRQ10, 11
➔ **IO=2C8h; IRQ=10, 11**		Serial Port I/O port address is 2C8h and the interrupt address is IRQ10, 11
➔ **IO=2D0h; IRQ=10, 11**		Serial Port I/O port address is 2D0h and the interrupt address is IRQ10, 11
➔ **IO=2D8h; IRQ=10, 11**		Serial Port I/O port address is 2D8h and the interrupt address is IRQ10, 11
➔ **IO=2E0h; IRQ=10, 11**		Serial Port I/O port address is 2E0h and the interrupt address is IRQ10, 11

5.3.9.2 Parallel Port Configuration

Use the **Parallel Port Configuration** menu (**BIOS Menu 13**) to configure the serial port n.



BIOS Menu 13: Parallel Port Configuration Menu

➔ **Parallel Port [Enabled]**

Use the **Parallel Port** option to enable or disable the parallel port.

➔ **Disabled**		Disable the parallel port
➔ **Enabled**	**DEFAULT**	Enable the parallel port

➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the parallel port IO port address and interrupt address.

➔ **Auto**	**DEFAULT**	The parallel port IO port address and interrupt address are automatically detected.
➔ **IO=378h; IRQ=5**		Parallel Port I/O port address is 378h and the interrupt address is IRQ5
➔ **IO=378h; IRQ=5, 7**		Parallel Port I/O port address is 378h and the interrupt address is IRQ5, 7

➔ **IO=278h; IRQ=5, 7** — Parallel Port I/O port address is 278h and the interrupt address is IRQ5, 7

➔ **IO=3BCh; IRQ=5, 7** — Parallel Port I/O port address is 3BCh and the interrupt address is IRQ5, 7

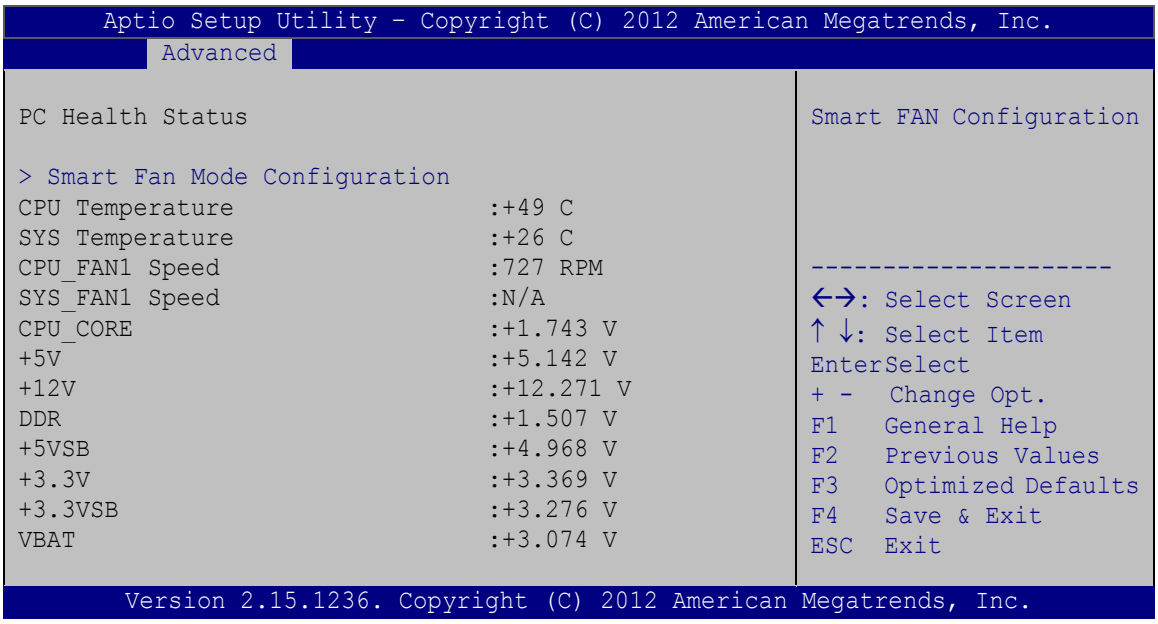
➔ **Device Mode [STD Printer Mode]**

Use the **Device Mode** option to select the mode the parallel port operates in. Configuration options are listed below.

- STD Printer Mode **Default**
- SPP Mode
- EPP-1.9 and SPP Mode
- EPP-1.7 and SPP Mode
- ECP Mode
- ECP and EPP 1.9 Mode
- ECP and EPP 1.7 Mode

5.3.10 iWDD H/W Monitor

The **iWDD H/W Monitor** menu (**BIOS Menu 14**) contains the fan configuration submenus and displays operating temperature, fan speeds and system voltages.



BIOS Menu 14: iWDD H/W Monitor

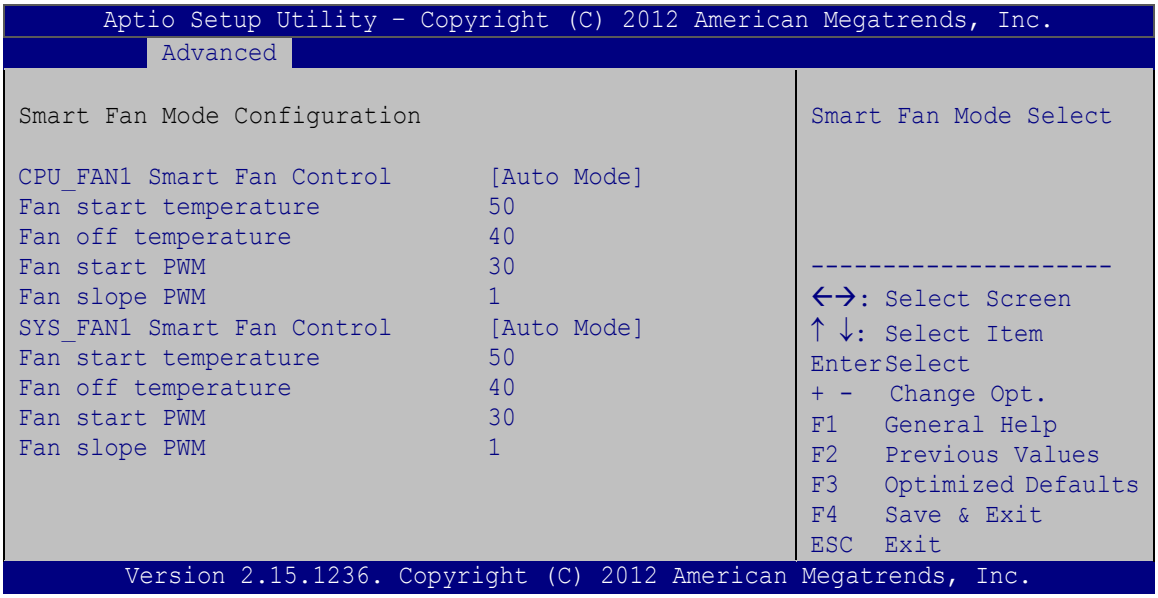
➔ PC Health Status

The following system parameters and values are shown. The system parameters that are monitored are:

- System Temperatures:
  - CPU Temperature
  - System Temperature
- Fan Speeds:
  - CPU Fan Speed
  - System Fan Speed
- Voltages:
  - CPU_CORE
  - +5V
  - +12V
  - DDR
  - +5VSB
  - +3.3V
  - +3.3VSB
  - VBAT

5.3.10.1 Smart Fan Mode Configuration

Use the **Smart Fan Mode Configuration** submenu (**BIOS Menu 15**) to configure smart fan temperature and speed settings.



BIOS Menu 15: Smart Fan Mode Configuration

➔ **CPU_FAN1 Smart Fan Control/SYS_FAN1 Smart Fan Control [Auto Mode]**

Use the **CPU_FAN1 Smart Fan Control/SYS_FAN1 Smart Fan Control** option to configure the CPU/System Smart Fan.

➔ **Auto Mode**	**DEFAULT**	The fan adjusts its speed using Auto Mode settings.
➔ **Manual Mode**		The fan spins at the speed set in Manual Mode settings.

➔ **Fan start/off temperature**

Use the + or – key to change the **Fan start/off temperature** value. Enter a decimal number between 1 and 100.

➔ **Fan start PWM**

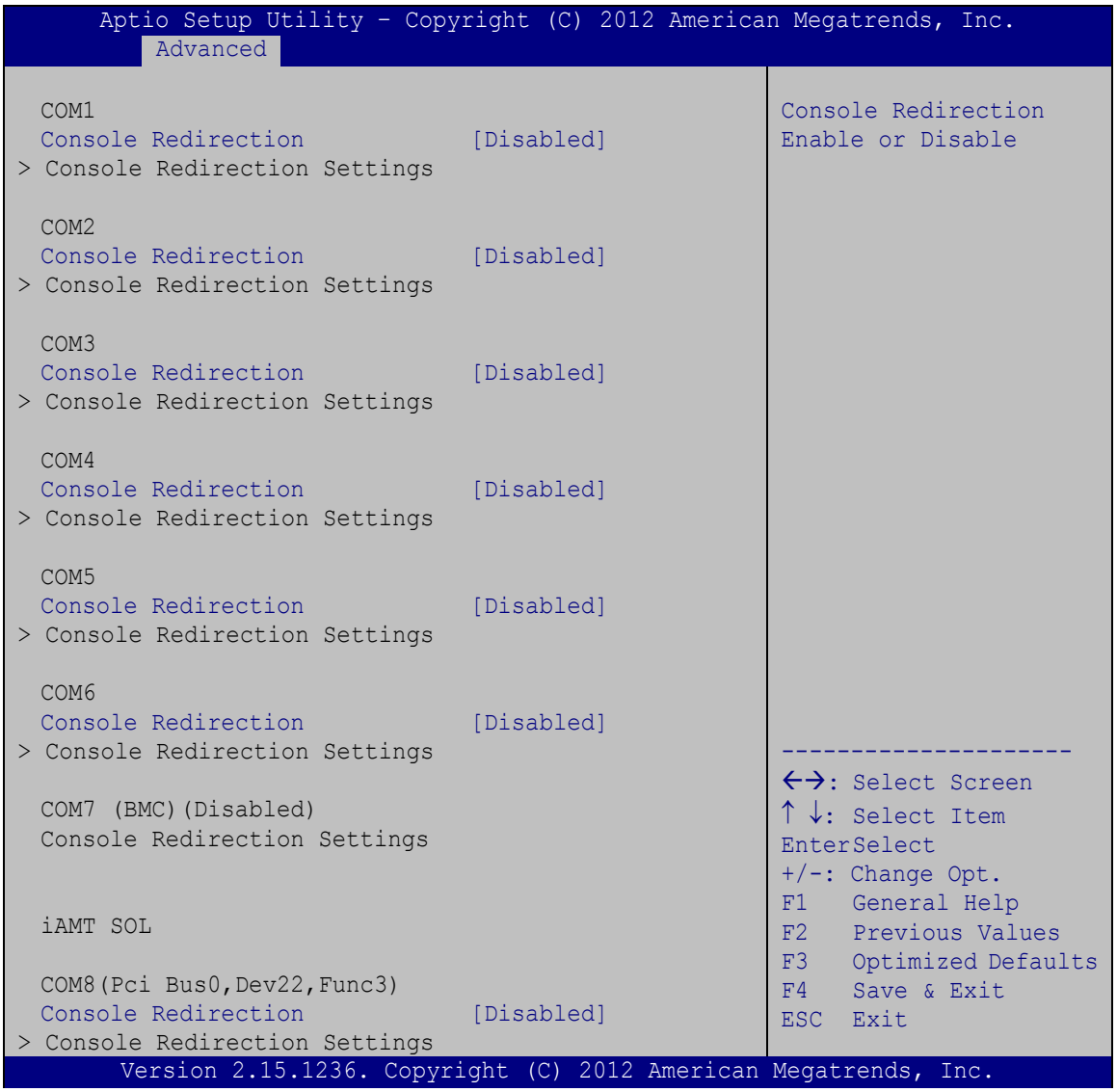
Use the + or – key to change the **Fan start PWM** value. Enter a decimal number between 1 and 128.

➔ **Fan slope PWM**

Use the + or – key to change the **Fan slope PWM** value. Enter a decimal number between 1 and 64.

5.3.11 Serial Port Console Redirection

The **Serial Port Console Redirection** menu (**BIOS Menu 16**) allows the console redirection options to be configured. Console redirection allows users to maintain a system remotely by re-directing keyboard input and text output through the serial port.



BIOS Menu 16: Serial Port Console Redirection

➔ **Console Redirection [Disabled]**

Use **Console Redirection** option to enable or disable the console redirection function.

➔	**Disabled**	**DEFAULT**	Disabled the console redirection function
➔	**Enabled**		Enabled the console redirection function

The following options are available in the **Console Redirection Settings** submenu when the Console Redirection option is enabled.

➔ **Terminal Type [ANSI]**

Use the **Terminal Type** option to specify the remote terminal type.

➔ **VT100**		The target terminal type is VT100
➔ **VT100+**		The target terminal type is VT100+
➔ **VT-UTF8**		The target terminal type is VT-UTF8
➔ **ANSI**	**DEFAULT**	The target terminal type is ANSI

➔ **Bits per second [115200]**

Use the **Bits per second** option to specify the serial port transmission speed. The speed must match the other side. Long or noisy lines may require lower speeds.

➔ **9600**		Sets the serial port transmission speed at 9600.
➔ **19200**		Sets the serial port transmission speed at 19200.
➔ **57600**		Sets the serial port transmission speed at 57600.
➔ **115200**	**DEFAULT**	Sets the serial port transmission speed at 115200.

➔ **Data Bits [8]**

Use the **Data Bits** option to specify the number of data bits.

➔ **7**		Sets the data bits at 7.
➔ **8**	**DEFAULT**	Sets the data bits at 8.

➔ **Parity [None]**

Use the **Parity** option to specify the parity bit that can be sent with the data bits for detecting the transmission errors.

➔ **None**	**DEFAULT**	No parity bit is sent with the data bits.
➔ **Even**		The parity bit is 0 if the number of ones in the data bits is even.

➔ **Odd**		The parity bit is 0 if the number of ones in the data bits is odd.
➔ **Mark**		The parity bit is always 1. This option does not provide error detection.
➔ **Space**		The parity bit is always 0. This option does not provide error detection.

➔ **Stop Bits [1]**

Use the **Stop Bits** option to specify the number of stop bits used to indicate the end of a serial data packet. Communication with slow devices may require more than 1 stop bit.

➔ **1**	**DEFAULT**	Sets the number of stop bits at 1.
➔ **2**		Sets the number of stop bits at 2.

5.3.12 iEi Feature

Use the **iEi Feature** menu (**BIOS Menu 17**) to configure IEI One Key Recovery function.

```
Aptio Setup Utility – Copyright (C) 2012 American Megatrends, Inc.
  Advanced

iEi Feature                                  | Auto Recovery Function
                                             | Reboot and recover
Auto Recovery Function          [Disabled]   | system automatically
                                             | within 10 min, when OS
                                             | crashes. Please install
                                             | Auto Recovery API
                                             | service before enabling
                                             | this function.
                                             |
                                             | ----------------------
                                             | →←: Select Screen
                                             | ↑ ↓: Select Item
                                             | Enter: Select
                                             | +/-: Change Opt.
                                             | F1: General Help
                                             | F2: Previous Values
                                             | F3: Optimized Defaults
                                             | F4: Save & Exit
                                             | ESC: Exit
Version 2.15.1236. Copyright (C) 2012 American Megatrends, Inc.
```

BIOS Menu 17: iEi Feature

➔ **Auto Recovery Function [Disabled]**

Use the **Auto Recovery Function** BIOS option to enable or disable the auto recovery function of the IEI One Key Recovery.

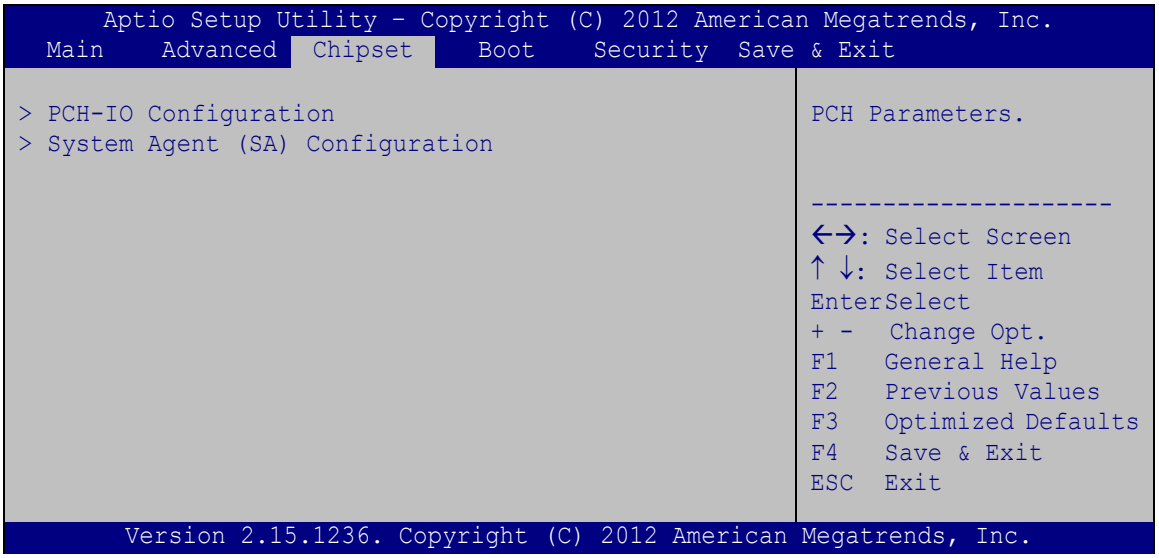
➔ **Disabled**	**DEFAULT**	Auto recovery function disabled
➔ **Enabled**		Auto recovery function enabled

5.4 Chipset

Use the **Chipset** menu (**BIOS Menu 18**) to access the PCH-IO and System Agent (SA) Subsystem configuration menus.

WARNING!

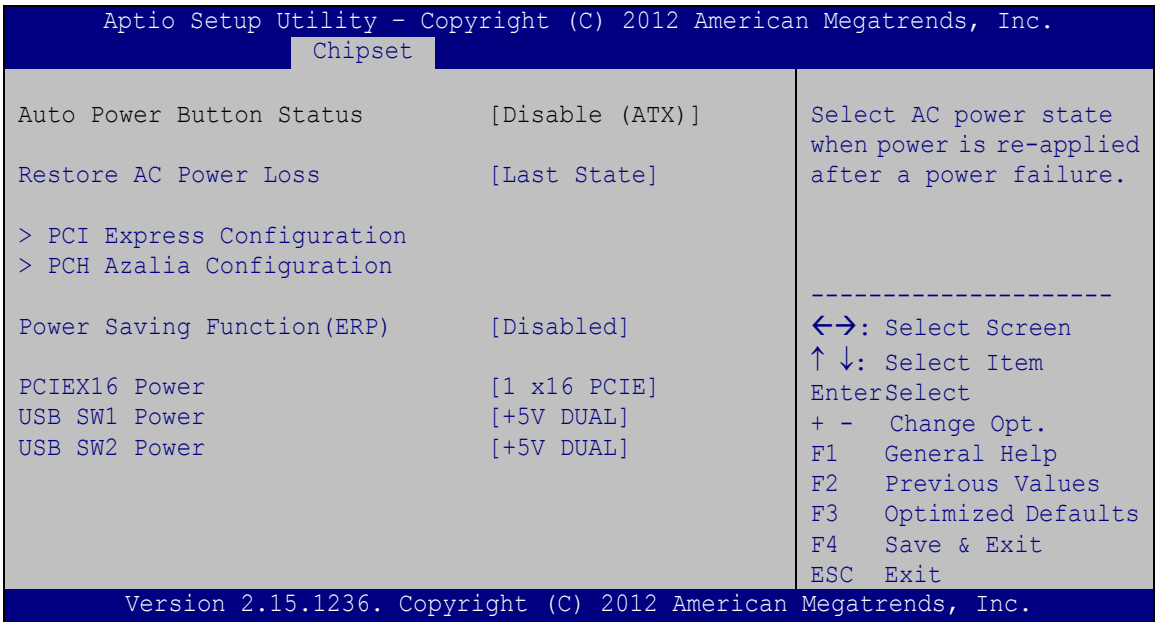
Setting the wrong values for the Chipset BIOS selections in the Chipset BIOS menu may cause the system to malfunction.



BIOS Menu 18: Chipset

5.4.1 PCH-IO Configuration

Use the **PCH-IO Configuration** menu (**BIOS Menu 19**) to configure the PCH chipset.



BIOS Menu 19: PCH-IO Configuration

➔ **Restore on AC Power Loss [Last State]**

Use the **Restore on AC Power Loss** BIOS option to specify what state the system returns to if there is a sudden loss of power to the system.

➔ **Power Off**		The system remains turned off
➔ **Power On**		The system turns on
➔ **Last State**	**DEFAULT**	The system returns to its previous state. If it was on, it turns itself on. If it was off, it remains off.

➔ **Power Saving Function [Disabled]**

Use the **Power Saving Function** BIOS option to enable or reduce power consumption in the S5 state. When enabled, the system can only be powered-up using the power button.

➔ **Disabled**	**DEFAULT**	Power Saving Function support disabled

➔ **Enabled** Power Saving Function support enabled

➔ **PCIEX16 Power [1 x16 PCIE]**

Use the **PCIEX16 Power** BIOS option to configure the PCIe x16 channel mode on the motherboard.

➔ **1 x16 PCIE** **DEFAULT** Configure the PCIe x16 slot as one PCIe x16

➔ **USB SW1 Power [+5V DUAL]**

Use the **USB SW1 Power** BIOS option to configure the USB power source for the corresponding USB connector (**Table 5-2**).

➔ **+5V** Set the USB power source to +5V

➔ **+5V DUAL** **DEFAULT** Set the USB power source to +5V dual

➔ **USB SW2 Power [+5V DUAL]**

Use the **USB SW2 Power** BIOS option to configure the USB power source for the corresponding USB connector (**Table 5-2**).

➔ **+5V** Set the USB power source to +5V

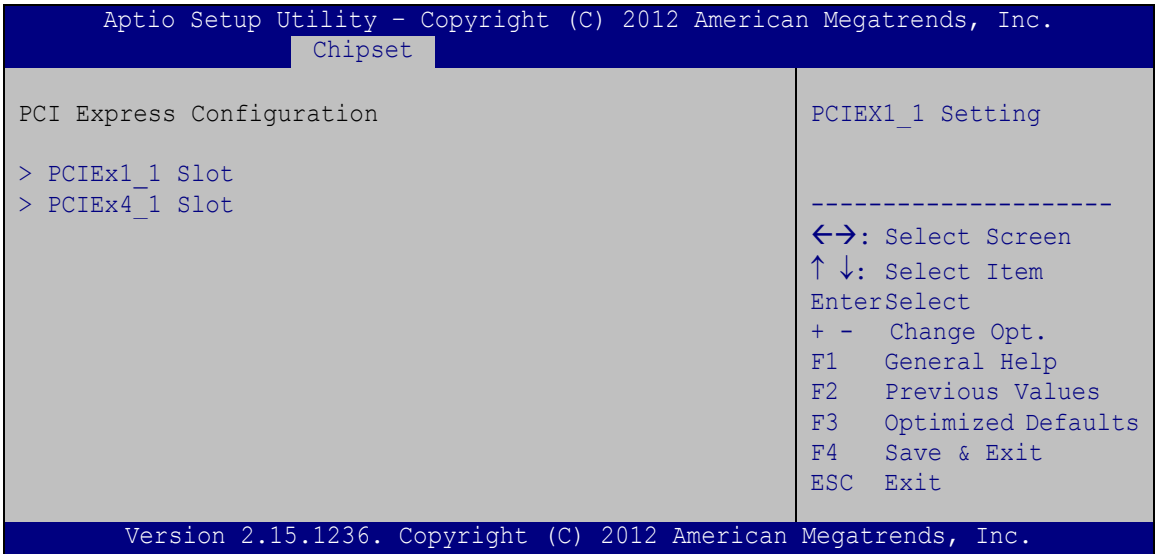
➔ **+5V DUAL** **DEFAULT** Set the USB power source to +5V dual

BIOS Options	Configured USB Ports
USB SW1 Power	K/M_USB1 (external USB 2.0 ports) LAN1_USB1 (external USB 3.1 Gen 1 ports)
USB SW2 Power	LAN2_USB2 (external USB 2.0 ports) USB1 (internal USB 2.0 ports) USB2 (internal USB 2.0 ports) CN4 (internal USB 3.1 Gen 1 ports)

Table 5-2: BIOS Options and Configured USB Ports

5.4.1.1 PCI Express Configuration

Use the **PCI Express Configuration** submenu (**BIOS Menu 20**) to configure the PCI Express slots.



BIOS Menu 20: PCI Express Configuration

➔ **PCIe Speed [Gen1]**

Use the **PCIe Speed** option to specify the PCI Express port speed. Configuration options are listed below.

- Auto
- Gen 1 **DEFAULT**
- Gen 2

➔ **Detect Non-Compliance Device [Enabled]**

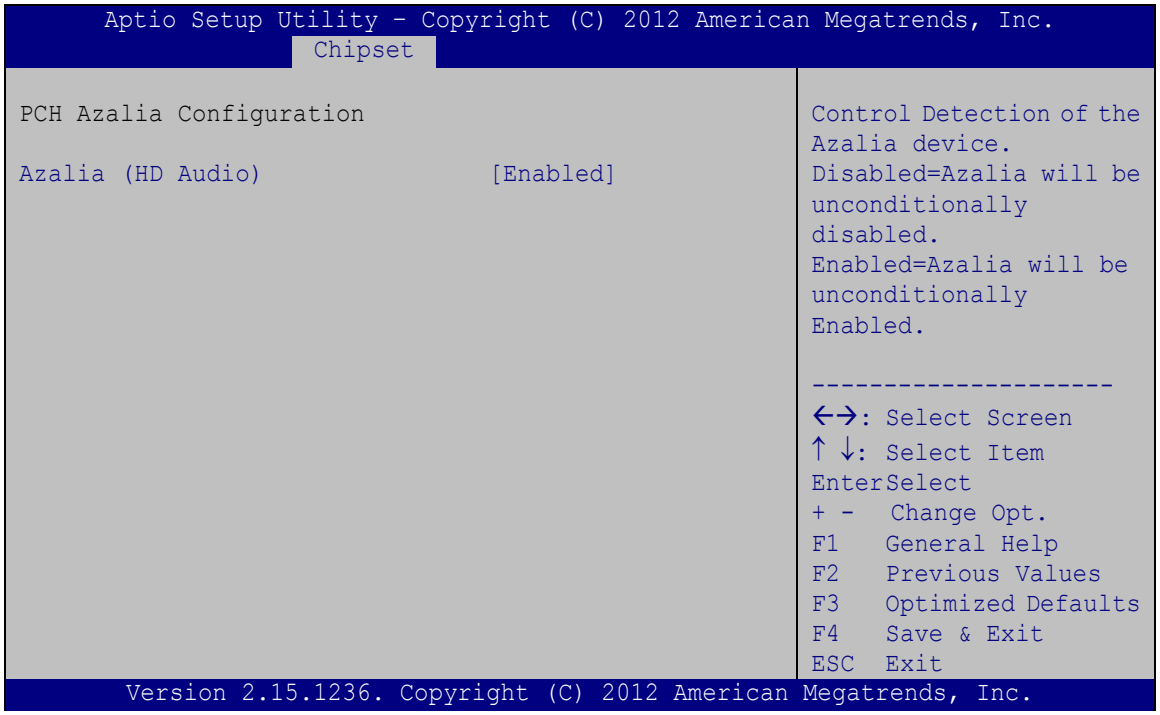
Use the **Detect Non-Compliance Device** option to configure whether to detect if a non-compliance PCI Express device is connected to the PCI Express port.

➔ **Disabled** Do not detect if a non-compliance PCI Express device is connected to the PCI Express port.

➔ **Enabled** **DEFAULT** Detect if a non-compliance PCI Express device is connected to the PCI Express port.

5.4.1.2 PCH Azalia Configuration

Use the **PCH Azalia Configuration** submenu (**BIOS Menu 21**) to configure the PCH Azalia codec.



BIOS Menu 21: PCH Azalia Configuration

➔ **Azalia [Enabled]**

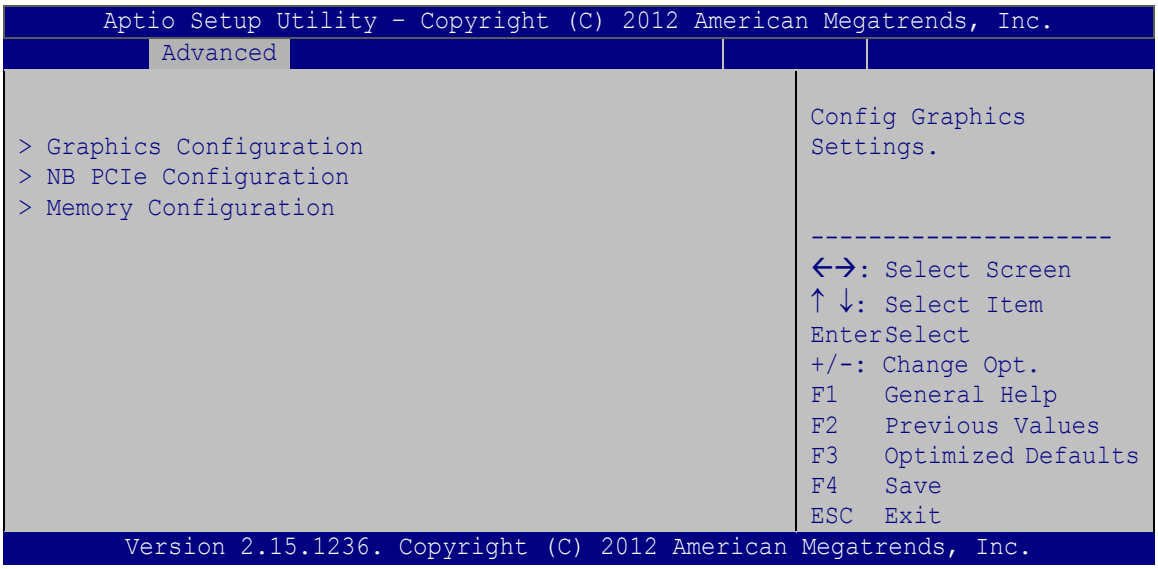
Use the **Azalia** option to enable or disable the High Definition Audio controller.

➔ **Disabled** The onboard High Definition Audio controller is disabled

➔ **Enabled** **DEFAULT** The onboard High Definition Audio controller is detected automatically and enabled

5.4.2 System Agent (SA) Configuration

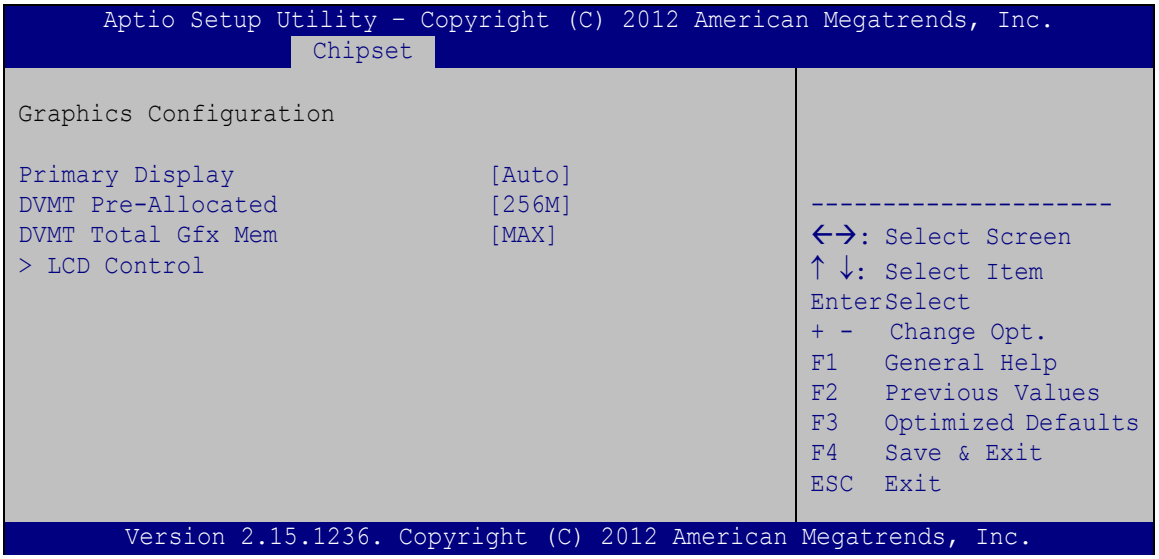
Use the **System Agent (SA) Configuration** menu (**BIOS Menu 22**) to configure the video device connected to the system.



BIOS Menu 22: System Agent (SA) Configuration

5.4.2.1 Graphics Configuration

Use the **Graphics Configuration** submenu (**BIOS Menu 23**) to configure the graphics settings.



BIOS Menu 23: Graphics Configuration

➔ **Primary Display [Auto]**

Use the **Primary Display** option to select the display device used by the system when it boots. Configuration options are listed below.

- Auto **DEFAULT**
- IGFX
- PEG
- PCIE/PCI

➔ **DVMT Pre-Allocated [256M]**

Use the **DVMT Pre-Allocated** option to specify the amount of system memory that can be used by the internal graphics device.

➔ **32M**		32 MB of memory used by internal graphics device
➔ **64M**		64 MB of memory used by internal graphics device
➔ **128M**		128 MB of memory used by internal graphics device
➔ **256M**	**DEFAULT**	256 MB of memory used by internal graphics device
➔ **512M**		512 MB of memory used by internal graphics device

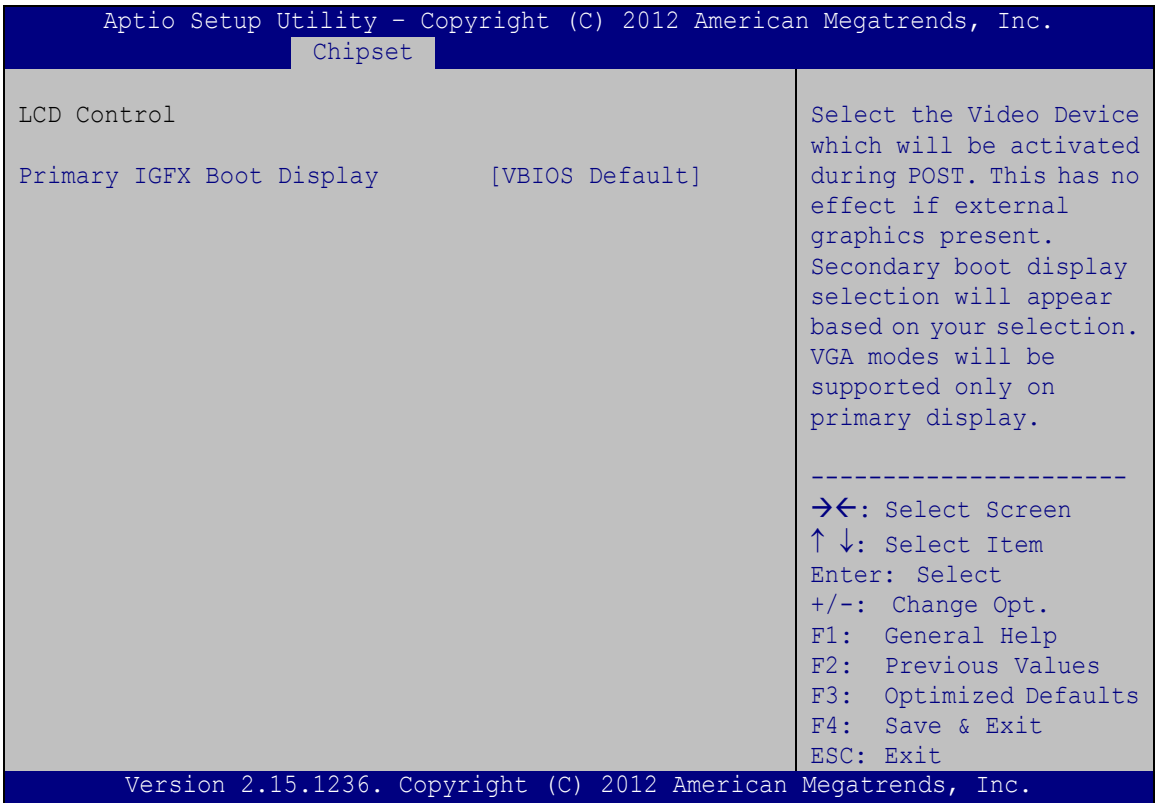
➔ **DVMT Total Gfx Mem [MAX]**

Use the **DVMT Total Gfx Mem** option to specify the maximum amount of memory that can be allocated as graphics memory. Configuration options are listed below.

- 128M
- 256M
- MAX **Default**

5.4.2.1.1 LCD Control

Use the **LCD Control** submenu (**BIOS Menu 24**) to select a display device which will be activated during POST.



BIOS Menu 24: LCD Control

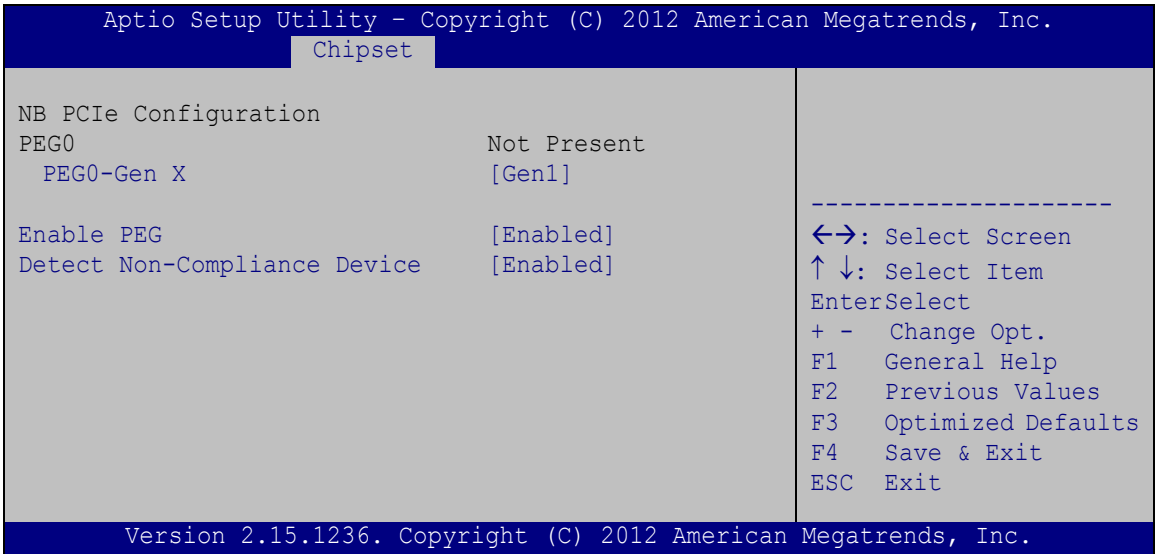
➔ **Primary IGFX Boot Display [VBIOS Default]**

Use the **Primary IGFX Boot Display** option to select the display device used by the system when it boots. Configuration options are listed below.

- VBIOS Default **DEFAULT**
- CRT
- DVI
- DP
- HDMI

5.4.2.2 NB PCIe Configuration

Use the **NB PCIe Configuration** submenu (**BIOS Menu 25**) to configure the northbridge PCIe settings.



BIOS Menu 25: NB PCIe Configuration

➔ **PEG0-Gen X [Gen1]**

Use the **PEG0-Gen X** option to configure PEG0 B0:D1:F0. Configuration options are listed below.

- Auto
- Gen1 **Default**
- Gen2
- Gen3

➔ **Enable PEG [Enabled]**

Use the **Enable PEG** option to enable or disable PEG.

➔ **Disabled**		Disables PEG.
➔ **Enabled**	**DEFAULT**	Enables PEG.
➔ **Auto**		Automatically detect PEG

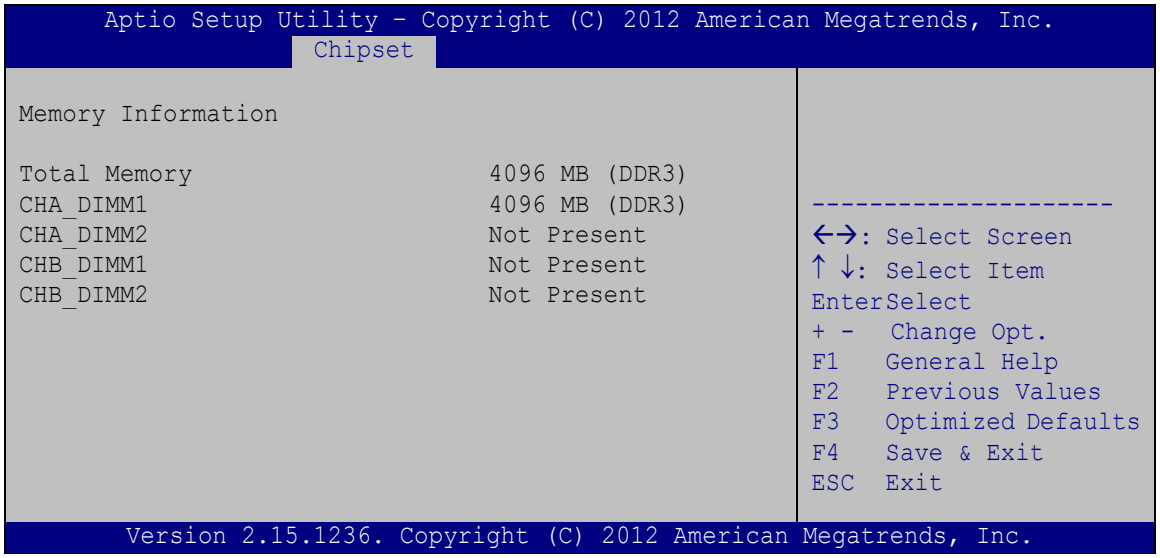
➔ **Detect Non-Compliance [Enabled]**

Use the **Detect Non-Compliance** option to detect non-compliance PCIe device in PEG.

➔ **Disabled**		Do not detect non-compliance PCIe device in PEG
➔ **Enabled**	**DEFAULT**	Detect non-compliance PCIe device in PEG

5.4.2.3 Memory Configuration

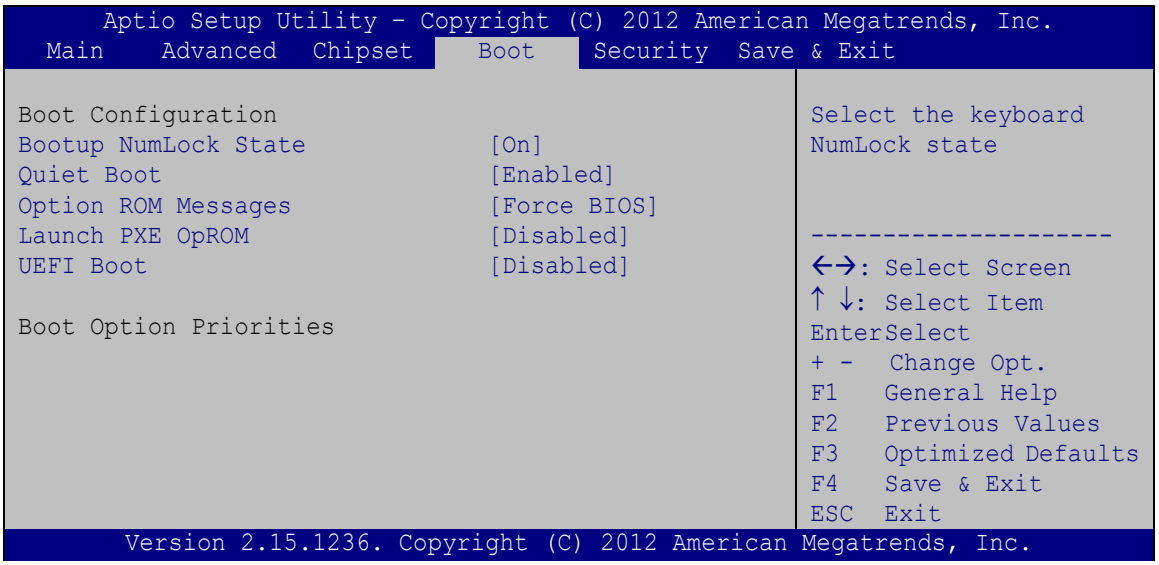
Use the **Memory Configuration** submenu (**BIOS Menu 26**) to configure the Memory settings.



BIOS Menu 26: Memory Configuration

5.5 Boot

Use the **Boot** menu (**BIOS Menu 27**) to configure system boot options.



BIOS Menu 27: Boot

➔ **Bootup NumLock State [On]**

Use the **Bootup NumLock State** BIOS option to specify if the number lock setting must be modified during boot up.

➔ **On** **DEFAULT** Allows the Number Lock on the keyboard to be enabled automatically when the computer system boots up. This allows the immediate use of the 10-key numeric keypad located on the right side of the keyboard. To confirm this, the Number Lock LED light on the keyboard is lit.

➔ **Off** Does not enable the keyboard Number Lock automatically. To use the 10-keys on the keyboard, press the Number Lock key located on the upper left-hand corner of the 10-key pad. The Number Lock LED on the keyboard lights up when the Number Lock is engaged.

➔ **Quiet Boot [Enabled]**

Use the **Quiet Boot** BIOS option to select the screen display when the system boots.

➔	**Disabled**		Normal POST messages displayed
➔	**Enabled**	**DEFAULT**	OEM Logo displayed instead of POST messages

➔ **Option ROM Messages [Force BIOS]**

Use the **Option ROM Messages** option to set the Option ROM display mode.

➔	**Force BIOS**	**DEFAULT**	Sets display mode to force BIOS.
➔	**Keep Current**		Sets display mode to current.

➔ **Launch PXE OpROM [Disabled]**

Use the **Launch PXE OpROM** option to enable or disable boot option for legacy network devices.

➔	**Disabled**	**DEFAULT**	Ignore all PXE Option ROMs
➔	**Enabled**		Load PXE Option ROMs

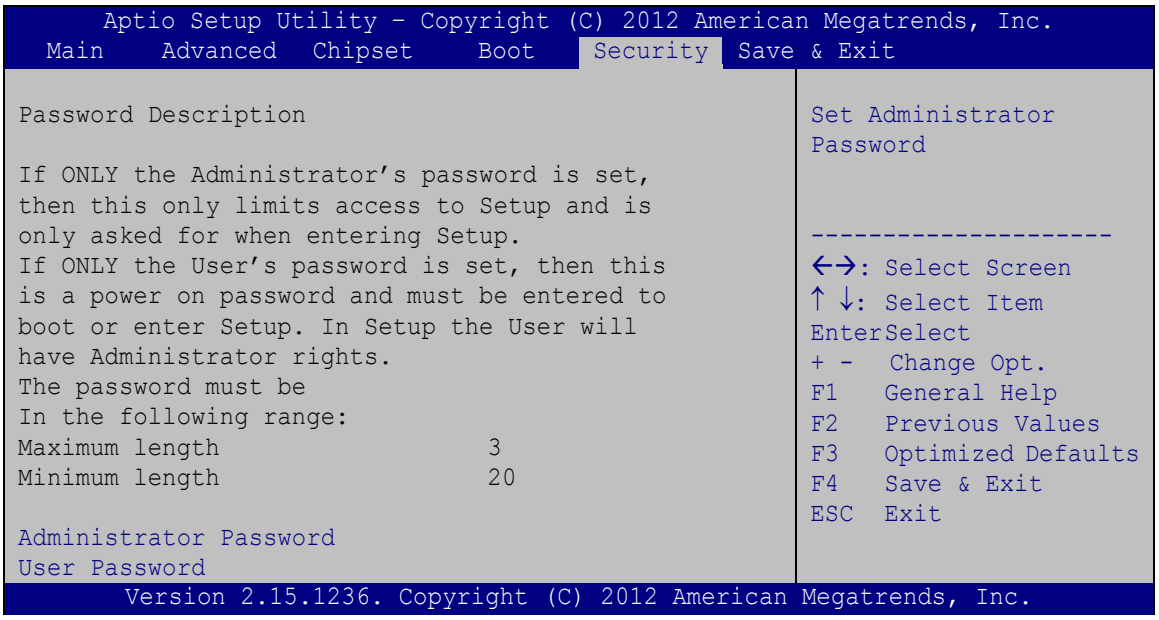
➔ **UEFI Boot [Disabled]**

Use the **UEFI Boot** option to enable or disable to boot from a UEFI device.

➔	**Disabled**	**DEFAULT**	Disable to boot from a UEFI device.
➔	**Enabled**		Enable to boot from a UEFI device.

5.6 Security

Use the **Security** menu (**BIOS Menu 28**) to set system and user passwords.



BIOS Menu 28: Security

➔ **Administrator Password**

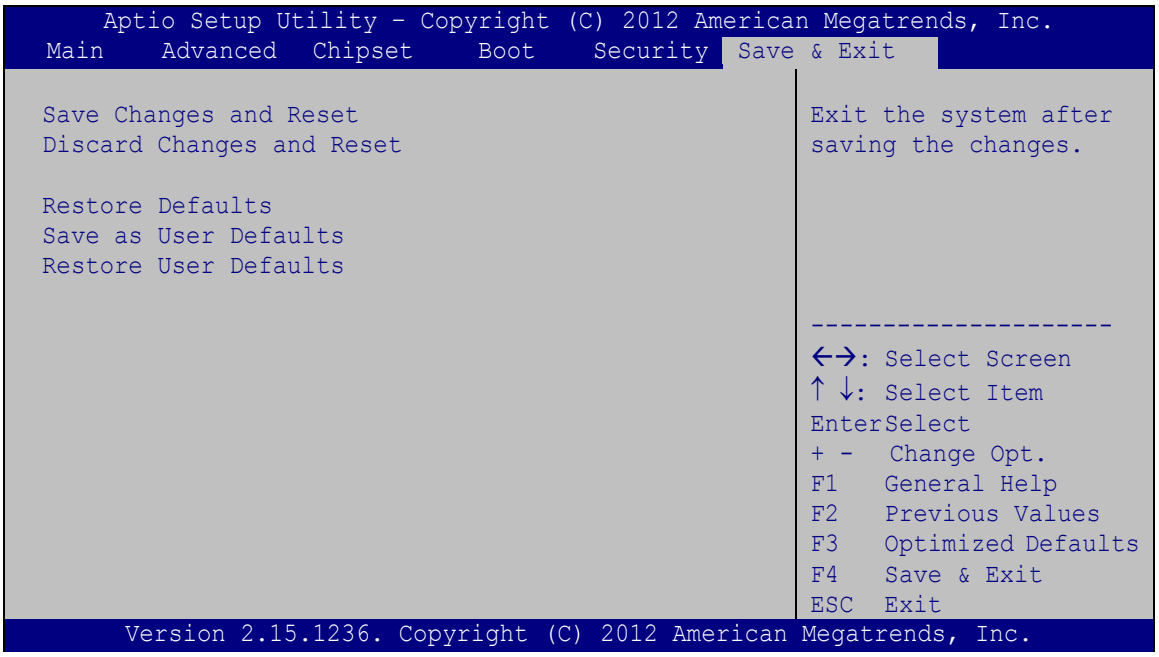
Use the **Administrator Password** to set or change a administrator password.

➔ **User Password**

Use the **User Password** to set or change a user password.

5.7 Exit

Use the **Exit** menu (**BIOS Menu 29**) to load default BIOS values, optimal failsafe values and to save configuration changes.



BIOS Menu 29:Exit

➔ **Save Changes and Reset**

Use the **Save Changes and Reset** option to save the changes made to the BIOS options and reset the system.

➔ **Discard Changes and Reset**

Use the **Discard Changes and Reset** option to exit the system without saving the changes made to the BIOS configuration setup program.

➔ **Restore Defaults**

Use the **Restore Defaults** option to load the optimal default values for each of the parameters on the Setup menus. **F3 key can be used for this operation.**

➔ **Save as User Defaults**

Use the **Save as User Defaults** option to save the changes done so far as user defaults.

➔ **Restore User Defaults**

Use the **Restore User Defaults** option to restore the user defaults to all the setup options.

Chapter

6

Software Drivers

6.1 Available Drivers

All the drivers for the IMBA-Q870-i2 are available on IEI Resource Download Center (https://download.ieiworld.com). Type IMBA-Q870-i2 and press Enter to find all the relevant software, utilities, and documentation.

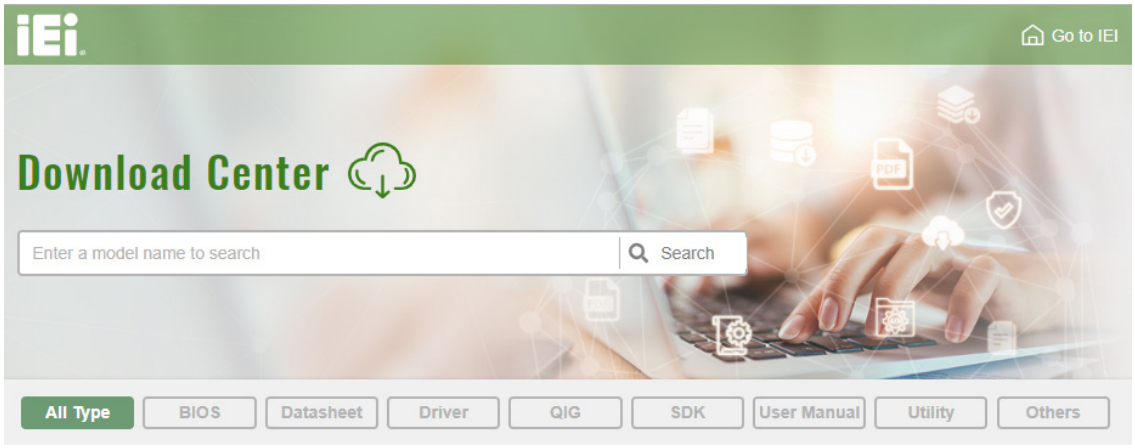
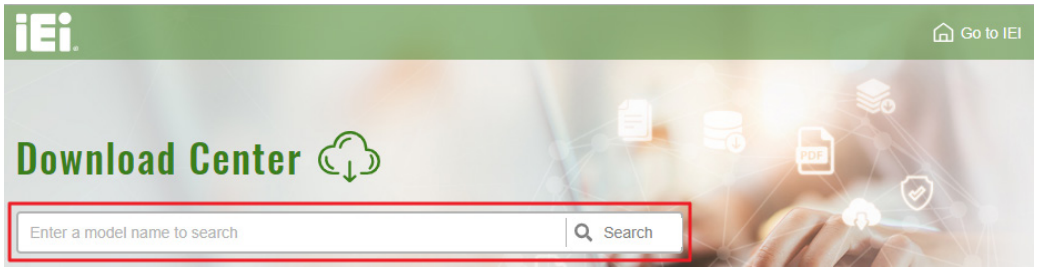


Figure 6-1: IEI Resource Download Center

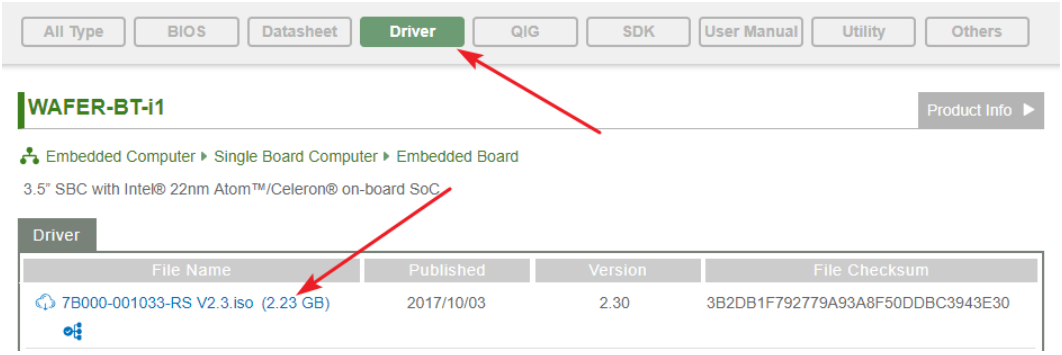
6.2 Driver Download

To download drivers from IEI Resource Download Center, follow the steps below.

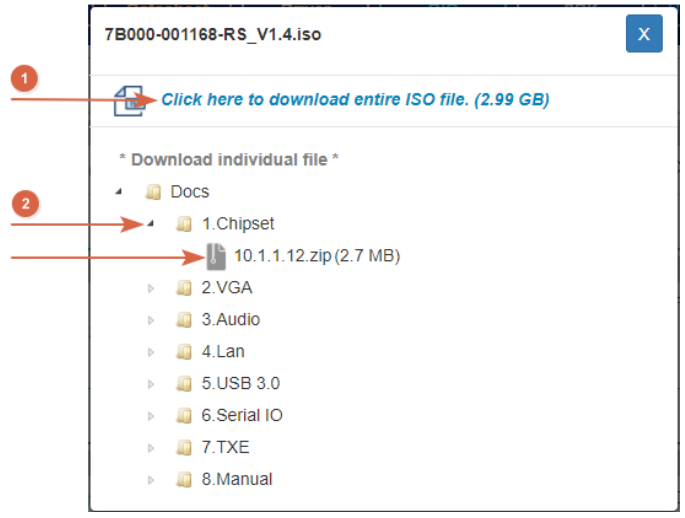
Step 1: Go to https://download.ieiworld.com. Type IMBA-Q870-i2 and press Enter.



Step 2: All product-related software, utilities, and documentation will be listed. You can choose **Driver** to filter the result.



Step 3: Click the driver file name on the page and you will be prompted with the following window. You can download the entire ISO file (❶), or click the small arrow to find an individual driver and click the file name to download (❷).



NOTE:

To install software from the downloaded ISO image file in Windows 8, 8.1 or 10, double-click the ISO file to mount it as a virtual drive to view its content. On Windows 7 system, an additional tool (such as Virtual CD-ROM Control Panel from Microsoft) is needed to mount the file.

Appendix

A

Regulatory Compliance

DECLARATION OF CONFORMITY

This equipment has been tested and found to comply with specifications for CE marking. If the user modifies and/or installs other devices in the equipment, the CE conformity declaration may no longer apply.

FCC WARNING

This equipment complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- This device may not cause harmful interference, and
- This device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

Appendix

B

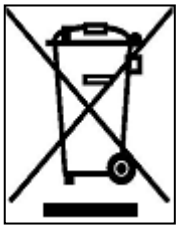
Product Disposal

CAUTION:

Risk of explosion if battery is replaced by an incorrect type. Only certified engineers should replace the on-board battery.

Dispose of used batteries according to instructions and local regulations.

- Outside the European Union–If you wish to dispose of used electrical and electronic products outside the European Union, please contact your local authority so as to comply with the correct disposal method.
- Within the European Union–The device that produces less waste and is easier to recycle is classified as electronic device in terms of the European Directive 2012/19/EU (WEEE), and must not be disposed of as domestic garbage.



EU-wide legislation, as implemented in each Member State, requires that waste electrical and electronic products carrying the mark (left) must be disposed of separately from normal household waste. This includes monitors and electrical accessories, such as signal cables or power cords. When you need to dispose of your device, please follow the guidance of your local authority, or ask the shop where you purchased the product. The mark on electrical and electronic products only applies to the current European Union Member States.

Please follow the national guidelines for electrical and electronic product disposal.

Appendix

C

BIOS Options

Below is a list of BIOS configuration options in the BIOS chapter.

- System Date [xx/xx/xx] 82
- System Time [xx:xx:xx] 82
- ACPI Sleep State [S1 (CPU Stop Clock)] 83
- Wake System with Fixed Time [Disabled] 84
- Security Device Support [Disable] 86
- Hyper-threading [Enabled] 87
- Active Processor Cores [All] 87
- Intel Virtualization Technology [Disabled] 88
- EIST [Enabled] 88
- SATA Controller(s) [Enabled] 89
- SATA Mode Selection [IDE] 89
- Intel(R) Rapid Start Technology [Disabled] 90
- Intel AMT [Enabled] 91
- Unconfigure ME [Disabled] 91
- USB Devices 92
- Legacy USB Support [Enabled] 92
- Serial Port [Enabled] 94
- Change Settings [Auto] 94
- Serial Port [Enabled] 95
- Change Settings [Auto] 95
- Device Mode [RS422/485] 96
- Serial Port [Enabled] 96
- Change Settings [Auto] 96
- Serial Port [Enabled] 96
- Change Settings [Auto] 97
- Device Mode [RS422/485] 97
- Serial Port [Enabled] 97
- Change Settings [Auto] 98
- Serial Port [Enabled] 98
- Change Settings [Auto] 99
- Parallel Port [Enabled] 100
- Change Settings [Auto] 100
- Device Mode [STD Printer Mode] 101

- PC Health Status 102
- CPU_FAN1 Smart Fan Control/SYS_FAN1 Smart Fan Control [Auto Mode] 103
- Fan start/off temperature 103
- Fan start PWM 104
- Fan slope PWM 104
- Console Redirection [Disabled] 105
- Terminal Type [ANSI] 106
- Bits per second [115200] 106
- Data Bits [8] 106
- Parity [None] 106
- Stop Bits [1] 107
- Auto Recovery Function [Disabled] 108
- Restore on AC Power Loss [Last State] 109
- Power Saving Function [Disabled] 109
- PCIEX16 Power [1 x16 PCIE] 110
- USB SW1 Power [+5V DUAL] 110
- USB SW2 Power [+5V DUAL] 110
- PCIe Speed [Gen1] 111
- Detect Non-Compliance Device [Enabled] 111
- Azalia [Enabled] 112
- Primary Display [Auto] 114
- DVMT Pre-Allocated [256M] 114
- DVMT Total Gfx Mem [MAX] 114
- Primary IGFX Boot Display [VBIOS Default] 115
- PEG0-Gen X [Gen1] 116
- Enable PEG [Enabled] 116
- Detect Non-Compliance [Enabled] 117
- Bootup NumLock State [On] 118
- Quiet Boot [Enabled] 119
- Option ROM Messages [Force BIOS] 119
- Launch PXE OpROM [Disabled] 119
- UEFI Boot [Disabled] 119
- Administrator Password 120
- User Password 120
- Save Changes and Reset 121

- Discard Changes and Reset 121
- Restore Defaults 121
- Save as User Defaults 121
- Restore User Defaults 121

Appendix

D

Digital I/O Interface

The DIO connector on the IMBA-Q870-i2 is interfaced to GPIO ports on the Super I/O chipset. The DIO has both 8-bit digital inputs and 8-bit digital outputs. The digital inputs and digital outputs are generally control signals that control the on/off circuit of external devices or TTL devices. Data can be read or written to the selected address to enable the DIO functions.

NOTE:

For further information, please refer to the datasheet for the Super I/O chipset.

The BIOS interrupt call **INT 15H** controls the digital I/O.

INT 15H:

AH – 6FH	
Sub-function:	
AL – 8	:Set the digital port as INPUT
AL	:Digital I/O input value

Assembly Language Sample 1

```
MOV     AX, 6F08H       ;setting the digital port as input
INT     15H             ;
```

AL low byte = value

AH – 6FH	
Sub-function:	
AL – 9	:Set the digital port as OUTPUT
BL	:Digital I/O output value

Assembly Language Sample 2

```
MOV     AX, 6F09H       ;setting the digital port as output
MOV     BL, 09H                 ;digital value is 09H
INT     15H             ;
```

Digital Output is 1001b

Appendix

E

Watchdog Timer

NOTE:

The following discussion applies to DOS environment. Contact IEI support or visit the IEI website for specific drivers for other operating systems.

The Watchdog Timer is provided to ensure that standalone systems can always recover from catastrophic conditions that cause the CPU to crash. This condition may have occurred by external EMIs or a software bug. When the CPU stops working correctly, Watchdog Timer either performs a hardware reset (cold boot) or a Non-Maskable Interrupt (NMI) to bring the system back to a known state.

A BIOS function call (INT 15H) is used to control the Watchdog Timer.

INT 15H:

AH – 6FH Sub-function:	
AL – 2:	Sets the Watchdog Timer's period.
BL:	Time-out value (Its unit-second is dependent on the item "Watchdog Timer unit select" in CMOS setup).

Table E-1: AH-6FH Sub-function

Call sub-function 2 to set the time-out period of Watchdog Timer first. If the time-out value is not zero, the Watchdog Timer starts counting down. When the timer value reaches zero, the system resets. To ensure that this reset condition does not occur, calling sub-function 2 must periodically refresh the Watchdog Timer. However, the watchdog timer is disabled if the time-out value is set to zero.

A tolerance of at least 10% must be maintained to avoid unknown routines within the operating system (DOS), such as disk I/O that can be very time-consuming.

NOTE:

When exiting a program it is necessary to disable the Watchdog Timer, otherwise the system resets.

EXAMPLE PROGRAM:

```
; INITIAL TIMER PERIOD COUNTER

;
W_LOOP:
;

        MOV     AX, 6F02H       ;setting the time-out value
        MOV     BL, 30          ;time-out value is 48 seconds
        INT     15H

;
; ADD THE APPLICATION PROGRAM HERE
;

        CMP     EXIT_AP, 1      ;is the application over?
        JNE     W_LOOP          ;No, restart the application

        MOV     AX, 6F02H       ;disable Watchdog Timer
        MOV     BL, 0           ;
        INT     15H

;
; EXIT ;
```

Appendix

F

Hazardous Materials Disclosure

The details provided in this appendix are to ensure that the product is compliant with the Peoples Republic of China (China) RoHS standards. The table below acknowledges the presences of small quantities of certain materials in the product, and is applicable to China RoHS only.

A label will be placed on each product to indicate the estimated "Environmentally Friendly Use Period" (EFUP). This is an estimate of the number of years that these substances would "not leak out or undergo abrupt change." This product may contain replaceable sub-assemblies/components which have a shorter EFUP such as batteries and lamps. These components will be separately marked.

Please refer to the following table.

Part Name	Toxic or Hazardous Substances and Elements					
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (CR(VI))	Polybrominated Biphenyls (PBB)	Polybrominated Diphenyl Ethers (PBDE)
Housing	O	O	O	O	O	O
Display	O	O	O	O	O	O
Printed Circuit Board	O	O	O	O	O	O
Metal Fasteners	O	O	O	O	O	O
Cable Assembly	O	O	O	O	O	O
Fan Assembly	O	O	O	O	O	O
Power Supply Assemblies	O	O	O	O	O	O
Battery	O	O	O	O	O	O

O: This toxic or hazardous substance is contained in all of the homogeneous materials for the part is below the limit requirement in SJ/T11363-2006 (now replaced by GB/T 26572-2011).

X: This toxic or hazardous substance is contained in at least one of the homogeneous materials for this part is above the limit requirement in SJ/T11363-2006 (now replaced by GB/T 26572-2011).

此附件旨在确保本产品符合中国 RoHS 标准。以下表格标示此产品中某有毒物质的含量符合中国 RoHS 标准规定的限量要求。

本产品上会附有"环境友好使用期限"的标签，此期限是估算这些物质"不会有泄漏或突变"的年限。本产品可能包含有较短的环境友好使用期限的可替换元件，像是电池或灯管，这些元件将会单独标示出来。

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (CR(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
壳体	O	O	O	O	O	O
显示	O	O	O	O	O	O
印刷电路板	O	O	O	O	O	O
金属螺帽	O	O	O	O	O	O
电缆组装	O	O	O	O	O	O
风扇组装	O	O	O	O	O	O
电力供应组装	O	O	O	O	O	O
电池	O	O	O	O	O	O

O: 表示该有毒有害物质在该部件所有物质材料中的含量均在 SJ/T 11363-2006 (现由 GB/T 26572-2011 取代) 标准规定的限量要求以下。

X: 表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T 11363-2006 (现由 GB/T 26572-2011 取代) 标准规定的限量要求。